

# Multilayer Ceramic Chip Capacitors

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**CKCL22C0G1H680K085AA**



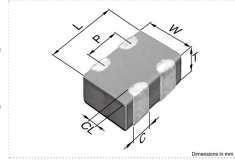
**TDK item description** ? CKCL22C0G1H680KT\*\*\*\*

**Applications** Commercial Grade

**Feature** 2-Array 2 in 1 Array

**Series** CKCL22(2012) [EIA 0805]

**Status** Production (Not Recommended for New Design)



Images are for reference only and show exemplary products.

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### Documents

- Catalog
- Specification
- RoHS Certificate
- SVHC/REACH Certificate
- Characterization Sheet
- Promotion Video
- Sample Kits

### Technical Support Tools

- SPICE Netlist (Simple)
- SPICE Netlist (Precision)
- Equivalent Circuit Model

### Size

Length(L)	2.00mm ±0.15mm
Width(W)	1.25mm ±0.15mm
Thickness(T)	0.85mm ±0.15mm
Terminal Width(C)	0.45mm Nom.
Terminal Spacing(P)	1.00mm Nom.
Recommended Land Pattern (PA)	0.55mm to 0.65mm
Recommended Land Pattern (PB)	0.55mm to 0.65mm
Recommended Land Pattern (PC)	0.40mm to 0.50mm
Recommended Land Pattern (PP)	0.95mm to 1.05mm

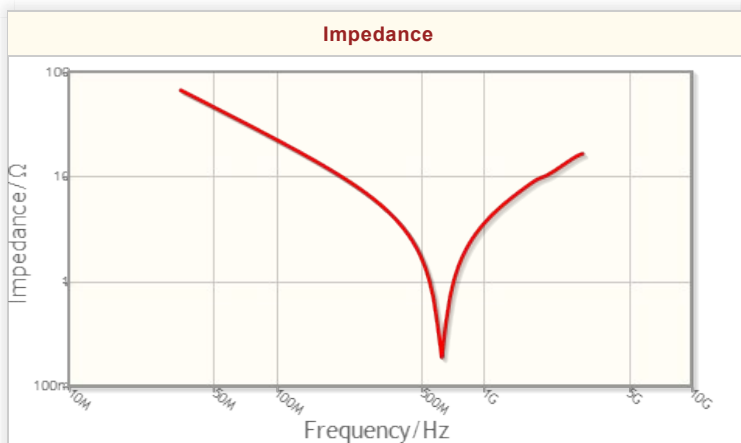
### Electrical Characteristics

Capacitance	68pF ±10%
Rated Voltage	50VDC
Temperature Characteristic ?	C0G(0±30ppm/°C)
Q (Min.)	1000
Insulation Resistance (Min.)	10000MΩ

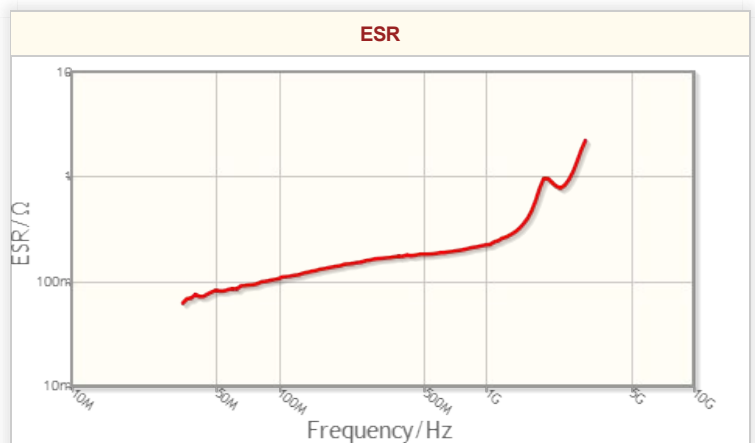
### Other

Soldering Method	Reflow
AEC-Q200	No
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	4000pcs

## Characteristic Graph (This is reference data, and does not guarantee the products characteristics.)



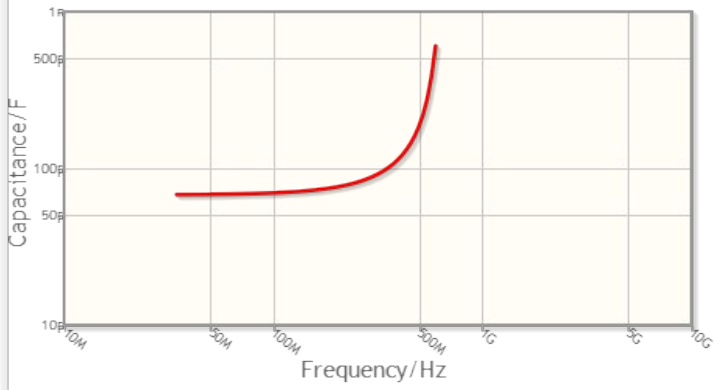
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### Capacitance

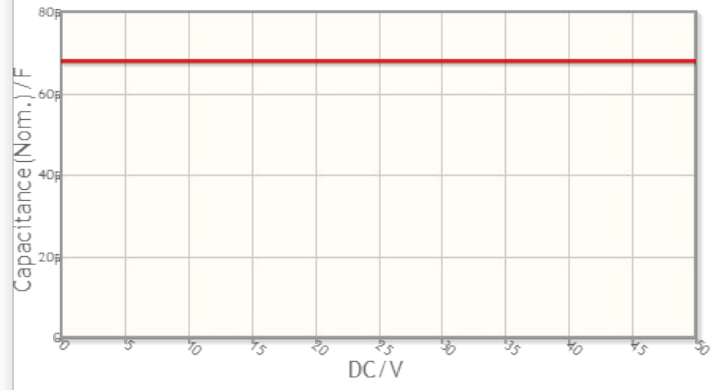


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Change settings

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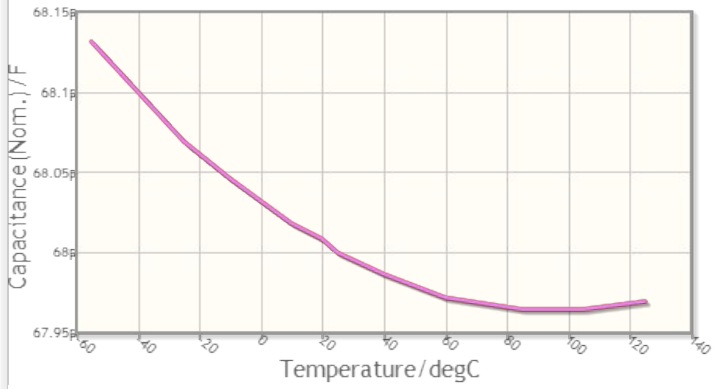
### DC Bias Characteristic



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### Temperature Characteristic



CKCL22C0G1H680K085AA( No Bias) CKCL22C0G1H680K085AA(DC Bias = 25V )

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### Ripple Temperature Rising

no data available

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