

-100mA / -50V Digital transistors (with built-in resistors)

DTA123EM / DTA123EE / DTA123EUA / DTA123EKA / DTA123ESA

●Applications

Inverter, Interface, Driver

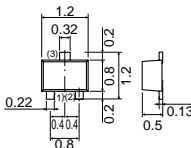
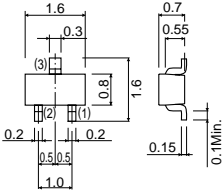
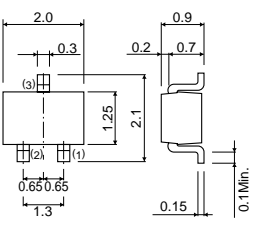
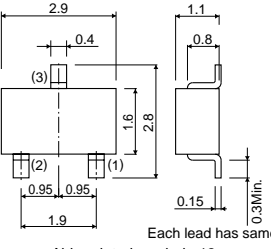
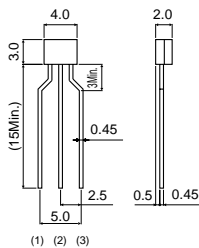
●Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making the device design easy.

●Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

●External dimensions (Unit : mm)

<p>DTA123EM</p>  <p>ROHM : VMT3 Abbreviated symbol : 12</p> <p>(1) IN (2) GND (3) OUT</p>	<p>DTA123EE</p>  <p>ROHM : EMT3 Abbreviated symbol : 12</p> <p>(1) GND (2) IN (3) OUT</p>
<p>DTA123EUA</p>  <p>ROHM : UMT3 EIAJ : SC-70 Each lead has same dimensions Abbreviated symbol : 12</p> <p>(1) GND (2) IN (3) OUT</p>	<p>DTA123EKA</p>  <p>ROHM : SMT3 EIAJ : SC-59 Each lead has same dimensions Abbreviated symbol : 12</p> <p>(1) GND (2) IN (3) OUT</p>
<p>DTA123ESA</p>  <p>ROHM : SPT EIAJ : SC-72 Abbreviated symbol : A123ES</p> <p>(1) GND (2) OUT (3) IN</p>	

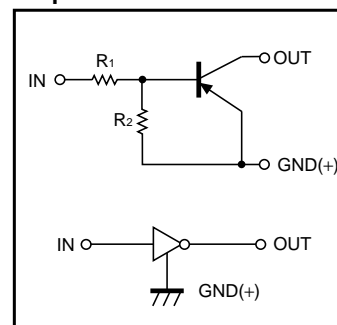
DTA123EM / DTA123EE / DTA123EUA DTA123EKA / DTA123ESA

Transistors

●Packaging specifications

Part No.	Package	VMT3	EMT3	UMT3	SMT3	SPT
	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T146	TP
	Basic ordering unit (pieces)	8000	3000	3000	3000	5000
DTA123EM	○	-	-	-	-	-
DTA123EE	-	○	-	-	-	-
DTA123EUA	-	-	○	-	-	-
DTA123EKA	-	-	-	○	-	-
DTA123ESA	-	-	-	-	-	○

●Equivalent circuit



$R_1=R_2=2.2k\Omega$

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits					Unit
		DTA123EM	DTA123EE	DTA123EUA	DTA123EKA	DTA123ESA	
Supply voltage	V _{CC}	-50					V
Input voltage	V _{IN}	-12 to +10					V
Output current	I _o	-100					mA
	I _{C(Max.)}	-100					
Power dissipation	P _D	150		200		300	mW
Junction temperature	T _j	150					°C
Storage temperature	T _{stg}	-55 to +150					°C

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V _{I(off)}	-	-	-0.5	V	V _{CC} =-5V, I _o =-100μA
	V _{I(on)}	-3	-	-		V _o =-0.3V, I _o =-20mA
Output voltage	V _{O(on)}	-	-0.1	-0.3	V	I _o /I _i =-10mA/-0.5mA
Input current	I _i	-	-	-3.8	mA	V _i =-5V
Output current	I _{O(off)}	-	-	-0.5	μA	V _{CC} =-50V, V _i =0V
DC current gain	G _i	20	-	-	-	V _o =-5V, I _o =-20mA
Input resistance	R ₁	1.54	2.2	2.86	kΩ	-
Resistance ratio	R ₂ /R ₁	0.8	1	1.2	-	-
Transition frequency	f _T *	-	250	-	MHz	V _{CE} =-10V, I _E =5mA, f=100MHz

* Characteristics of built-in transistor

Transistors

●Electrical characteristic curves

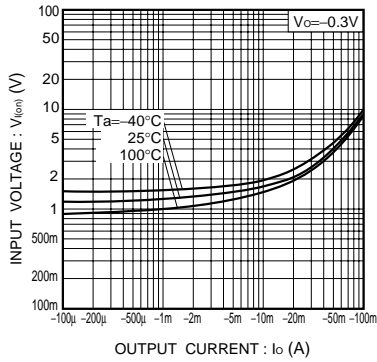


Fig.1 Input voltage vs. output current (ON characteristics)

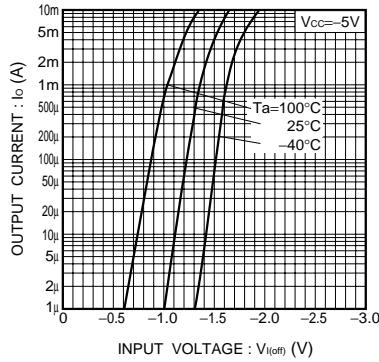


Fig.2 Output current vs. input voltage (OFF characteristics)

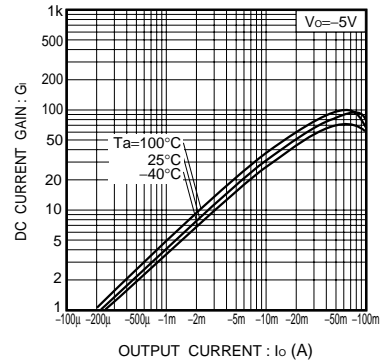


Fig.3 DC current gain vs. output current

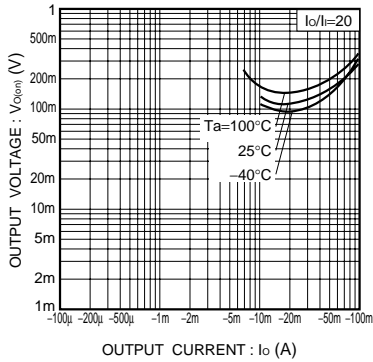


Fig.4 Output voltage vs. output current

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