

# swissbit®

Product Fact Sheet

## Industrial M.2 2242 SATA SSD

**X-86m2 Series**  
SATA Gen3 – 6.0 Gbit/s, 3D pSLC

Commercial and Industrial  
Temperature Grade

Date: February 12, 2021  
Revision: 1.02



## Product Summary

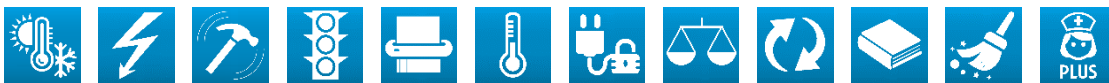
- **Capacities:** 10 GBytes, 20 GBytes, 40 GBytes, 80 GBytes, 160 GBytes (3D pSLC)
- **Form Factor:** m.2 2242 type (42 mm x 22 mm x 3.58 mm) acc. PCI-SIG
- **Interface<sup>1</sup>:** SATA Gen3 – 6 Gbit/s (Gen2 – 3 Gbit/s and Gen1 – 1.5 Gbit/s backward compatible)
- **Command Sets:** Supports ATA/ATAPI-8 and ACS-4
- **Performance:**
  - Burst Transfer Rate: Up to 600 MBytes/s in SATA Gen3 – 6.0 Gbit/s
  - Read Performance: Sequential Read up to 372 MBytes/s, Random Read 4K up to 13,100 IOPS
  - Write Performance: Sequential Write up to 223 MBytes/s, Random Write 4K up to 8,300 IOPS
- **Operating Temperature Range<sup>2</sup>:**
  - Commercial: 0 °C to 70 °C
  - Industrial: -40°C to 85 °C
- **Storage Temperature Range:** -40 °C to 85 °C
- **Operating Voltage:** 3.3 V ± 5%
- **Power (160 GBytes) typ:**
  - Read (Active): 1,105 mW
  - Write (Active): 875 mW
  - Idle: 195 mW
  - Slumber: 80 mW
  - DEVSLP: 10 mW
- **Data Retention:** 10 Years @ Life Begin / 1 Year @ Life End
- **Endurance in DiskWritesPerDay (DWPD):**
  - JEDEC Enterprise Workload: up to 2.7
  - JEDEC Client Workload: up to 13
- **Shock/Vibration:** 1,500 *g* / 50 *g*
- **High-Performance Dual Core 32-Bit Processor with Integrated, Parallel Flash Interface Engines:**
  - Triple-Level Cell (TLC) 3D NAND Flash in pSLC mode
  - Flexible BCH and GCC ECC engines provide superior error correction performance
- **High Reliability:**
  - Mean Time Between Failure (MTBF): > 2,000,000 hours @ 25°C
  - Data Reliability: < 1 non-recoverable error per 10<sup>16</sup> bits read
  - 30 µinch Gold-Plated Connector (IPC-6012B Class 2 Compliant)

<sup>1</sup> The verification of host system and storage device compatibility is in customer's responsibility. Swissbit can provide guidance and support on request.

<sup>2</sup> Adequate airflow is required to ensure the temperature, as reported in the S.M.A.R.T. data, does not exceed 120°C (industrial temperature drive) and 105°C (commercial temperature drive) respectively.

## Product Features

- Dynamic and Static Wear Leveling
- Subpage Mode Flash Translation Layer (FTL)
- Optimized FW algorithms especially for high read access and long data retention applications:
  - Proven power fail management for highest reliability
  - Near Miss ECC technology  
Minimize the risk of uncorrectable bit failure over the product lifetime. Each read command analyzes the ECC margin level and refreshes data if necessary.
  - Read Disturb Management  
Read commands are monitored and the data is refreshed when critical numbers are reached.
  - Wear Leveling technology  
Equal wear leveling of static and dynamic data. The wear leveling assures that dynamic data as well as static data is balanced evenly across the memory. This guarantees the maximum write endurance of the device.
  - Data Care Management  
An interruptible background process controls the user data for read disturb effects or high temperature related retention degradation and refreshes data if necessary.
- Lifetime Enhancements
  - Dynamic Bad Block Remapping
  - Write Amplification Reduction
- TRIM and NCQ Support
- ATA Security Feature Set Support
- DEVSLP Compatible
- In-Field Firmware Update<sup>3</sup>
- Enterprise-Grade Self-Monitoring, Analysis, and Reporting Technology (S.M.A.R.T.)
- End-to-End (E2E) Data Path Protection
- Advanced protection against radiation and soft-errors by SRAM ECC and low-alpha package
- AES-128/256 encryption support with CBC and XTS modes (on request)
- TCG Opal 2.0 and secure boot feature (on request)
- On-die temperature sensor
- Life Cycle Management
- Controlled "Locked" BOM
- RoHS 6 compliance
- Swissbit Life Time Monitoring (SBLTM) Tool and SDK



### Why Swissbit?

Swissbit is focused on the design, development, manufacture, and support of leading edge memory and storage solutions for the worldwide OEM/ODM marketplace. As a global supplier, Swissbit recognizes and addresses the higher level of application requirements of today's industrial, Netcom, and automotive customers by providing best-in-class products and services, with uncompromised attention to driving overall value and quality.

<sup>3</sup> The support of In-Field FW update capabilities on host systems is recommended.

# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

## Swissbit:

[SFSA010GM1AO1TO-I-5S-11P-STD](#) [SFSA020GM1AO1TO-I-6B-11P-STD](#) [SFSA040GM1AO2TO-I-6B-21P-STD](#)  
[SFSA080GM1AO1TO-I-8C-11P-STD](#) [SFSA160GM1AO2TO-I-8C-21P-STD](#) [SFSA040GM1AO2TO-C-6B-21P-STD](#)  
[SFSA080GM1AO1TO-C-8C-11P-STD](#) [SFSA160GM1AO2TO-C-8C-21P-STD](#) [SFSA010GM1AO1TO-C-5S-11P-STD](#)  
[SFSA020GM1AO1TO-C-6B-11P-STD](#)