# LT3597 60V Triple Step-Down 

 LED Driver
## DESCRIPTION

Demonstration circuit 1497A is a 60V triple step-down LED driver featuring the LT3597. The demo board is optimized for 9 white LEDs, 100 mA output per regulator from a 48 V input. When different number or different color of LEDs is used for evaluation, attentions should be given to the maximum output voltage setting and fault reporting. The connections between the board and LEDs strings should be as short as feasible.

The circuit achieves 10,000:1 PWM dimming at 100 Hz PWM frequency. LED dimming can also be done by analog control of the CTRL1-3 pin. If dimming is not required, leave the PWM1-3 and CTRL1-3 terminals unconnected.

The demo circuit pulls PWM1-3 and CTRL1-3 pins up to $V_{\text {REF }}$. The switching frequency is set to 1 MHz on the demo circuit for best solution size. The LT3597 internal compensation makes it a lot easier for a designer to design different application circuits.
The LT3597 data sheet gives a complete description of the part, operation and application information. The data sheet must be read in conjunction with this quick start guide for demo circuit 1497A.

Design files for this circuit board are available at http://www.linear.com/demo
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Figure 1. PWM Dimming

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PGRFORMANCE SUMMARY $\left(T_{A}=25^{\circ} \mathrm{C}\right)$

| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {IN }}{ }^{\text {* }}$ | Input Supply |  | 6 |  | 55 | V |
| $\mathrm{V}_{\text {LED }}$ | LED String Voltage |  | 28 |  | 34 | V |
| $\mathrm{V}_{\text {OUT(MAX) }}$ | Maximum Output Voltage |  |  | 38 |  | V |
| IOUT | Output Current | R23 = R24 = R25 = 20k | 98 | 100 | 102 | mA |
| DIM | PWM Dimming Ratio | PWM Dimming Frquency $=100 \mathrm{~Hz}$ |  | 0,000 |  |  |
| FSW | Switching Frequency | R22 $=33.2 \mathrm{k}$ | 0.9 | 1 | 1.1 | MHz |
| EFE | Efficiency | $\mathrm{V}_{\text {IN }}=48 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=29 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=100 \mathrm{~mA}$, BIAS $=5 \mathrm{~V}$, No Dimming |  | 87 |  | \% |

${ }^{*}$ Actual $\mathrm{V}_{\text {IN }}$ range should be determined by the load. For 9 white LEDs per channel, use a 48 V input for evaluation.

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## PUICK START PROCEDURE

Demonstration circuit 1497A is easy to set up to evaluate the performance of the LT3597. Refer to Figure 2 for proper measurement equipment setup and follow the procedure below:

1. With power off, connect the input power supply to $\mathrm{V}_{\text {IN }}$ and GND.
2. With power off, connect a 5 V bias supply to BIAS and GND. Bias voltage should be less then 25 V .
3. With power off, connect 3 LED strings, 9 white LEDs per string, between LED1+ and LED1-, LED2+ and LED2-, LED3+ and LED3- respectively.
4. Turn on the power at the input. Be careful not to look at the LEDs directly.
5. Carefully evaluate all design parameters as needed.
6. To evaluate PWM dimming, apply a PWM signal to PWM1, PWM2 and/or PWM3. To evaluate 10,000:1 PWM dimming, the PWM signal should have a frequency of 100 Hz and the minimum pulse width can be as low as $1 \mu \mathrm{~s}$.
7. To modify the demo board for other applications, please contact the Linear Applications Group for help.


Figure 2. Demo Board Setup

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## PARTS LIST

| ITEM | QTY | REFERENCE | PART DESCRIPTION | MANUFACTURER/PART NUMBER |
| :---: | :---: | :--- | :--- | :--- |
| Required Circuit Components | Capacitor, X7R, 4.7 $\mu$ F, 50V, 10\%,1206 | Murata, GRM31CR71H475KA12L |  |  |
| 1 | 3 | C1, C2, C3 | Capacitor, X7R, $0.22 \mu \mathrm{~F}, 25 \mathrm{~V}, 10 \%, 0603$ | Murata, GRM188R71E224KA88L |
| 2 | 3 | C4, C5, C6 | Capacitor, X7R, $2.2 \mu \mathrm{~F}, 100 \mathrm{~V}, 10 \%, 1210$ | Murata, GRM32ER72A225KA35L |
| 3 | 2 | C7, C10 | Capacitor, X5R, 4.7 FF, 25V, 10\%, 0805 | Taiyo Yuden, TMK212BJ475KG-T |
| 4 | 1 | C8 | Diode, Schottky, Power DI-123 | Diodes/Zetex, DFLS160-7 |
| 5 | 3 | D1, D2, D3 | Inductor, 100 $\mu \mathrm{H}$ | Sumida, CDR6D28MNNP-101NC |
| 6 | 3 | L1, L2, L3 | Resistor, Chip, $93.1 \mathrm{k}, 1 / 16 \mathrm{~W}, 1 \%, 0402$ | Vishay, CRCW040293K1FKED |
| 7 | 3 | R1, R2, R3 | Resistor, Chip, $3.01 \mathrm{k} \Omega, 1 / 16 \mathrm{~W}, 1 \%, 0402$ | Vishay, CRCW04023K01FKED |
| 8 | 3 | R4, R5, R6 | Resistor, Chip, 82.5k $, 1 / 16 \mathrm{~W}, 1 \%, 0402$ | Vishay, CRCW040282K5FKED |
| 9 | 1 | R16 | Resistor, Chip, $49.9 \mathrm{k} \Omega, 1 / 16 \mathrm{~W}, 1 \%, 0402$ | Vishay, CRCW040249K9FKED |
| 10 | 1 | R19 | Resistor, Chip,10k $\Omega, 1 / 16 \mathrm{~W}, 1 \%, 0402$ | Vishay, CRCW040210K0FKED |
| 11 | 1 | R20 | Resistor, Chip, 33.2k $\Omega, 1 / 16 \mathrm{~W}, 1 \%, 0402$ | Vishay, CRCW040233K2FKED |
| 12 | 1 | R22 | Resistor, Chip, 20k $\Omega, 1 / 16 \mathrm{~W}, 1 \%, 0402$ | Vishay, CRCW040220K0FKED |
| 13 | 3 | R23, R24, R25 | I.C. LT3597EUHG, 52-Pin QFN-5mm $\times 8 \mathrm{~mm}$ | Linear Technology, LT3597EUHG |
| 14 | 1 | U1 |  |  |

Additional Demo Board Circuit Components

| 1 | 1 | C9 | Capacitor, Aluminum 4.7 $\mu$ F, 63V | Sun Electric, 63CE4R7BS |
| :---: | :---: | :--- | :--- | :--- |
| 2 | 0 | D4 | Diode, OPT, SOD-323 |  |
| 3 | 1 | R7 | Resistor, Chip, 270k $\Omega, 1 / 16 \mathrm{~W}, 1 \%, 0603$ | Vishay, CRCW0603270KFKEA |
| 4 | 1 | R9 | Resistor, Chip, 91k $\Omega, 1 / 16 \mathrm{~W}, 1 \%, 0402$ | Vishay, CRCW040291K0FKED |
| 5 | 7 | R10-R14, R17, R18 | Resistor, Chip, 100k $\Omega, 1 / 16 \mathrm{~W}, 1 \%, 0402$ | Vishay, CRCW0402100KFKED |
| 6 | 1 | R15 | Resistor, Chip, 8.87k $\Omega, 1 / 16 \mathrm{~W}, 1 \%, 0402$ | Vishay, CRCW04028K87FKED |
| 7 | 1 | R21 | Thermistor, Chip, 100k,1\%, 0603 | Murata, NCP18WF104F12RB |
| Hardware - For Demo Board Only Turret, Testpoint, 091" Mill Max 2501-2-00-80-00-00-07-0   <br> 1 8 E1-E5, E12-E14 Turret, Testpoint, 063" Mill Max 2308-2-00-80-00-00-07-0 <br> 2 10 E6-E11, E15-E18 Jumper, 3-Pin 1 Row .079CC Samtec, TMM-103-02-L-S <br> 3 1 JP1 Shunt, .079" Center Samtec, 2SN-BK-G <br> 4 1 SHUNT FOR PJ1 Stand-Off, Nylon 0.5" Tall Keystone, 8833 (Snap On) <br> 5 4    |  |  |  |  |

## SCHEMATIC DIAGRAM



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This notice contains important safety information about temperatures and voltages. For further safety concerns, please contact a LTC application engineer.

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