Switching Transistor

PNP Silicon

Features

• Moisture Sensitivity Level: 1

• ESD Rating: Human Body Model; 4 kV,

Machine Model; 400 V

• These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	-40	Vdc
Collector-Base Voltage	V _{CBO}	-40	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current - Continuous	Ic	-600	mAdc

THERMAL CHARACTERISTICS

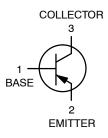
Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board T _A = 25°C	P _D	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	833	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



ON Semiconductor®

http://onsemi.com





SC-70 CASE 419 STYLE 3

MARKING DIAGRAM



2T = Specific Device Code

M = Date Code ■ Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBT4403WT1G	SC-70 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic			Min	Max	Unit	
OFF CHARACTERISTICS						
Collector-Emitter Breakdown Voltage (Note	V _{(BR)CEO}	-40	_	Vdc		
Collector - Base Breakdown Voltage (I _C = -0.	1 mAdc, I _E = 0)	V _{(BR)CBO}	-40	-	Vdc	
Emitter-Base Breakdown Voltage (I _E = -0.1	mAdc, I _C = 0)	V _{(BR)EBO}	-5.0	-	Vdc	
Base Cutoff Current (V _{CE} = -35 Vdc, V _{EB} = -	-0.4 Vdc)	I _{BEV}	-	-0.1	μAdc	
Collector Cutoff Current (V _{CE} = −35 Vdc, V _{EE}	s = -0.4 Vdc)	I _{CEX}	-	-0.1	μAdc	
ON CHARACTERISTICS						
$ \begin{array}{ll} DC \; Current \; Gain \\ (I_C = -0.1 \; mAdc, \; V_{CE} = -1.0 \; Vdc) \\ (I_C = -1.0 \; mAdc, \; V_{CE} = -1.0 \; Vdc) \\ (I_C = -10 \; mAdc, \; V_{CE} = -1.0 \; Vdc) \\ (I_C = -150 \; mAdc, \; V_{CE} = -2.0 \; Vdc) \\ (I_C = -500 \; mAdc, \; V_{CE} = -2.0 \; Vdc) \\ \end{array} $		h _{FE}	30 60 100 100 20	- - 300 -	-	
Collector – Emitter Saturation Voltage (Note 1) ($I_C = -150$ mAdc, $I_B = -15$ mAdc) ($I_C = -500$ mAdc, $I_B = -50$ mAdc)			- -	-0.4 -0.75	Vdc	
Base – Emitter Saturation Voltage (Note 1) $ (I_C = -150 \text{ mAdc}, I_B = -15 \text{ mAdc}) $ $ (I_C = -500 \text{ mAdc}, I_B = -50 \text{ mAdc}) $	V _{BE(sat)}	-0.75 -	-0.95 -1.3	Vdc		
SMALL-SIGNAL CHARACTERISTICS						
Current-Gain - Bandwidth Product ($I_C = -20$	mAdc, V _{CE} = -10 Vdc, f = 100 MHz)	f _T	200	-	MHz	
Collector-Base Capacitance (V _{CB} = -10 Vdc	, I _E = 0, f = 1.0 MHz)	C _{cb}	=	8.5	pF	
Emitter-Base Capacitance (V _{BE} = -0.5 Vdc,	I _C = 0, f = 1.0 MHz)	C _{eb}	-	30	pF	
Input Impedance (I _C = -1.0 mAdc, V _{CE} = -10 Vdc, f = 1.0 kHz)			1.5	15	kΩ	
Voltage Feedback Ratio (I _C = -1.0 mAdc, V _{CE} = -10 Vdc, f = 1.0 kHz)			0.1	8.0	X 10 ⁻⁴	
Small-Signal Current Gain (I _C = -1.0 mAdc, V _{CE} = -10 Vdc, f = 1.0 kHz)			60	500	-	
Output Admittance (I _C = -1.0 mAdc, V _{CE} = -10 Vdc, f = 1.0 kHz)			1.0	100	μmhos	
SWITCHING CHARACTERISTICS						
Delay Time	(V _{CC} = -30 Vdc, V _{EB} = -2.0 Vdc,	t _d	=	15		
Rise Time	$I_C = -150 \text{ mAdc}, I_{B1} = -15 \text{ mAdc})$	t _r	-	20	ns	
Storage Time	(V _{CC} = -30 Vdc, I _C = -150 mAdc,	t _s	=	225		
Fall Time	$I_{B1} = I_{B2} = -15 \text{ mAdc}$	t _f	-	30	ns	

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

SWITCHING TIME EQUIVALENT TEST CIRCUIT

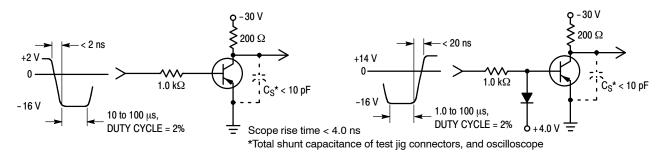


Figure 1. Turn-On Time

Figure 2. Turn-Off Time

TRANSIENT CHARACTERISTICS

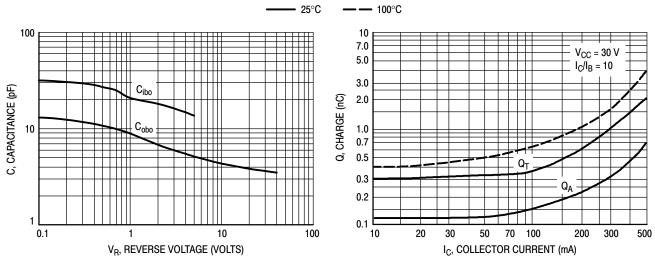


Figure 3. Capacitances

Figure 4. Charge Data

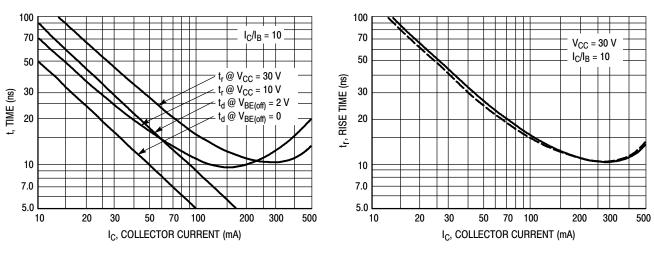


Figure 5. Turn-On Time

Figure 6. Rise Time

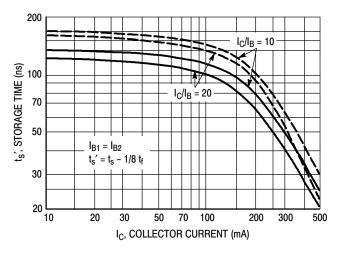


Figure 7. Storage Time

SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE

 $V_{CE} = -10 \text{ Vdc}$, $T_A = 25^{\circ}\text{C}$; Bandwidth = 1.0 Hz

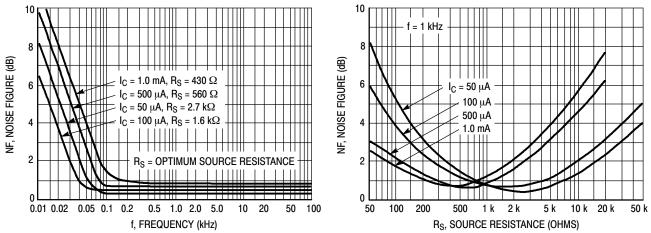


Figure 8. Frequency Effects

Figure 9. Source Resistance Effects

h PARAMETERS

 $V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz}, T_A = 25^{\circ}\text{C}$

This group of graphs illustrates the relationship between h_{fe} and other "h" parameters for this series of transistors. To obtain these curves, a high-gain and a low-gain unit were selected from the MMBT4403WT1 lines, and the same units were used to develop the correspondingly numbered curves on each graph.

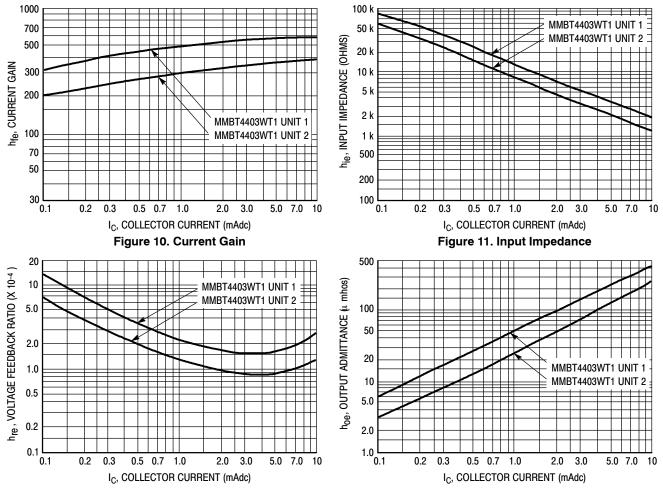


Figure 12. Voltage Feedback Ratio

Figure 13. Output Admittance

STATIC CHARACTERISTICS

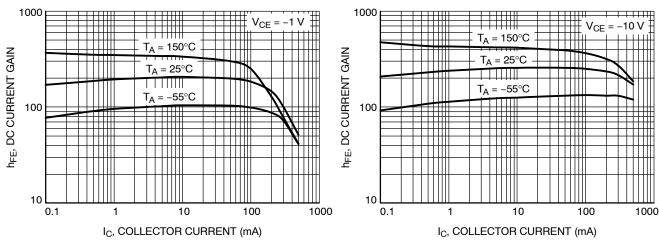


Figure 14. DC Current Gain vs. Collector Current

Figure 15. DC Current Gain vs. Collector Current

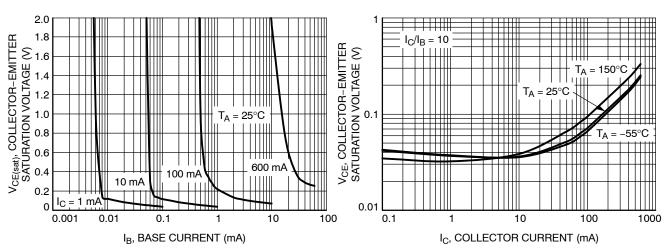


Figure 16. Saturation Region

Figure 17. Collector Emitter Saturation Voltage vs. Collector Current

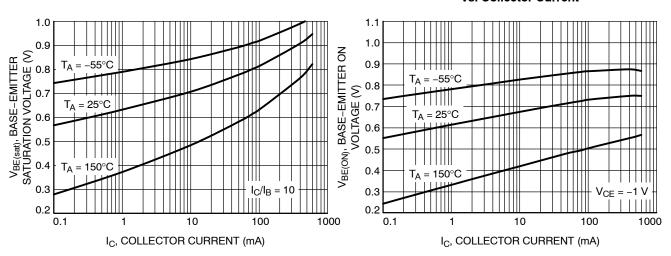


Figure 18. Base Emitter Saturation Voltage vs.
Collector Current

Figure 19. Base-Emitter Turn-On Voltage vs.
Collector Current

STATIC CHARACTERISTICS

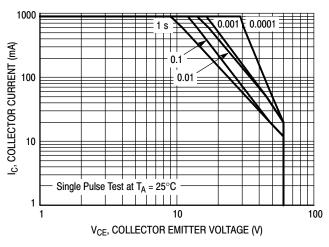


Figure 20. Safe Operating Area

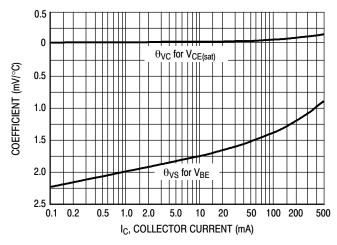


Figure 21. Temperature Coefficients





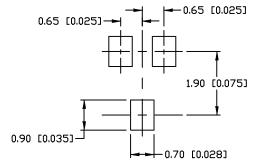
SC-70 (SOT-323) CASE 419 ISSUE P

DATE 07 OCT 2021

NOTES:

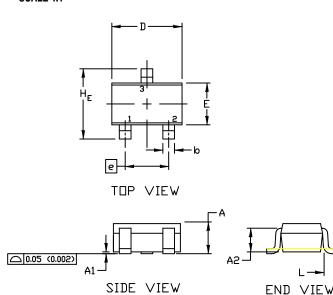
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH

	MILLIMETERS				INCHES	
DIM	MIN.	N□M.	MAX.	MIN.	N□M.	MAX.
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2		0.70 REF		0.028 BSC		
b	0.30	0.35	0.40	0.012	0.014	0.016
С	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
Ε	1.15	1.24	1.35	0.045	0.049	0.053
e	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC				0.026 BS	C
L	0.20	0.38	0.56	0.008	0.015	0.022
HE	2.00	2.10	2.40	0.079	0.083	0.095
				`		



For additional information on our Pb-Free strategy and soldering details, please download the IIN Semiconductor Soldering and Mounting Techniques Reference Manual, SILDERRM/D.

SOLDERING FOOTPRINT



GENERIC MARKING DIAGRAM



XX = Specific Device Code

M = Date Code

■ = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1: CANCELLED	STYLE 2: PIN 1. ANODE 2. N.C. 3. CATHODE	STYLE 3: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. CATHODE	
STYLE 6:	STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:	STYLE 11:
PIN 1. EMITTER	PIN 1. BASE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. CATHODE
2. BASE	2. EMITTER	2. SOURCE	2. CATHODE	2. ANODE	2. CATHODE
3. COLLECTOR	3. COLLECTOR	3. DRAIN	3. CATHODE-ANODE	3. ANODE-CATHODE	3. CATHODE

DOCUMENT NUMBER:	98ASB42819B	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	SC-70 (SOT-323)		PAGE 1 OF 1	

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer pu

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT: Email Requests to: orderlit@onsemi.com

onsemi Website: www.onsemi.com

TECHNICAL SUPPORT North American Technical Support: Voice Mail: 1 800-282-9855 Toll Free USA/Canada Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support:

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative