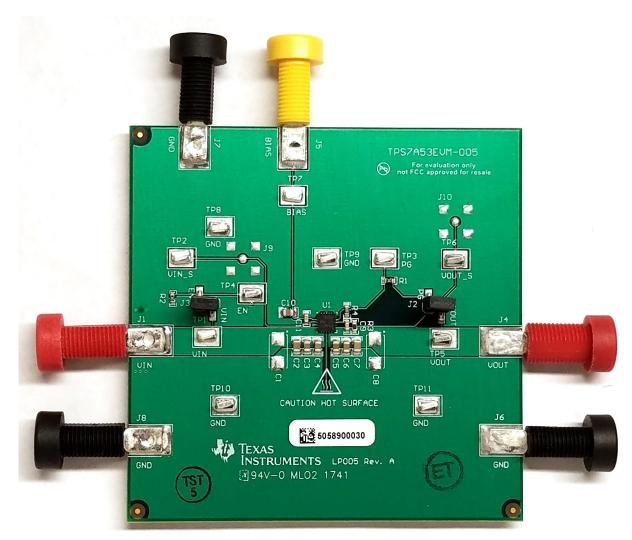


User's Guide SBVU043–March 2018

TPS7A53EVM-005 Evaluation Module



This user's guide describes the operational use of the TPS7A53EVM-005 evaluation module (EVM) as a reference design for engineering demonstration and evaluation of the TPS7A5301QRGR, low-dropout linear regulator (LDO). Included in this user's guide are setup and operating instructions, thermal and layout guidelines, a printed-circuit board (PCB) layout, a schematic diagram, and a bill of materials (BOM).

Throughout this document, the terms *demonstration kit*, *evaluation board*, *EVM*, and *evaluation module* are synonymous with the TPS7A53EVM-005.

The following related documents are available through the Texas Instruments web site at www.ti.com.

Table 1. Related Documentation

Device	Literature Number		
TPS7A53-Q1	SBVS298		



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1 Introduction

Texas Instruments' TPS7A53EVM-005 EVM helps design engineers evaluate the operation and performance of the TPS7A53-Q1 for possible use in their own circuit application. This particular EVM configuration contains a single low-noise, high-PSRR linear regulator for radar applications. The regulator is capable of delivering up to 3 A to the load with ultralow VIN to VOUT dropout voltage. For stability, use a 47-µF (or larger) output capacitor for the TPS7A53.

1.1 Before You Begin

The following warnings and cautions are noted for the safety of anyone using or working close to the TPS7A53EVM-005. Observe all safety precautions.



Warning Warning Hot surface. Contact may cause burns. Do not touch.

CAUTION

The circuit module may be damaged by overtemperature. To avoid damage, monitor the temperature during evaluation and provide cooling, as needed, for your system environment.

CAUTION

Some power supplies can be damaged by application of external voltages. If using more than one power supply, check your equipment requirements and use blocking diodes or other isolation techniques, as needed, to prevent damage to your equipment.

CAUTION

The circuit module is not a finished product or electrical appliance. The module does not contain current or voltage thresholds for circuit protection. It must be used by qualified personnel with additional equipment for evaluation only.



2 EVM Setup

This section describes how to properly connect and set up the TPS7A53EVM-005, including the jumpers and connectors on the EVM board.

2.1 Input/Output Connectors and Jumper Descriptions

2.1.1 J1 – VIN

Input power-supply voltage connector. Twist together the positive input lead and ground return lead from the input power supply, and keep them as short as possible to minimize input inductance. Add additional bulk capacitance between the input supply and ground (use the C1 footprint) if the supply leads are greater than six inches. For example, an additional 47-µF electrolytic capacitor connected from the input supply (J1) to ground can improve the transient response of the TPS7A53-Q1, and eliminates unwanted ringing on the input because of long wire connections.

2.1.2 J2 – PG

Pullup-voltage selector for PG. This EVM is designed so that PG can be pulled up either to VOUT by shorting J2, or pulled up to another voltage by applying an external voltage to the PG post.

2.1.3 J3 – EN

Output enable. To enable the output, connect a jumper to short VIN to EN.

2.1.4 J4 – VOUT

Regulated output voltage connector.

2.1.5 J5 – BIAS

If the input supply (VIN) voltage is less than 1.4 V but greater than 1.1 V, use a BIAS voltage of 3.0 V to 6.5 V to provide power to the TPS7A53-Q1. If the input voltage is greater than 1.4 V, the BIAS pin does not have to be connected. The BIAS supply pin typically consumes 2.3 mA.

2.1.6 J6 – GND

Output ground return connector.

2.1.7 J7 – GND

Bias ground return connector.

2.1.8 J8 – GND

Input ground return connector.

2.1.9 TP1 – VIN

Input test point.

2.1.10 TP2 – VIN_S

Input sense test point.

2.1.11 TP3 – PG

4

PG test point.

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2.1.12 TP4 – EN

Enable test point.

2.1.13 TP5 – VOUT

Output test point.

2.1.14 TP6 - VOUT_S

Output sense test point.

2.1.15 TP7 - BIAS

Bias test point.

2.1.16 TP8 – GND

Ground test point.

2.1.17 TP9 - GND

Ground test point.

2.1.18 TP10 - GND

Ground test point.

2.1.19 TP11 - GND

Ground test point.

2.2 Soldering Guidelines

To avoid damaging the integrated circuit (IC), use a hot-air system for any solder rework to modify the EVM for the purpose of repair or other application reasons.

2.3 Equipment Connection

Use the following steps when connecting the equipment:

- 1. Set the input and bias power supplies to 6.5 V (max), and turn the power supplies off.
- 2. Connect the positive voltage lead from the input power supply to VIN at the J1 connector of the EVM.
- 3. Connect the ground lead from the input power supply to GND at the J2 connector of the EVM.
- 4. Connect a 0-A to 3-A load between OUT and GND. The connector used depends on the desired output current.
- 5. Disable the output by floating J3.

3 Operation

Use the following steps to operate the equipment:

- 1. Turn on the power supplies.
- 2. Enable the output by jumping J3 (the EN pin) to VIN.
- 3. Vary the respective load and input voltage as necessary for test purposes.

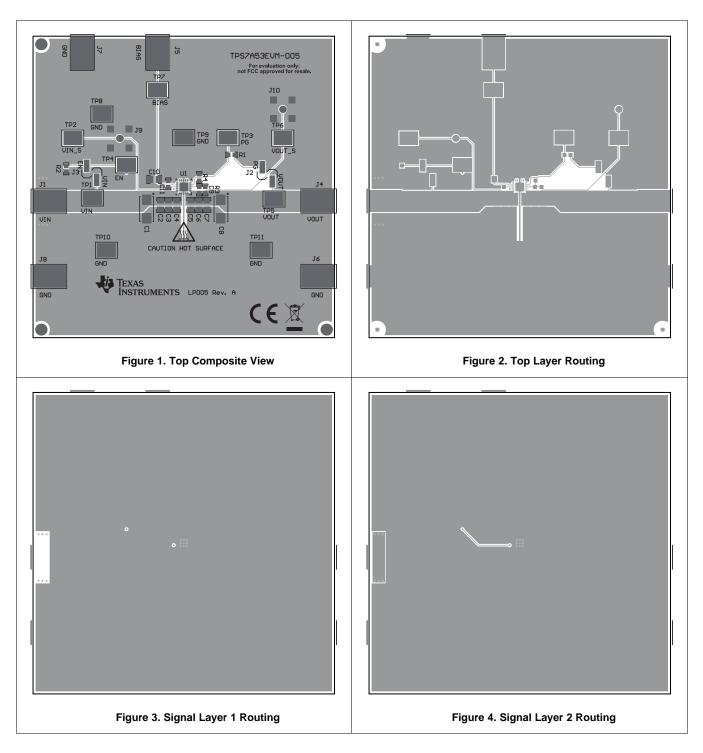


PCB Layout

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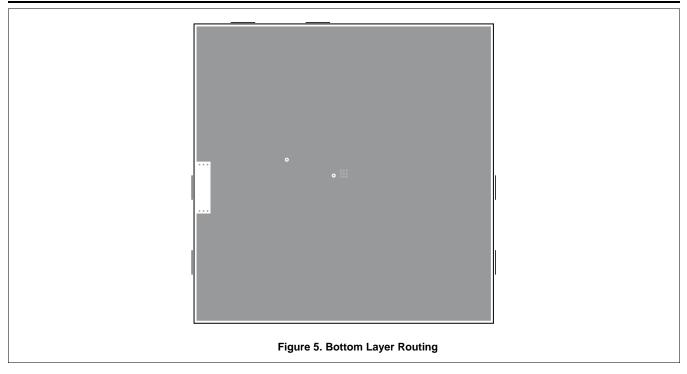
4 PCB Layout

Figure 1 to Figure 5 illustrate the PCB layout for this EVM.





PCB Layout

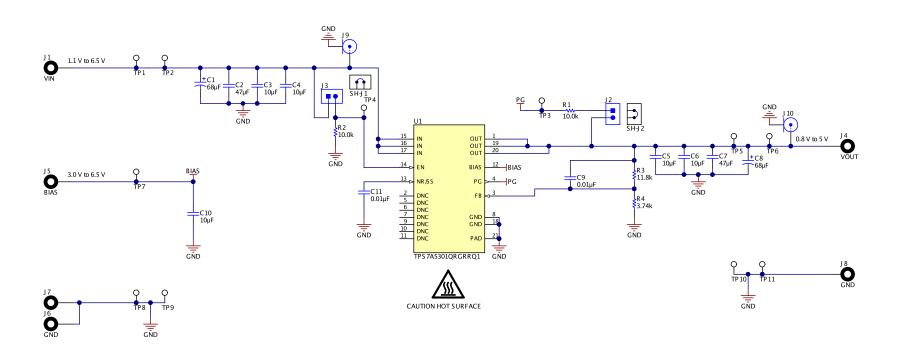




Schematic

5 Schematic

Figure 6 is the schematic for this EVM.



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Figure 6. TPS7A53EVM-005 Schematic



6 Bill of Materials

Table 2 shows the TPS7A53EVM-005 BOM.

Designator	QTY	Value	Description	Package Reference	Part Number	Manufacturer	Alternate Part Number	Alternate Manufacturer
!PCB1	1		Printed Circuit Board		LP005	Any		
C2, C7	2	47uF	CAP, CERM, 47 µF, 10 V, ±20%, X5R, 0805	0805	GRM21BR61A476ME15	Murata		
C3, C4, C5, C6, C10	5	10uF	CAP, CERM, 10 µF, 16 V, ±10%, X5R, 0805	0805	GRM21BR61C106KE15L	Murata		
C9, C11	2	0.01u F	CAP, CERM, 0.01 µF, 50 V, ±10%, X7R, 0603	0603	GRM188R71H103KA01D	Murata		
J1, J4	2		Standard Banana Jack, Insulated, Red	6091	6091	Keystone		
J2, J3	2		Header, 2.54 mm, 2x1, Gold, R/A, SMT	Header, 2.54 mm, 2x1, R/A, SMT	87898-0204	Molex		
J5	1		BANANA JACK, 15A, Insulated, Nylon, Yellow	940x438x438mil	108-0907-001	Emerson Network Power		
J6, J7, J8	3		Standard Banana Jack, Insulated, Black	6092	6092	Keystone		
R1, R2	2	10.0k	RES, 10.0 k, 1%, 0.1 W, 0603	0603	CRCW060310K0FKEA	Vishay-Dale		
R3	1	11.8k	RES, 11.8 k, 1%, 0.1 W, 0603	0603	CRCW060311K8FKEA	Vishay-Dale		
R4	1	3.74k	RES, 3.74 k, 1%, 0.1 W, 0603	0603	CRCW06033K74FKEA	Vishay-Dale		
SH-J1, SH-J2	2	1x2	Shunt, 100mil, Gold plated, Black	Shunt	969102-0000-DA	3M	SNT-100-BK-G	Samtec
TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8, TP9, TP10, TP11	11	SMT	Test Point, Compact, SMT	Testpoint_Keystone_Compact	5016	Keystone		
U1	1		Automotive-Grade 3-A, High-Accuracy, Low-Noise, LDO Voltage Regulator, RGR0020A (VQFN-20)	RGR0020A	TPS7A5301QRGRRQ1	Texas Instruments		
C1, C8	0	68uF	CAP, TA, 68 µF, 16 V, ±10%, 0.2 ohm, SMD	6032-28	TPSC686K016R0200	AVX		
FID1, FID2, FID3	0		Fiducial mark. There is nothing to buy or mount.	N/A	N/A	N/A		
J9, J10	0		Connector, SMA Jack, Vertical, Gold, SMD	SMA	142-0711-201	Emerson Network Power		

Table 2. TPS7A53EVM-005 BOM⁽¹⁾⁽²⁾⁽³⁾⁽⁴⁾

⁽¹⁾ These assemblies are ESD sensitive, observe ESD precautions.

⁽²⁾ These assemblies must be clean and free from flux and all contaminants. Use of no-clean flux is not acceptable.

⁽³⁾ These assemblies must comply with workmanship standards IPC-A-610 Class 2.

⁽⁴⁾ Unless otherwise noted in the Alternate Part Number or Alternate Manufacturer columns, all parts may be substituted with equivalents.

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CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

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- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

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