

SPIRIT2

CEM

## Sub-1GHz programmable transceiver module

The **RC-SPIRIT2-XXX** module is based on STMicroelectronics S2-LP transceiver. Pin to pin compatible with the SPSGRF family modules manufactured from STMicroelectronics.

This device is an easy-to-use sub 1GHz transceiver certified module with many programmable features. The module is designed for a maximum performance in a minimal space, with 4 programmable I/O pins. Programmable from external microcontroller via SPI interface.

Mechanical Dimensions :  $11.5 \times 13.5 \times 2.5$ mm Metal shield.

For more information and details, please refer to the S2-LP datasheet (www.st.com).

Module Information standard version :

## RC-SPIRIT2 - XXX - XX

*Frequency* 433=433MHz 868=868MHz Antenna Type NA=Pad version default = UFL connector

Using this module, it will be possible to **easily update** a product designed with Spirit1 technology (SPSGRF modules) with the new S2-LP device.

These RF modules are CE certified and integrate wireless connectivity in target electronic devices without requiring particular RF experience or expertise. This certified solution optimizes the time to market of the final applications.

#### **Applications :**

- Low-Power Wireless Systems
- Home and Building Automation
- Smart Grid and Automatic Meter Reading
- Wireless Sensor Network
- 6LoWPAN systems

#### **Features**

- Ultra Low consumption technology
- Easy to Use
- Small Dimension SMD mounting
- CE CERTIFIED



| Technical Characteristics      |                     |       |         |       |       |
|--------------------------------|---------------------|-------|---------|-------|-------|
| Parameter                      | Symbol              | Min.  | Тур.    | Max.  | Units |
| Supply Voltage                 | V <sub>cc</sub>     | 1.8   | 3.00    | 3.6   | VDC   |
| Supply Current RX Mode         | I <sub>CRX</sub>    |       | 7.20    |       | mA    |
| Supply Current TX Mode +10dBm  | I <sub>CTX1</sub>   |       | 11.00   |       | mA    |
| Supply Current TX Mode +16dBm  | I <sub>CTX2</sub>   |       | 20.00   |       | mA    |
| Supply Current Standby Mode    | I <sub>CTXAV</sub>  |       | 0.50    |       | μA    |
| Supply Current Shut Down Mode  | I <sub>CTXAV1</sub> |       | 2.50    |       | nA    |
| Operative Frequency Band       | F <sub>of</sub>     |       | 433/868 |       | MHz   |
| RF Power Output 50ohm(*)       | P <sub>oo</sub>     | -30.0 |         | +15.5 | dBm   |
| RF Sensibility 38.4 kbps 2GFSK | S <sub>d</sub>      |       | -109    |       | dBm   |
| RF Sensibility 0.3 kbps 2GFSK  | S <sub>cc</sub>     |       | -128    |       | dBm   |
| Operative Temperature          | T <sub>1</sub>      | -30.0 |         | +75.0 | °C    |

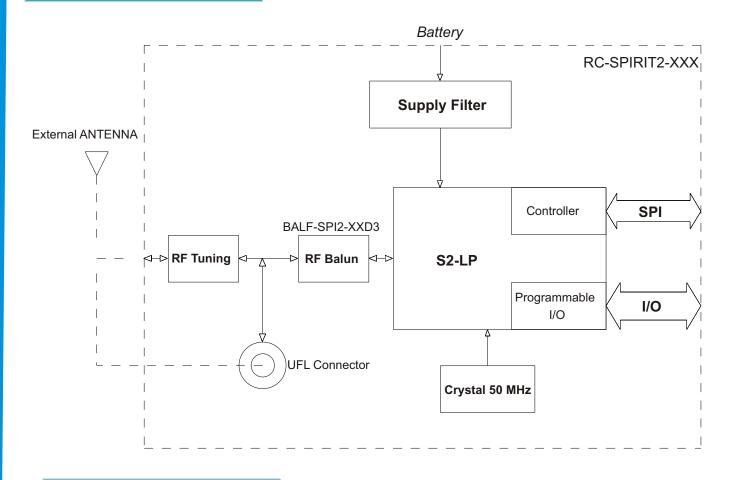
(\*) It is possible to reach the max value if the device (S2-LP) is programmed in Boost Mode (see the STMicroelectronics S2LP datasheet).

| Eleo | ctrical Parameters about t                          | he cryst         | al XTAL | _ used    |         |       |                       |
|------|---|------------------|---------|-----------|---------|-------|-----------------------|
| No.  | Item  | Symb.            | Ele     | ctrical S | Remark  |       |                       |
| NO.  | item  | Synnb.           | Min.    | Тур.      | Max.    | Units | Remark                |
| 1    | Nominal Frequency                                   | F0               | 5       | 50.00000  | 0       | MHz   |                       |
| 2    | Mode of Vibration                                   |                  |         | Funda     | amental |       |                       |
| 3    | Frequency Tolerance                                 | △F/F0            | -10     | -         | 10      | ppm   | At 25℃±3℃             |
| 4    | Operating Temperature<br>Range                      | T <sub>OPR</sub> | -30     | -         | 85      | °C    |                       |
| 5    | Frequency Stability<br>(over operating temperature) | тс               | -15     | -         | 15      | ppm   | Ref. to 25℃           |
| 6    | Storage Temperature                                 | T <sub>STG</sub> | -55     | -         | 125     | °C    |                       |
| 7    | Load capacitance                                    | CL               | -       | 8         | -       | pF    |                       |
| 8    | Equivalent Series Resistance                        | ESR              | -       | -         | 60      | Ω     |                       |
| 9    | Drive Level   | DL               | -       | 50        | 200     | μW    |                       |
| 10   | Insulation Resistance                               | IR               | 500     | -         | -       | MΩ    | At 100V <sub>DC</sub> |
| 11   | Shunt Capacitance                                   | C0               | -       | -         | 2       | pF    |                       |
| 12   | Aging Per Year                                      | Fa               | -2      | -         | 2       | ppm   | First Year            |
| 13   | Package type  | ETSB             |         |           |         |       |                       |





#### **Block Diagram**



#### **RF BALUN Used :**

Frequency : 433MHz Model : BALF-SPI2-02D3

#### **STMicroelectronics Datasheet:**

https://www.st.com/resource/en/datasheet/balf-spi2-02d3.pdf

Frequency : 868/915MHz Model : BALF-SPI2-01D3

#### STMicroelectronics Datasheet:

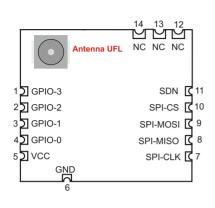
https://www.st.com/resource/en/datasheet/balf-spi2-01d3.pdf

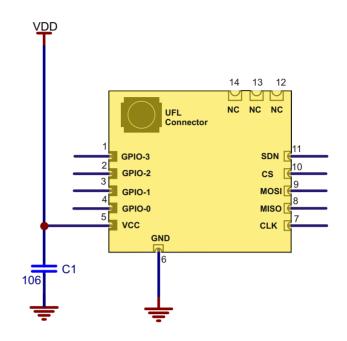
Radiocontrolli s.r.l refuses any responsibility for irregular uses of the devices and for any possible lack or inaccuracy of the data and reserves the right to change in whole or in part these information without notice.

www.radiocontrolli.com



#### Pin out device



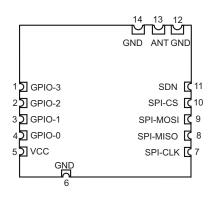


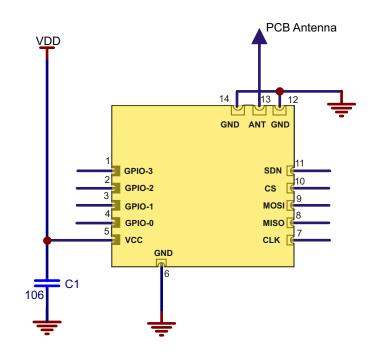
## Version with UFL Connector RC-SPIRIT2-XXX (Antenna through UFL Connector)

| Pin Descriptio | ns       |     |   |
|----------------|----------|-----|---|
| Pin Number     | Name     | I/O | Description   |
| 01             | GPIO-3   | I/O | General purpose I/O may be configured through the SPI registers to perform various functions. |
| 02             | GPIO-2   | I/O | General purpose I/O may be configured through the SPI registers to perform various functions. |
| 03             | GPIO-1   | I/O | General purpose I/O may be configured through the SPI registers to perform various functions. |
| 04             | GPIO-0   | I/O | General purpose I/O may be configured through the SPI registers to perform various functions. |
| 05             | VCC      | -   | Supply Voltage  |
| 06             | GND      | -   | Ground  |
| 07             | SPI-CLK  | I   | SPI slave clock input   |
| 08             | SPI-MISO | 0   | SPI MISO (MASTER in / SLAVE)  |
| 09             | SPI-MOSI | I   | SPI MOSI (MASTER out SLAVE in)  |
| 10             | SPI-CS   | I   | SPI "Chip Select" (SPI slave select)  |
| 11             | SDN      | I   | SHUTDOWN input (active high)  |
| 12,13,14       | N.C.     | -   | Not connected   |



#### Pin out device





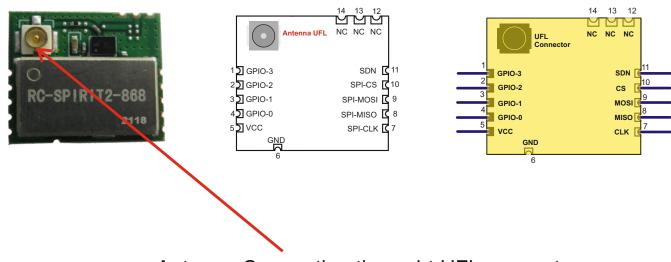
## Version without UFL Connector RC-SPIRIT2-XXX-NA (Antenna connection through PAD N.13)

| Pin Descriptio | ns       |     |  |
|----------------|----------|-----|--|
| Pin Number     | Name     | I/O | Description  |
| 01             | GPIO-3   | I/O | General purpose I/O may be configured through the SPI registers to perform various functions.    |
| 02             | GPIO-2   | I/O | General purpose I/O may be configured through<br>the SPI registers to perform various functions. |
| 03             | GPIO-1   | I/O | General purpose I/O may be configured through the SPI registers to perform various functions.    |
| 04             | GPIO-0   | I/O | General purpose I/O may be configured through the SPI registers to perform various functions.    |
| 05             | VCC      | -   | Supply Voltage   |
| 06             | GND      | -   | Ground   |
| 07             | SPI-CLK  | I   | SPI slave clock input  |
| 08             | SPI-MISO | 0   | SPI MISO (MASTER in / SLAVE)   |
| 09             | SPI-MOSI | Ι   | SPI MOSI (MASTER out SLAVE in)   |
| 10             | SPI-CS   | I   | SPI "Chip Select" (SPI slave select)   |
| 11             | SDN      | Ι   | SHUTDOWN input (active high)   |
| 12             | GND      | -   | Ground   |
| 13             | ANT      | -   | Antenna Connection   |
| 14             | GND      | -   | Ground   |



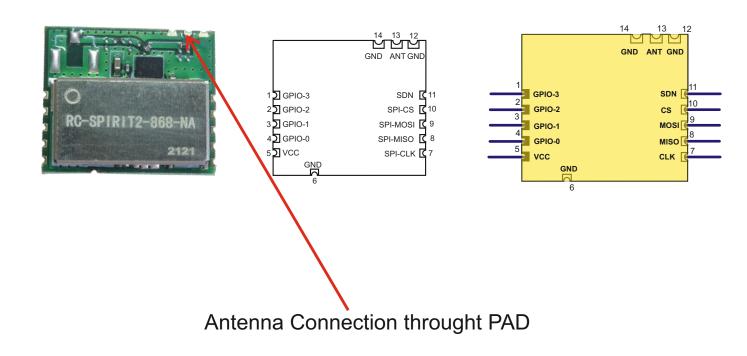


## STANDARD Version = RC-SPIRIT2-XXX



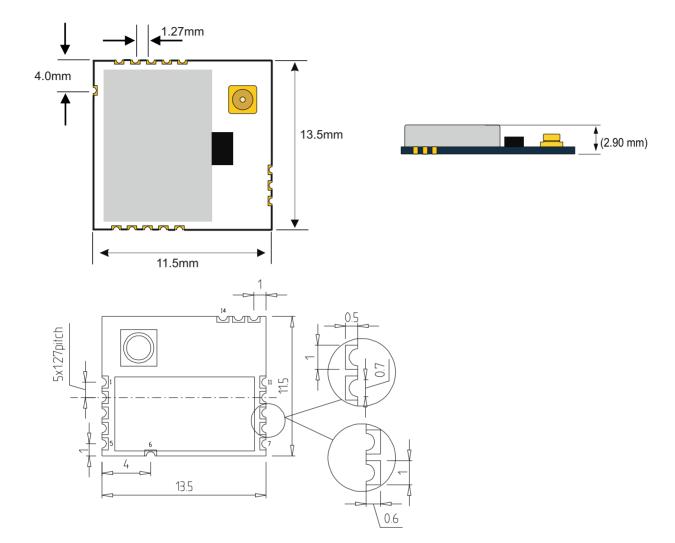
Antenna Connection throught UFL connector

## «NA» Version = RC-SPIRIT2-XXX-NA

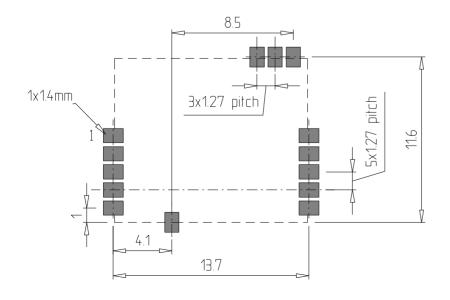




**Mechanical dimensions** 



## **Recommended PCB Layout**







#### **Recommended Hardware design**

#### 1) Hardware

All unused pins should be left floating; do not ground. All GND pins must be well grounded. Traces should not be routed underneath the module.

#### 2) Power Supply

The transceiver module must be powered from a regulated voltage. It is recommended to keep the power supply line for VCC as short and low impedence as possible. Near the power pins it is recommended to insert a ceramic the decoupling capacitor (100nF).

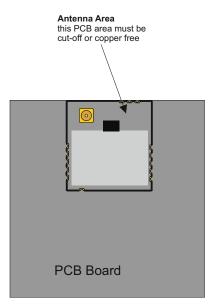
#### 3) Ground Plane

It is recommended to have a copper ground plane under the shielded zone of the module. The ground plane should be unbroken.

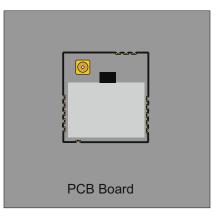


#### 4) Module Placement

The antenna on the PCB has an omnidirectional radiation pattern. To maximize antenna efficiency, an adequate grounding plane must be provided under the module. Instead the areas underneath and surrounding the antenna area must be free of copper.



Recommended location XY plane

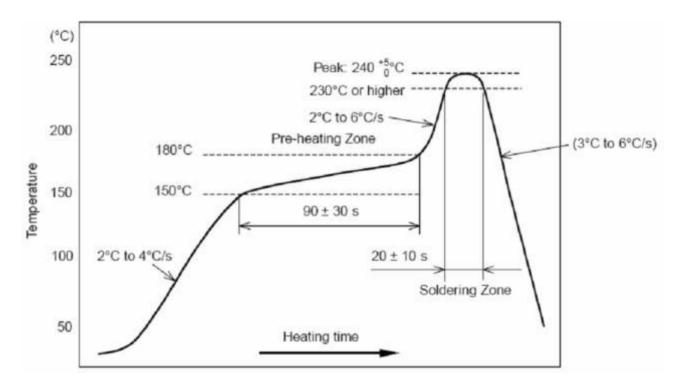


Not Recommended location XY plane





#### Recommended Reflow Profile for Lead Free Solder







# Sub-1 GHz transceiver development kit based on RC-SPIRIT2-XXX module.

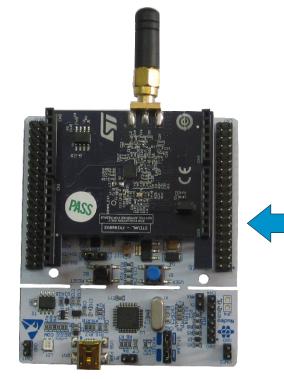
The **RC-SPIRIT2-XXX-EK** is an evaluation board based on the RC-SPIRIT2-XXX module. This module is based on STMicroelectronics chip (S2-LP) that is a sub-1 GHz ultra-low power, data-rate transceiver, suitable for ISM bands and Wireless M-Bus.

## RC-SPIRIT2 - XXX - EK

*Frequency* 434=434MHz 868=868MHz



The Evaluation board can be used instead of those provided by the chip manufacter (www.st.com) denominated STEVAL-FKIXXXV2. With this board it is possible to use all the SW resources provided for the development activity.



Development kit with **STEVAL-FKI868V1** Development kit with **STEVAL-FKI433V2** 



Development kit with **RC-SPIRIT2-868-EK** Development kit with **RC-SPIRIT2-434-EK** 





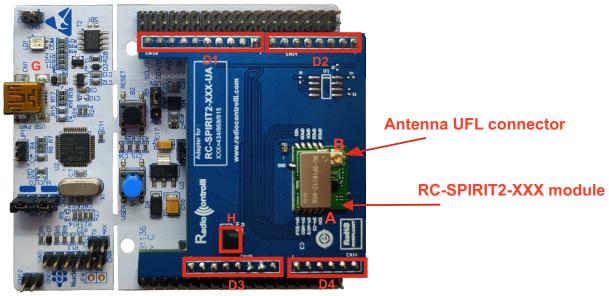
## Hardware description

The RC-SPIRIT2-XXX-EK evaluation boards are designed to work in the sub 1GHz band (433/868). Some features on the boards are (see the picture below) :

- RC-SPIRIT2-XXX module (A)
- Two rows with Arduino compliant connectors (D1-4)
- UFL connector (B)
- A NUCLEO-L152RE or NUCLEO-L053R8 evaluation board (G)
- A jumper for RC-SPIRIT2-XXX current measurement (H)

RC-SPIRIT2-XXX signal test points are split across two rows which are Arduino compliant connectors: CN1,CN2,CN3,CN4.

The RC-SPIRIT2-XXX shield is connected to the Nucleo motherboard via the Arduino connectors.



| RadioCon | trolli Module |              | ARDUINO      | Connector |                |
|----------|---------------|--------------|--------------|-----------|----------------|
| Pin      | Name          | CN1 (D1)     | CN2 (D2)     | CN3 (D3)  | CN4 (D4)       |
| 1        | SDO           | Pin 5 (MISO) |              |           |                |
| 2        | SDI           | Pin 4 (MOSI) |              |           |                |
| 3        | SCLK          |              | Pin 4 (SCLK) |           |                |
| 4        | CSn           |              |              |           | Pin 2 (CS)     |
| 5        | GPIO-0        |              |              |           | Pin 1 (GPIO-0) |
| 6        | GPIO-1        |              |              |           | Pin 3 (GPIO-1) |
| 7        | GPIO-2        |              |              |           | Pin 4 (GPIO-2) |
| 8        | GPIO-3        |              |              |           | Pin 6 (GPIO-3) |
| 9        | GND           | Pin 7        |              | Pin 6, 7  |                |
| 10       | SDN           |              | Pin 8 (SDN)  |           |                |
| 11       | GND           | Pin 7        |              | Pin 6, 7  |                |
| 12       | GND           | Pin 7        |              | Pin 6, 7  |                |
| 13       | VCC           |              |              | Pin 4     |                |
| 14       | VCC           |              |              | Pin 4     |                |
| 15       | GND           | Pin 7        |              | Pin 6, 7  |                |
| 16       | GND           | Pin 7        |              | Pin 6, 7  |                |
| 17       | GND           | Pin 7        |              | Pin 6, 7  |                |
| 18       | GND           | Pin 7        |              | Pin 6, 7  |                |



## **Hardware Setup**

The board can be powered by the Nucleo evaluation board mini USB connector. When the JP1 jumper is fitted (H) in the previous figure, the radio section is supplied. By removing this jumper and connecting a power meter, you can measure the RC-SPIRIT2-XXX current consumption.

- 1) Connect an antenna to the SMA connector
- 2) Ensure the jumper configuration on the board is correct
- 3) Connect the STM32 Nucleo board to the PC through a USB cable (via CN5 connector)

To use the application Notes STSW-S2LP-DK follow step by step the document «*Getting Started with the S2-LP development kit*» from STMicroelectronics denominated UM2149.pdf.

## **Evaluation Software package**

The STSW-S2LP-DK is an evaluation SW package based on the S2-LP high performance ultralow power RF transceiver for RF wireless applications in the sub-1 GHz band. It is designed to operate in the license-free ISM and SRD frequency bands at 433, 868 and 920 MHz, but can also be programmed to operate at additional frequencies in the 413-479, 452-527, 826-958, 904-1055 MHz bands. The STSW-S2LP-DK package supports the S2-LP kit platforms available on the associated web pages. It provides an S2-LP library with a complete set of APIs to interface with the S2-LP features, as well as a set of applications demonstrating the use of features of the same device. Each demonstration application comes with a complete set of source files.

The S2-LP\_DK GUI application provides an interactive PC interface for the registers on the S2-LP. Its main function is to configure the analog radio section and the packet handler in a user friendly manner for the most common applications.

| S2-UP DK - Application v1.3.1<br>Tools Help  |  |              |       |                  |                       |       | _             |              | > |
|--|--|--------------|-------|------------------|-----------------------|-------|---------------|--------------|---|
| Chest (CT DIC) · Classe  | 83-L7 vanises 3.0 VI<br>Buard, STEVAL FICH |              | 44    | Registers tal    | Pagolar               |       | 1964          | Drive        |   |
|  | HIGHE STHUZLUSZRE                          | L-Wurdeo     |       | > 0x80           | GROE CO               | 1 MF  | OverA         | DVDA         | 1 |
| Rade settings  |  |              |       | > 0x81           | GPIOH_CO              |       | 0x82          | 0x82         |   |
| Presumence lives e Cleta sate Pre  | quercy deviation Drawel filter             | Test F/P     |       | 1 0v82           | GR02_00               |       | 0x42          | 0x42         |   |
| 432,000004 MHz 30,000 Mages  | 13,979 Meter 202, 115                      |              | -     | > 0xE3<br>> 0xE5 | GR05_C0               | 100   | 0x42          | 0x42         |   |
| Contraction of the second seco |  | TECH         | START | 2 0466           | SHATE                 |       | 0x24          | Ov16         |   |
| Modulation Output conver   |  | 75 264       | TMT . | > 0.67           | \$10.01               |       | 0x50          | 0x27         |   |
| The second se  | KE * CONTRURE                              |              |       | > 048            | SMED                  |       | DVCB          | 0v62         |   |
|  | OICAU Internet                             |              |       | > 049            | 1,01958               |       | 0x2#          | 0x2A         |   |
|  |  |              |       | > 0x8A<br>> 0x8C | IF, OFFSEI<br>ORINACE | 0.040 | 0xC2<br>0x3F  | Ox88<br>Dx3F |   |
| radiet settings V femanisaiser first V Louis   | evel Comment V Romins 1011                 |              |       | > 0400           | OWUM                  |       | Overo         | Overo        |   |
|  | Contraction of the second second           |              |       | > 0vfK           | M004                  |       | 0x82          | Ov83         |   |
|  |  |              |       | > 0.07           | M003                  |       | 0847          | 0x28         |   |
|  | Data electoration                          |              |       | 2.0x10           | MOD2                  |       | 0x27          | 0x77         |   |
| Packet format  | T PEC                                      | -            |       | > 0/1            | MOD1<br>MOD0          |       | OwES<br>OwA3  | 0483         |   |
| R MARC O WHENS   | 2 Data whiteway                            | CONTRARE     |       | 2 0413           | CHEVE                 |       | Ovt 3         | 0x23         |   |
|  | and other the state of                     | PACKET       |       | 2 0:14           | APC2                  |       | OvC8          | OvC8         |   |
|  |  |              |       | > 0x15           | APCI                  |       | Oct 6         | Ov16-        |   |
|  |  | 1000         |       | 2 0x16<br>2 0x17 | APC0                  |       | 0425          | 0x25         |   |
| Lengths are expressed in biz *   |  | Length Pield |       | 2 0/18           | RSSI, PUT<br>RSSI, TH |       | OvED<br>Ov26- | 0x83         |   |
| the grade of the state of the s |  | 1 by/te      |       | 2 0/19           | AGOCTRUS              | 1     | Ox80          | 0450         |   |
| Proverble longith Svitc  | Address Park                               | 360          |       | 2 0x1A           | AGOCTELA              |       | 0x54          | 0x54         |   |
|  |  |              |       | > 0/18           | AGOCTRLS              |       | Owtito        | OvHID        |   |
| 22 2 lwgth 22 2 0s 8   | Enzole                                     | Poly Coll?   |       | 2 040            | AGOCTRL2              |       | 0x22          | 0x22         |   |
|  |  |              |       | > 0x10           | AGOCTRES              |       | Ov50<br>Ov5C  | 0x59<br>0x80 |   |
|  |  |              |       | 2 Oct#           | ANT SELE              |       | 0x55          | 0x45         |   |
| Pediat   |  |              |       | > 0.20           | 0.00085               | 52    | OwDB          | OviCit       |   |
| PRIMAL STREWORD  | UNITS INFLORD                              | CRE          |       | 2 0x21           | CLOCORES              |       | 0x58          | 0x56         |   |
|  |  |              |       | > 0x28           | PORTOTRU              |       | Over D        | Dv80<br>Dv10 |   |
| 32 b/a 32 b/a  | 5 bits 1                                   | 1 bits       |       | 2 0x20           | PORTING               |       | Owito         | Overo        |   |
|  | prinzielu                                  |              |       | > 0.02           | POUTOTRE              |       | 0x00          | 0x20         |   |
|  |  |              |       | > 0x2P           | PECCEPTE              | 2     | Ded1          | OwBD         |   |
|  |  |              |       | 2.0.50           | PORTOTRE              | 1     | 0x50          | 0x20         |   |
|  |  |              |       |                  |                       |       |               |              |   |



### **NUCLEO BOARD L0**

### Notes for using the STMicroelectronics application «STSW-S2LP-DK»

In the last version of this application (versions higher than 1.3.1) the following things happen:

- When you work with the STEVAL board everything works fine !!
- When you work with the RC-SPIRIT2-XXX-EK board there will be the following request :

| OM6 (ST DK)     Open   |  | << Re                  | egisters table             |               |
|--|--|------------------------|----------------------------|---------------|
| Radio settings   | uency deviation Channel filter Te  |                        | ddress Register            | Value Default |
| MHz ksps   |  | TX CW START            |                            |               |
| Modulation Output power           Output power         Normal           Image: Constraint of the second seco | CONFIGURE  | TX PN9 START           |                            |               |
| Packet settings \/ Transmission Test \/ Low Leve   | el Command / Running RSSI /  |                        | ×                          |               |
| Packet format  | Old GUI firmware detected.<br>Please click Ok to start an upgra                              |                        |                            |               |
| BASIC     WMBUS  | (please note that system can be<br>If the upgrade procedure fails, p<br>the Binaries folder. |                        |                            |               |
| BASIC WMBUS Lengths are expressed in bits  | If the upgrade procedure fails, p  |                        |                            |               |
| Lengths are expressed in bits  Preamble length Sync  | If the upgrade procedure fails, p<br>the Binaries folder.<br>Address Field CRC               | lease flash the approp | oriate firmware located in |               |
| Lengths are expressed in bits  | If the upgrade procedure fails, p<br>the Binaries folder.<br>Address Field CRC               | lease flash the approp | oriate firmware located in |               |
| Lengths are expressed in bits   Preamble length Sync   | If the upgrade procedure fails, p<br>the Binaries folder.<br>Address Field CRC               | lease flash the approp | oriate firmware located in |               |
| Lengths are expressed in bits   Preamble length Sync   | If the upgrade procedure fails, p<br>the Binaries folder.<br>Address Field CRC               | lease flash the approp | oriate firmware located in |               |

If your answer is "CANCEL" everything works well and you can use this software to verify all the features about the RadioControlli module .

We suggest pressing the «CANCEL» button, in this way it will be possible to use the application created by STMicroelectronics to check all the features of the RC-S2LP-XXX module.

Instead, if your answer is "OK" there will be the update of the NUCLEO board and it will no longer be possible to work with the Radiocontrolli board (instead with the STEVAL is everything OK).

(The problem is that in the STEVAL board there is an EEPROM where the STMicroelectronics write a secret code, and this application recognizes this code, instead the RadioControlli evaluation board does not contain this secret code (infact the EEPROM is not mounted on the board).

To return in the old condition and enable you to try the RadioControlli module, you will must update the NUCLEO board with another software version (provided by us) and transfer it to the Node\_L053R8.

Contact <u>sales@radiocontrolli.com</u> to achieve this Nucleo version.



## **NUCLEO BOARD L1**

## Notes for using the STMicroelectronics application «STSW-S2LP-DK»

Using a board NUCLEO L1 is not possible to run the application STWS-S2LP-DK with the RadioControlli evaluation board RC-SPIRIT2-XXX-EK.

The application always tries to recognize the secret code (STMicroelectronics) inserted in the EEPROM of the STEVAL board.

The RadioControlli evaluation board does not contain this secret code (the EEPROM is not mounted).

You will must update the NUCLEO with another software version (provided by us) and transfer it to the Node\_L152RE.

Contact <a href="mailto:sales@radiocontrolli.com">sales@radiocontrolli.com</a> to achieve this NUCLEO L1 version.