User Guide
EVB-ATEK255P4-01

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Revisions

Revision No	Revision Date	Revision Reason	Section / Page No
1.0	19.07.2021	Initial Version	
1.1	08.01.2022	Format and Content Fixed	
1.2	16.04.2022	Format and Content Fixed	

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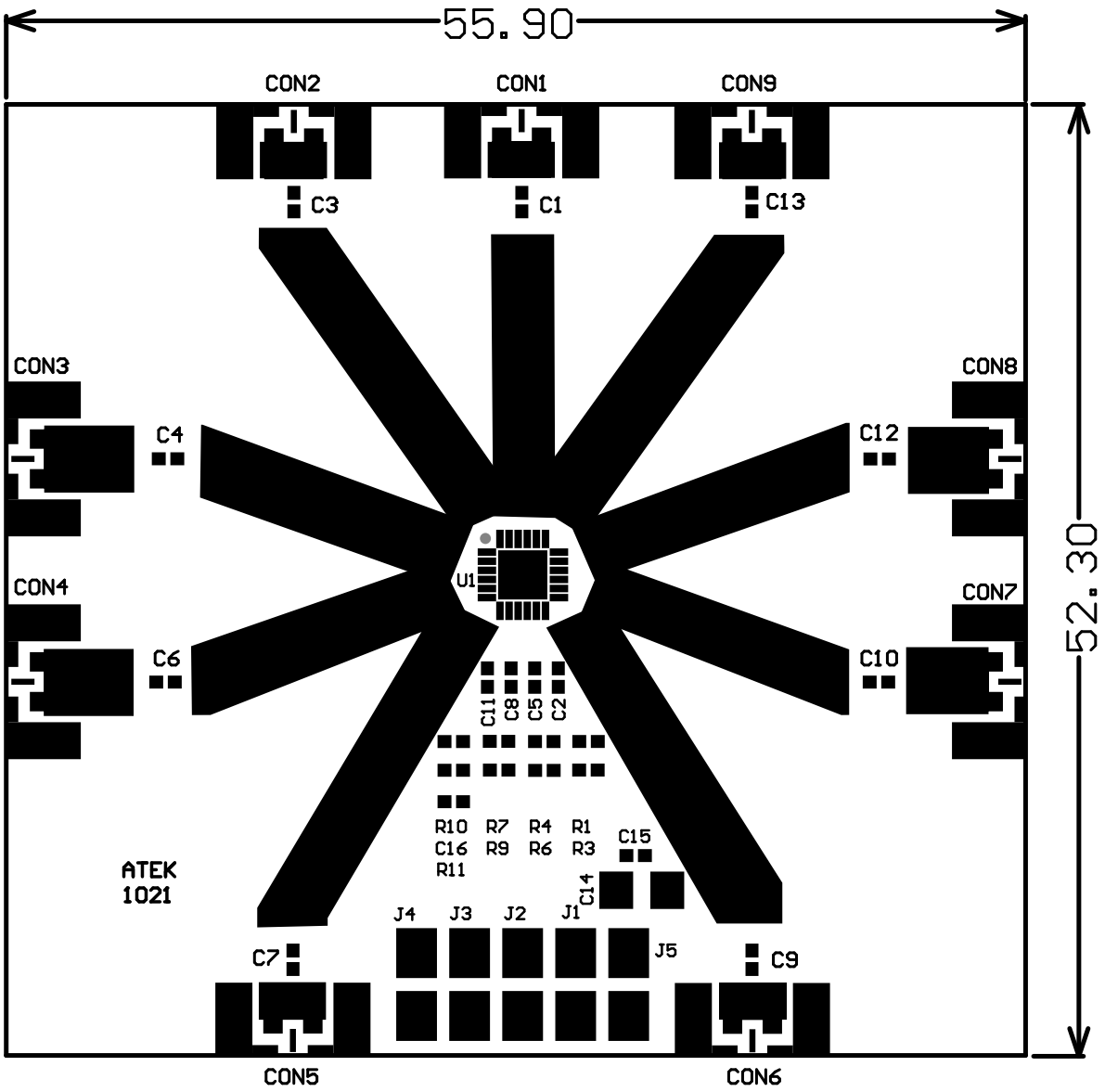
1 GENERAL INFORMATION



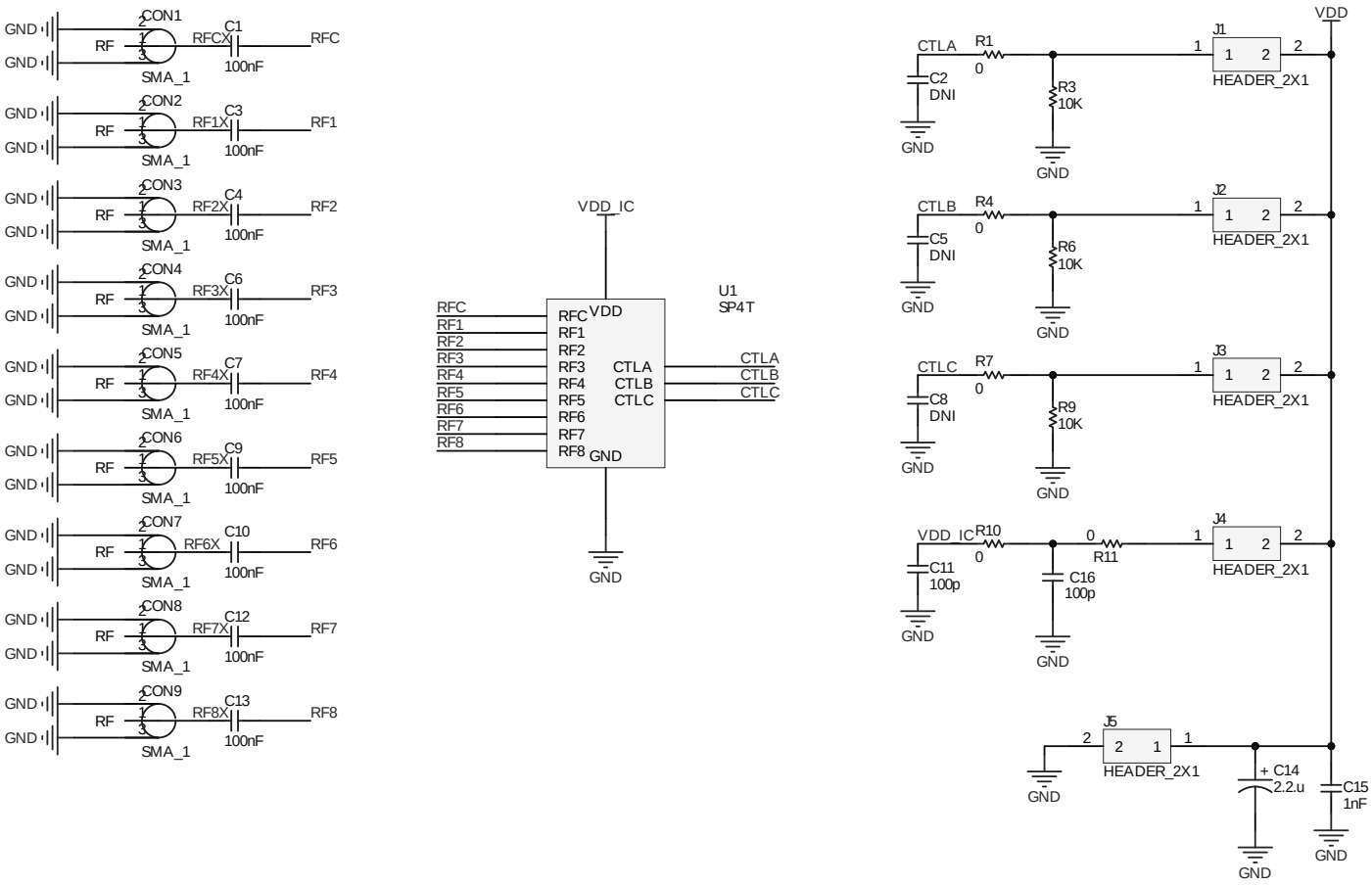
PIN Name	Definition	Comment
CO1	RF IN	SMA Connector
CO3, CO4, CO7, CO8	RF OUT	SMA Connector
CO2, CO5, CO6, CO9	N/A	SMA Connector
J1 Up, J1 Down, J2 Up, J3 Up, J4 Up, J5 Up	VDD	2.54mm Header
J5 Down	GND	2.54mm Header
J4 Down	CTRL A	2.54mm Header
J3 Down	CTRL B	2.54mm Header
J2 Down	N/A	2.54mm Header

Notes:

1. VDD Voltage is detailed in Datasheet.
2. Control Voltage is detailed in Datasheet.
3. The definition of up, down, right, and left is valid for this view of PCB.

2 DESIGN INFORMATION

2.1 SCHEMATIC



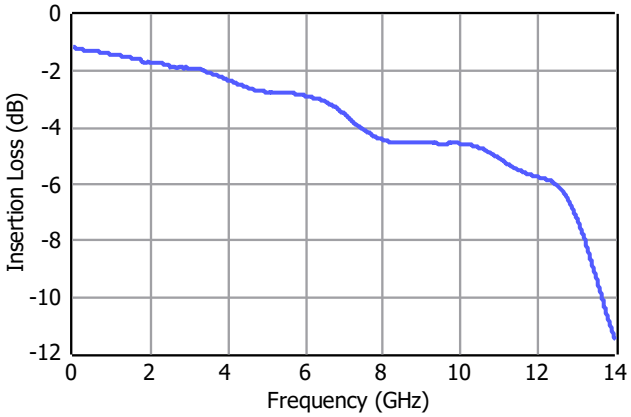
2.2 BOM

Designator	Footprint	Qty	Comment	PN
C1, C4, C6, C10, C12	0402	5	100nF	
C2, C3, C5, C7, C8, C9, C13	0402	7	DNP	
C11, C16	0402	1	100nF	
C14	CASEA	1	2.2uF	
C15	0402	1	1nF	
CO1, CO3, CO4, CO7, CO8	SMA Connector	5	SMA Connector	
CO2, CO5, CO6, CO9	SMA Connector	4	DNP	
J1, J2, J3, J4, J5	2x1 Header	5	2x1 Header	
R1, R4, R7, R10, R11	0402	5	0R	
R3, R6, R9	0402	3	1k	
U1	ATEKQ4424	1	SP4T	ATEK255P4

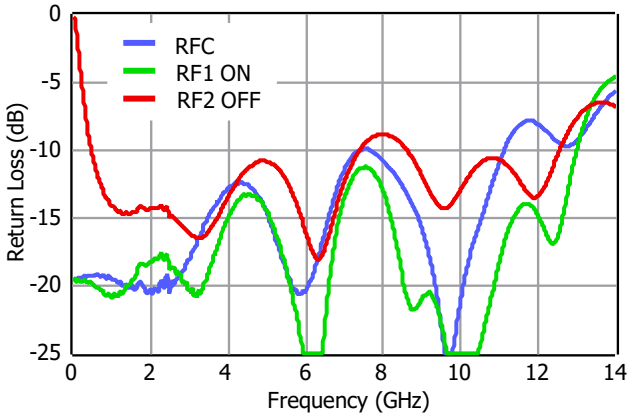
3 TYPICAL PERFORMANCE PLOTS

Conditions unless otherwise specified: V_{CTRL} = 5 V, T = 25 C, CW. For details, please refer to the datasheet.

Insertion Loss



Return Loss



Isolation from RFC to RF2