Dual Synchronous Buck PMIC

Adaptable PMIC AnD8240

Product Description

The AnD8240 Adaptable PMIC uses AnDAPT AmP™ advanced technology consisting of fully flexible digital fabric combined with high performance analog blocks. The AnD8240 consists of two configurable 6A Synchronous Buck Regulators and four high current LDOs. The AnD8240 is fully tested and ready to use in designs. The AnD8240 Buck regulators use Voltage Mode control. The AnD8240 also has an integrated sequencer and 4 additional auxiliary LDOs. The user can modify output voltage and rail sequencing using resistor or WebAdapter™ online tools. The sequencer has the capability to be based on timed delays or Power Good signals. Adaptable PMICs provide fastest prototyping and time to market, while providing best in class performance and flexibility. The AnD8240 design is available in the WebAmP™ software tool library for full customization capability. The Adaptable PMIC is optimized to power high end Processors by integrating multiple power rails into single chip designs.

Features

- Two 6A Synchronous Buck Regulators
- PVIN: 4.75V to 14V, V_{OUT}: 0.7V to 5.0V
- 571kHz Switching Frequency
- Integrated 30 mΩ MOSFET
- Protection: UVLO, OCP, OVP, OTP
- Four 1A LDO (V_{OUT}: 0.6V to 3.3V)
- Four auxiliary LDOs: 1.2V, 1.8V, 2.5V, 3.3V internal input voltage 4.75V, or external 5V up to 200mA output current
- Adjustable output voltage with 2.4 mV resolution
- 1% load regulation
- Buck efficiency up to 95%
- Power-good flag output and Enable input
- Soft start/stop, sequencing, pre-bias startup
- −40°C to +125°C operating junction temperature
- Easy WebAmP upgrade path to On-Demand PMIC

Applications

- On-demand power management, multi-rail power integration
- Powering server, processor, memory, storage, network switcher and router platforms
- Powering FPGA, processor, SSD, subsystem power control & sequencing

Product Detail

The AnD8240 Adaptable PMIC consists of two customizable, Synchronous Buck Regulators, customizable control and status pins including enable input, an optional power-good output, and optional output flags to signal when the system triggers an overvoltage (OVP), overcurrent (OCP), or undervoltage lockout (UVLO) condition. It also includes four high current LDOs.
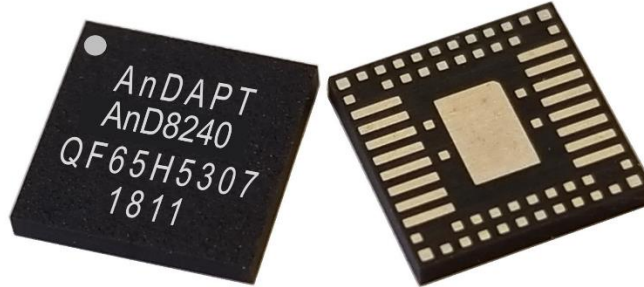

Dual Buck WebAmP Design View
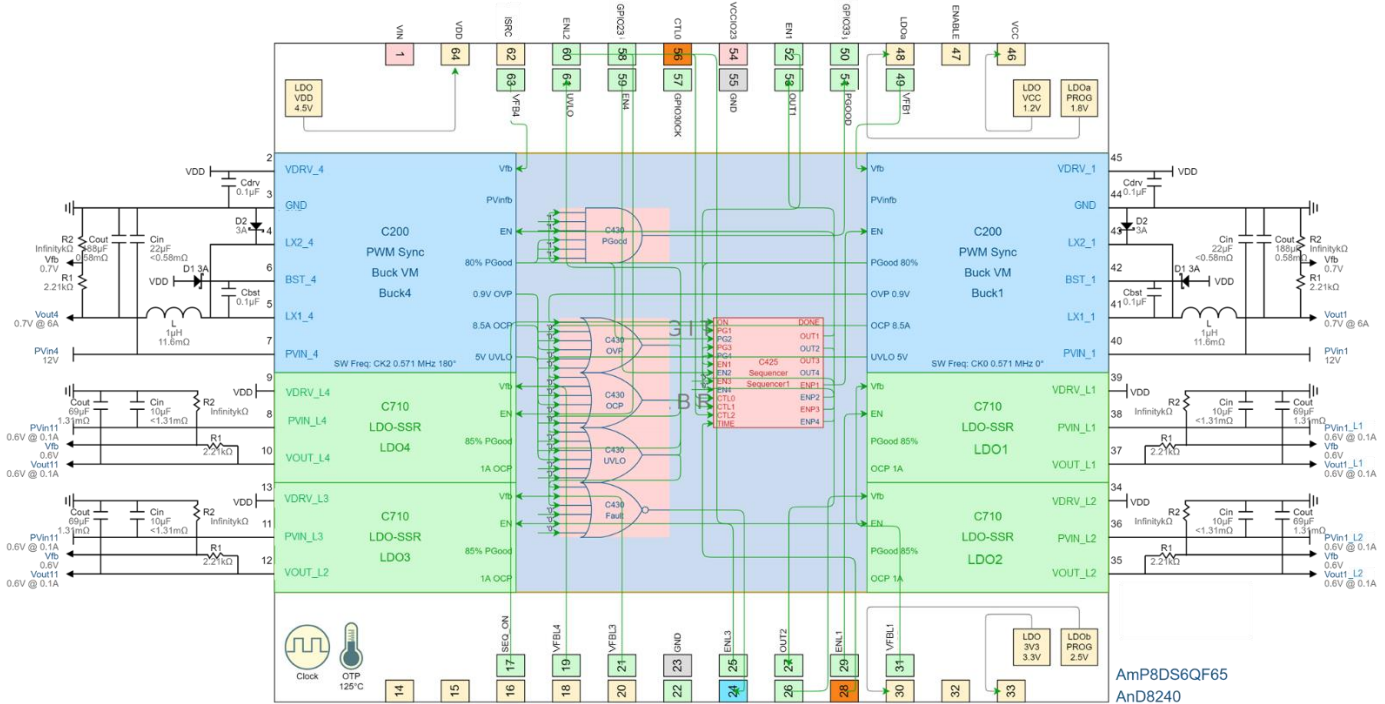
Order Information

Part Number	Package	Description	Availability
AnD8240QF65	QF65	Multi-Rail Dual Buck PMIC	Now

Package Marking Example – QF65

Package Pinout

TOP VIEW