

**ALL-200G**  
**Universal Gang Programmer**  
**User's Manual**

Apr. 2014

## Caution : USB Interface Compatibility

- For USB 2.0 compatibility, your computer should work with the following operation system or later revision : Windows 2000 SP4/XP SP1/Server 2003/Vista/7/8
- Software incompatibility may happen if your computer uses any USB Host Controller from following:
  - Compaq PCI to USB Open Host Controller
  - VIA Tech PCI Universal Serial Bus Host Controller
  - VIA Tech 3038 PCI to USB Open Host Controller
  - SiS 7001 PCI to USB Open Host Controller
  - OPTi 82C861 PCI to USB Open Host Controller
- To check the validation of USB Controller on your computer:
  - Click [Start], point to [Settings], and click [Control Panel]
  - Click [System], [Hardware], [Device Manager] and then [Universal Serial BusController]
  - Check whether the USB Controller shows up or not
- In case your USB interface card is incompatible with ALL-200G, you are suggested to use the USB interface card we approved and recommended.  
Please refer to “optional accessory” in ALL-200G data sheet or contact HI-LO distributors for further information.



## VERIFICATION OF COMPLIANCE

*This Verification of Compliance is hereby issued to the below named company. The test results of this report relate only to the tested sample identified in this report.*

**Technical Standard: EMC DIRECTIVE 2004/108/EC  
(EN55022 / EN55024)**

### General Information

Applicant: HI-LO SYSTEM RESEARCH CO., Ltd.  
4F., No. 18, Ln. 76, Rueiguang Rd., Neihu Dist.,  
Taipei City 11491, Taiwan

### Product Description

EUT Description: USB 2.0 Interface  
Brand Name: **HI-LO SYSTEMS**  
Model Number: ALL-200G; FLASH-100

### Measurement Standard

EN 55022: 2010  
EN 61000-3-2: 2006 + A1: 2009 + A2: 2009  
EN 61000-3-3: 2013  
EN 55024: 2010  
(IEC 61000-4-2: 2008; IEC 61000-4-3: 2006 + A1: 2007 + A2: 2010; IEC 61000-4-4: 2012;  
IEC 61000-4-5: 2005; IEC 61000-4-6: 2008; IEC 61000-4-8: 2009; IEC 61000-4-11: 2004)

### Measurement Facilities

Xindian Lab.: Compliance Certification Services Inc.  
No.163-1, Jhongsheng Rd., Xindian Dist., New Taipei City, 23151 Taiwan.  
Tel: +886-2-22170894 / Fax: +886-2-22171029

*This device has been shown to be in compliance with and was tested in accordance with the measurement procedures specified in the Standards & Specifications listed above and as indicated in the measurement report number: T140521T02-E*

Sam Hu / Assistant Manager

Date: July 4, 2014



程智科技股份有限公司  
Compliance Certification Services Inc.



### VERIFICATION OF COMPLIANCE

This Verification of Compliance is hereby issued to the product designated below.

Product	USB 2.0 Interface
Model	ALL-200G
Trade name	<b>HI-LO SYSTEMS</b>
Applicant	HI-LO SYSTEM RESEARCH CO., Ltd. 4F, No.18, Ln.76, Rueiguang Rd, Neihu Dist., Taipei City 11491, Taiwan
Applicable Standard(s)	EN 60950-1:2006+A11:2009+A1:2010+A12:2011 IEC 60950-1:2005+A1:2009
Report No.	T140521T02-LV
Laboratory	Compliance Certification Services Inc. (Tainan Lab.) No.8, Jiucengling, Xinhua Dist., Tainan City 712, Taiwan (R.O.C.)

This device has been tested and found to comply with the stated standard(s), which is(are) required by the Directive 2006/95/EC .  
The test results are indicated in the test report and are applicable only to the tested sample identified in the report.

Amber Liao / Section Manager  
Tainan Lab  
Date: Aug. 30, 2014



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# 1. Introduction

This manual guides you to install and operate ALL-200G under Windows 2000/XP/Server 2003/Vista/7/8. ALL-200G works with PC through USB 1.1/2.0 (Universal Serial Bus) to perform high-speed data transmission. The high-speed processor in programmer precisely controls programming timing and flow, this ensures accurate programming waveforms always generated on ALL-200G.

ALL-200G is embedded with 4Mbit memory to support the programming capacity for most E(E)PROM, MCU/MPU, and PLD. The software automatically uses PC memory as buffer to support high-density memory ICs, such as FLASH devices.

## 1.1 Programmer and Accessories

Each ALL-200G package contains the following standard accessories:

- Base Unit.
- An AC power cable, 1.8 M in length.
- A USB cable (Type A to Type B), 1.0 M in length.
- A CD-ROM for Driver Files.
- User's Manual.

◇ Optional Accessories:

Please find the required HEADs on HI-LO Website (<http://www.hilosystems.com.tw/>).



## 1.2 PC System Requirements

### Minimal Requirements

- PC/Pentium III or above.
- Microsoft compatible mouse.
- A hard disk with at least 50 Mbyte free space.
- A CD-ROM drive with speed x2 or above.
- At least one USB port (Version 1.1/2.0)
- Equip 128MB memory space or above.
- Operating System: Windows 2000/XP/Server 2003/Vista/7/8.
- A monitor with 800 x 600 pixels or above.

### Recommended Requirements

- PC/Pentium IX or above.
- Microsoft compatible mouse.
- A hard disk with at least 150 Mbyte free space.
- A CD-ROM drive with speed x8 or above.
- At least one USB port (Version 2.0)
- Equip 512MB memory space or above.
- Operating System: Windows 2000/XP/Server 2003/Vista/7/8.
- A monitor with 800 x 600 pixels or above.

### 1.3 ALL-200G Specification

Device Support	Pin Count : from 8 pins up to over 300 pins Device Type : EPROM,EEPROM,Serial PROM, FLASH, PLD/CPLD/FPGA, MPU/MCU,
Device Contact	Optioal HEADs
Max Sockets in parallel	8 sockets on optional GANG Programming Module
Controller	16 bits high-speed controller with big sized FPGA & CPLD
Interface Port	USB port
Data Transfer Rate	USB 1.1 : 12 Mb/s USB 2.0 : 480 Mb/s (suggested)
Max Sites in parallel	Up to 8 via tiered star USB
Functions	Load file,Read Master,Program,Verify,Auto,ID Check, Checksum, Blank Check, Erase, Protect/Unprotect, Secure, Edit, Function Configuration,Self Test
Host Computer Requirements	<ul style="list-style-type: none"> <li>● An Intel Pentium III or compatible processor with 128MB of RAM</li> <li>● At least one USB port available (V 1.1/ 2.0)</li> <li>● 50 MB free hard disk space with Windows 2000/XP/ Server 2003/Vista/7/8 operating system</li> <li>● CD-ROM Drive</li> </ul>
Power	AC voltage : 100-240 VAC Frequency : 50-60 Hz Power consumption : 50 W
Dimension	W x D x H : 162mm x 257mm x 100mm
Weight	~ 2.24 kg (Base Unit) ~ 2.56 kg (with socket boards on top)
Operating Temperature	0 ~ 40°C (32 ~ 105°F)
Safety Standards	CE Approved

## 2. Single ALL-200G Installation

### 2.1 Hardware

Before installation, make sure your PC has USB 1.1/2.0 port which can be connected to ALL-200G Programmer through USB cable.

USB 2.0 is suggested for fast data transmission with ALL-200G Programmer.

**USB 1.1 (Full Speed) : Transmission rate 12 Mb/s**

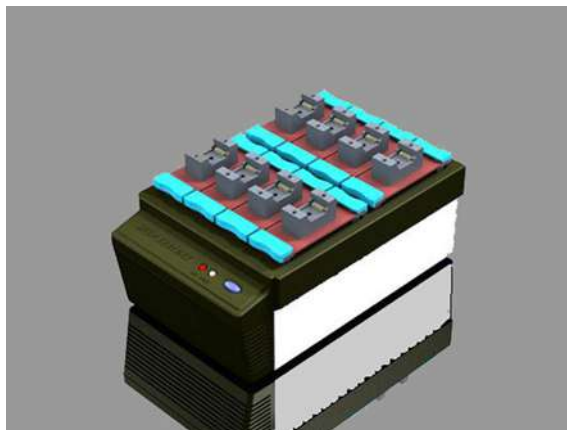
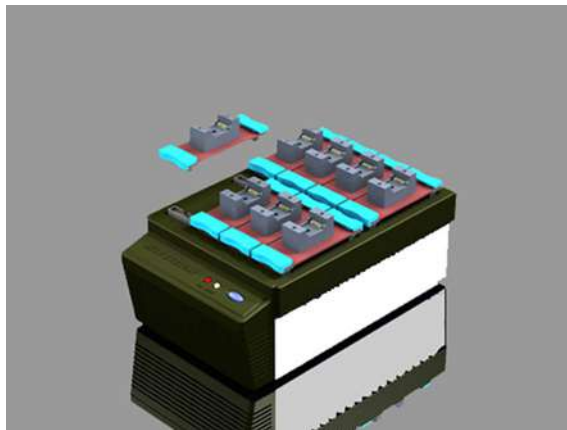
**USB 2.0 (High Speed) : Transmission rate 480 Mb/s (Suggested)**

## 2.1.1 ALL-200G Hardware Installation and Applications

### Step 1:

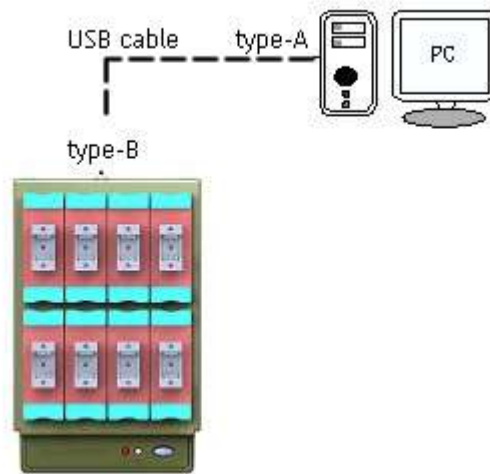
Make sure power of Base Unit is in “OFF” state and then positioning and mounting HEAD onto Base Unit.

See figures below:

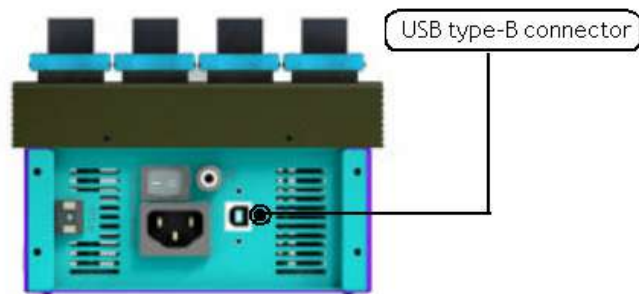


**Step 2:**

Connect programmer and PC as figures below.



Connect the Type B end of USB cable to the USB Type B connector at rear panel of programmer, connect the Type A end of USB cable to the USB Type A connector on the PC.

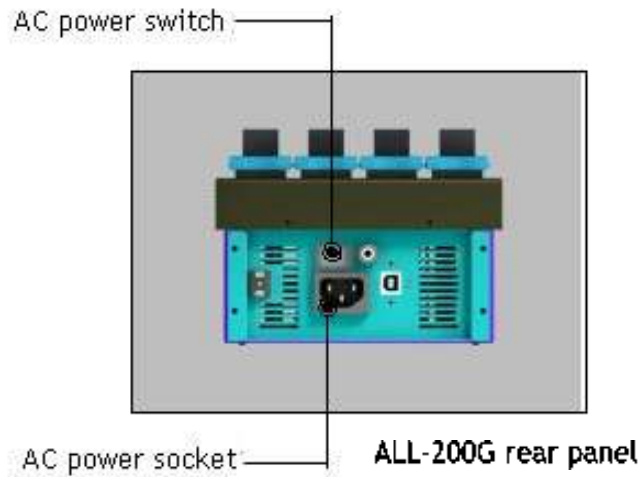


ALL-200G rear panel

**Step 3:**

Connect power cable to AC power socket of ALL-200G Programmer and plug in the other end to the outlet of power source (100-240VAC/50-60 Hz).

Power on the ALL-200G from the switch above the AC power socket on the rear panel.

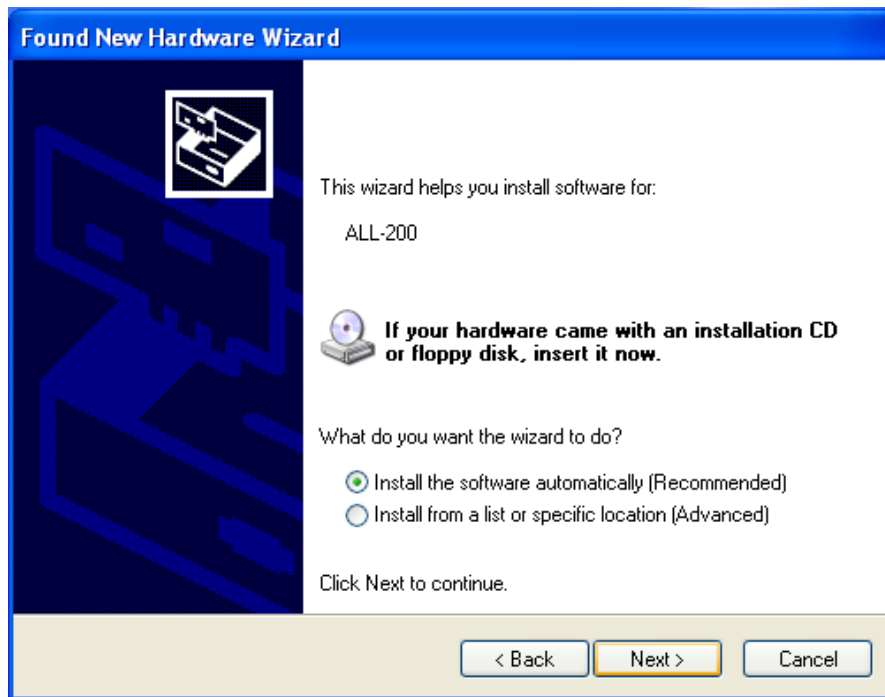


- ✧ Note: Please have PC completely powered on before turning on ALL-200G for installation and applications.

## 2.2 USB Driver

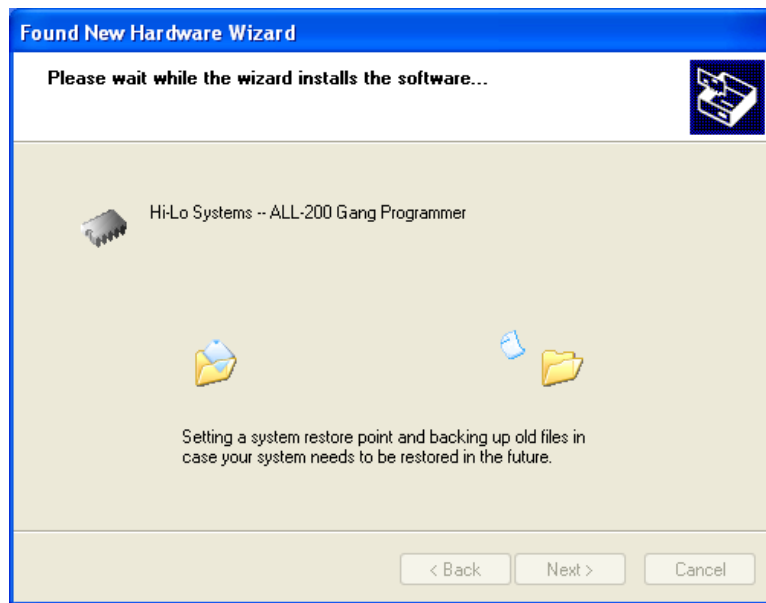
Insert the Driver Files CD into CD-ROM drive, connect USB cable between PC and ALL-200G, power on ALL-200G Programmer, the PC will detect the new hardware and a window will pop up “Found New Hardware Wizard”.

### Step 1:



- ✧ To install under Windows 2000/XP/Server 2003/Vista/7/8, you need to change Log-in authority to “Administrator” or “Power-User” in order to install new software/hardware driver.

## Step 2:



PC will allocate files named “ALL200.INF” and “ALL200.SYS” for installation. Then click “Next” to continue.

## Step 3:



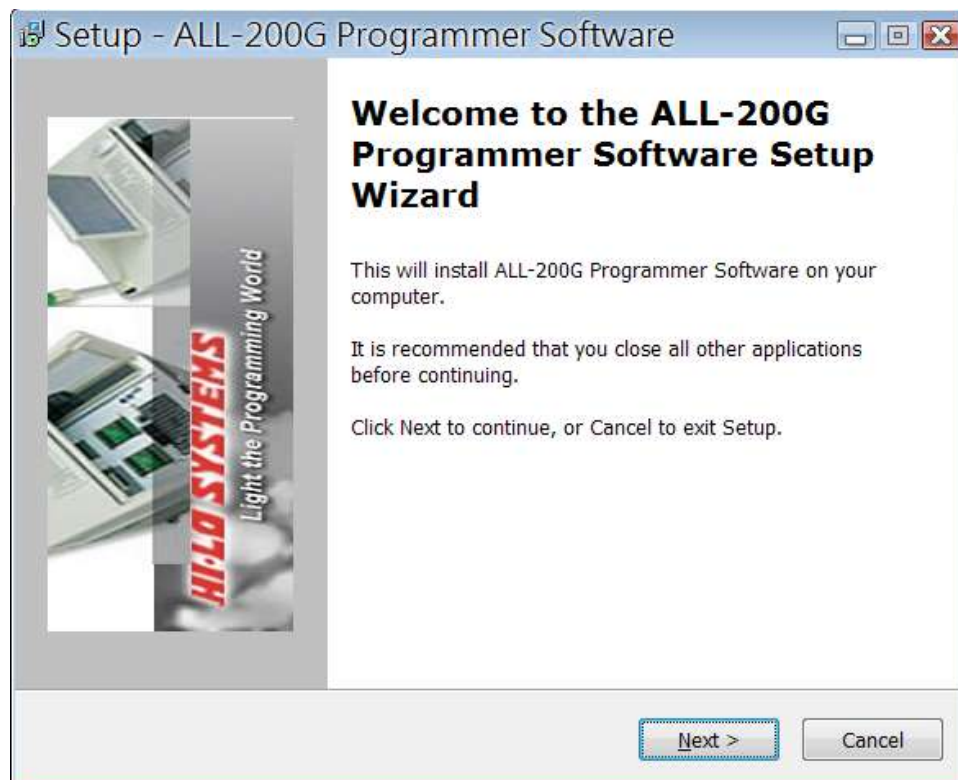
Windows has finished the USB driver installation for ALL-200G. Click “Finish” to complete USB driver installation.



## 2.3 Software

Insert Driver Files CD to CD-ROM drive and it'll run automatically.

But if it doesn't, go to directory of ALL-200G under File Manager to execute the SETUP.EXE file, or run the SETUP.EXE from START menu of WINDOWS and follow all steps accordingly as follows:

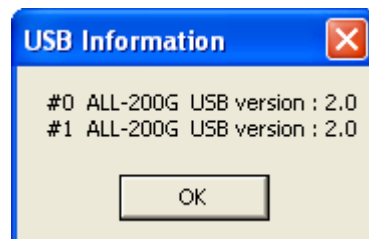


### Check of installed software:

- (1) Check the software installation with File Manager and make sure HACCESS.EXE, individual IC programming driver and Utility files exist under C:\Program Files\ALL-200G directory. HACCESS.EXE is a system file easy for you to select IC Manufacturer, Product Type and the corresponding programming driver.
- (2) When programming driver is executed, software will automatically check the connection of ALL-200G. If software cannot detect ALL-200G, the connection and/or installation might have problem and it might not be possible to access ALL-200G.



- (3) Run "USB Info." option under HACCESS menu to check the connection between ALL-200G and PC.



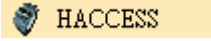
## 3. ALL-200G Basic Operation

### 3.1 Getting Started

We will have a brief description of ALL-200G basic operation, introduce how to access the desired IC programming driver through HACCESS, the main system program. We will also introduce functions of Device, Load, Blank check, and Program by taking SPANSION S29AL004D-TA-01 as an example.

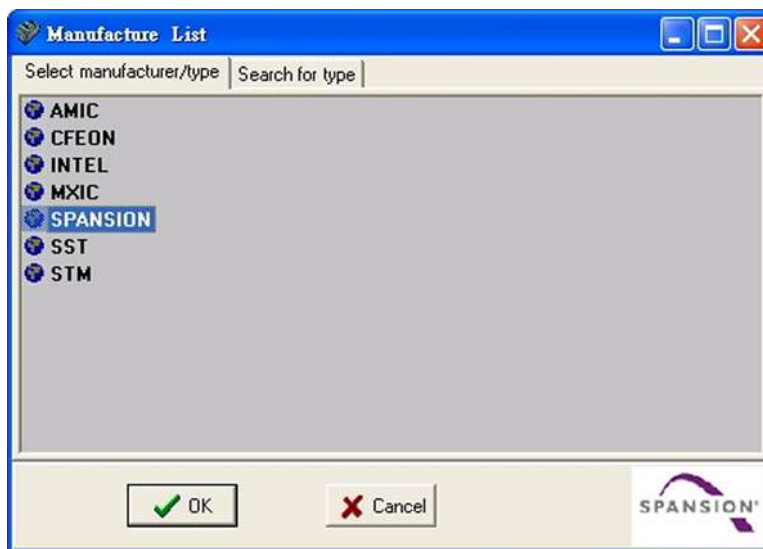
✧ For best view of ALL-200G information displayed, user's screen should have resolution 800x600 pixels or above.

#### 3.1.1 Start with HACCESS

Click the icon of HACCESS  to activate HACCESS.EXE and get the following display on the window.

#### 3.1.2 Entries to Programming Driver

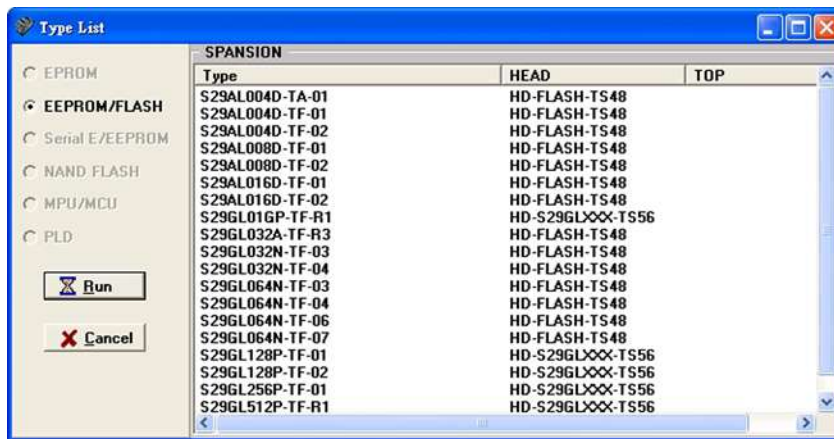
Click “Device” and “Select Device” to display two entries to programming driver, “Select manufacturer/type” and “Search for type” (as the figure below).



(1) Select manufacturer/type

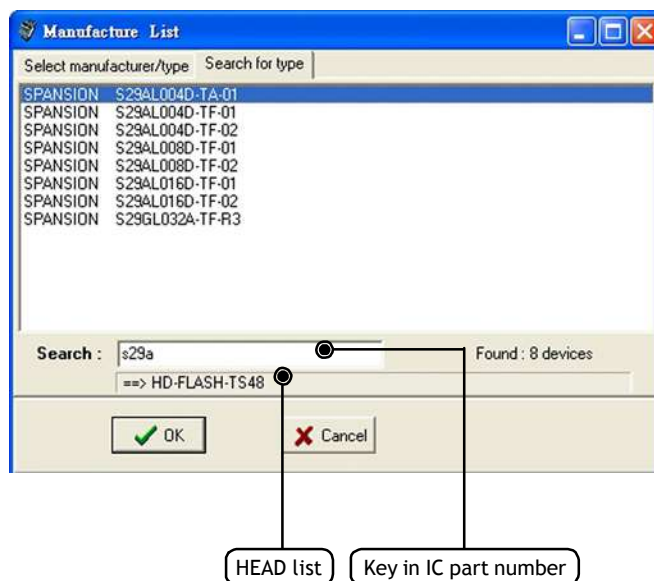
Click “Select manufacturer/type” in “Manufacturer List” to display IC Manufacturer options. Select “SPANSION” and click “OK” to display Type List.

Select “EEPROM/FLASH” in product groups at left side and select “S29AL004D-TA-01” in product types at right side of Type List and then click “Run”.



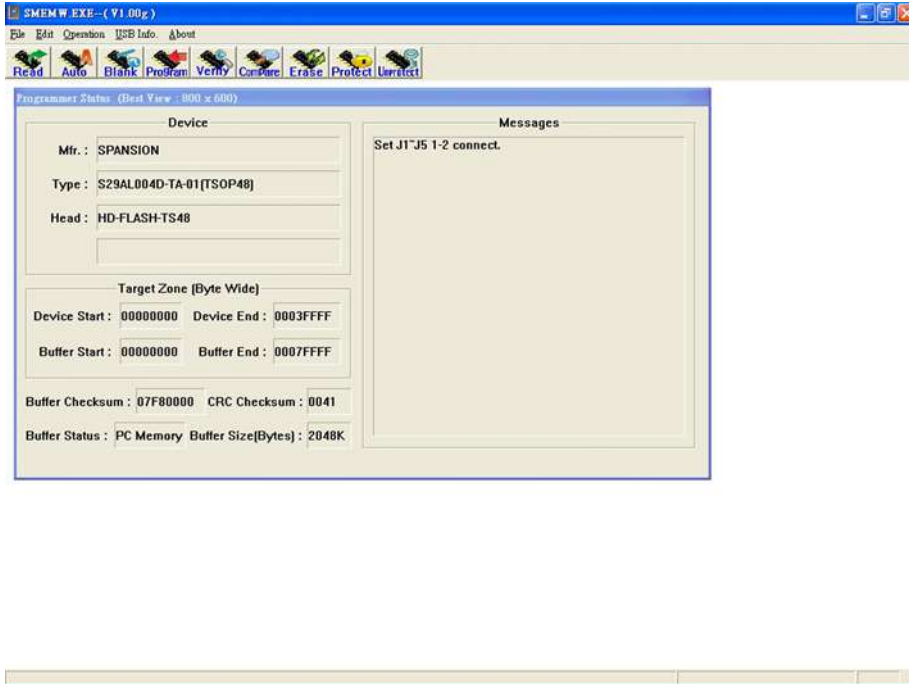
(2) Search for type

Click “Search for type” in “Manufacturer List”, select “S29AL004D- TA-01” and the required HEAD P/N is displayed under the block of Search. Then click “OK” to enter the main menu of driver file.



### 3.1.3 Enter Main Menu of the Driver File

Main Menu of the Driver File contains three major parts, the first row for menu of main functions, the second row for quick function keys, and the rest for dialogue boxes of IC information like IC Manufacturer, Product Type, HEAD needed... etc.

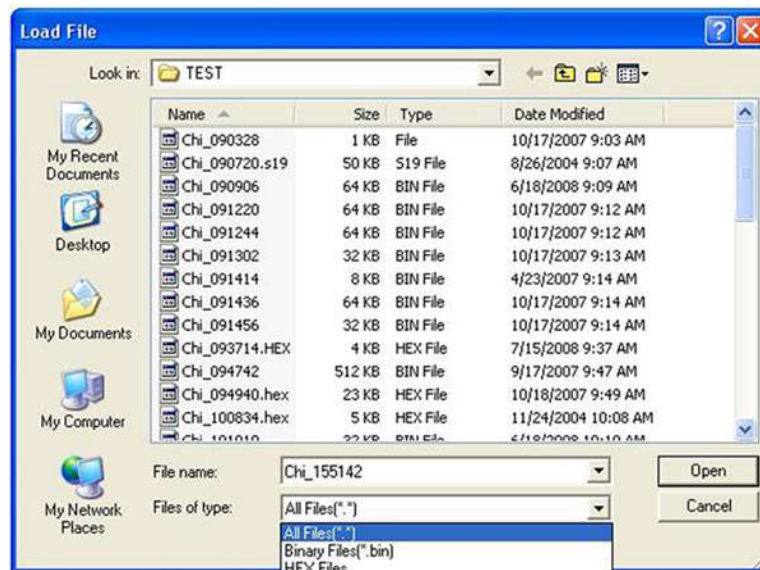


#### Remark:

When reselecting IC manufacturer and product type, the information in dialog box will be updated accordingly and the driver file will be downloaded to ALL-200G Programmer. If the message "File not found" appears, it means the driver file is not available in PC. Then check the attached Driver File CD or visit HI-LO web site at <http://www.hilosystems.com.tw> for S/W download. If a specific HEAD is needed, "File not found" may also indicate the absence of the required HEAD S/W. Try to install the S/W again.

### 3.2 Load File to Programmer buffer

Programming code should be loaded after you select IC Manufacturer and Product Type. In general, programming code is saved in a file in Bin/Hex format. This code needs to be loaded to programmer buffer and then programmed into Blank IC devices. To load file to programmer buffer, click “File” menu, select “Load File to Programmer Buffer” option, following dialogue box will appear:



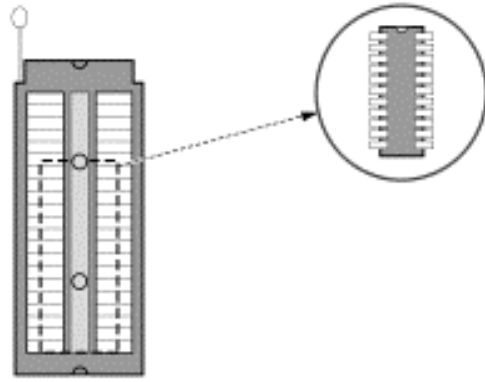
Function of file loading is similar to that under Windows environment. Enter the file name to be downloaded and click “Open”, the named file will be loaded to programmer buffer. **Note: Disk drive and file path must be correct.** Select and click the correct drive and folder that the file located to. If it cannot be operated by mouse, apply <TAB>, <UP>, <DOWN>, and <ENTER> keys for selecting and confirming.

### 3.3 Read Contents from Master IC to Buffer

When programming code is stored in a Master IC, insert the Master IC onto socket of HEAD # 1, click “Read” button on screen or press “R” key on keyboard to read programming code from Master IC to programmer buffer.



When inserting Master IC onto socket of HEAD # 1, make sure Pin 1 orientation identical with the indicative positioning diagram by socket. If IC’s pin count is less than the socket’s, please put IC onto socket as the diagram indicated overleaf.



✧ Caution! Incorrect IC positioning might cause IC damage or be programmed to an unknown state.



### 3.4 Program Buffer Contents to IC

Insert ICs to be programmed onto sockets, click “Auto” button on screen or press “A” key on keyboard, following dialogue box will appear.

Site #	Status :	1	2	3	4	5	6	7	8	OK	NG
0										8	8
1										8	8
2											
3											
4											
5											
6											
7											
Total :										16	16

Click “Run” button on screen or “Y” key on keyboard or “YES” on programmer to start programming buffer contents to blank ICs.

After programming, system will automatically verify data read from programmed ICs with data in programmer buffer. If both data match, the corresponding LED by the programming unit will be lit indicating successful programming; if it’s not lit, the programming fails.

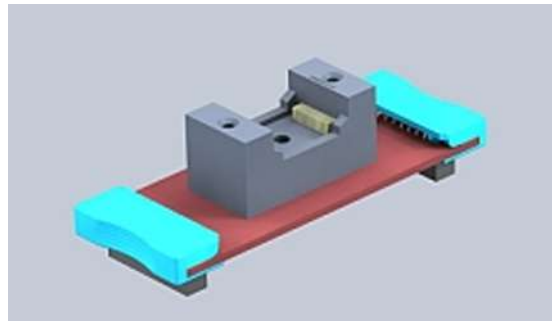
For next IC programming cycle, the “BUSY” LED must be off. Then insert blank ICs onto sockets, click “Run” button on screen or “Y” on keyboard or “YES” on programmer to continue programming.

✧ Click “Close” button on screen or <ESC> key on keyboard to go back to main menu.

### 3.5 Optional

For the sake of supporting various ICs nowadays, HEADs are designed for most kinds of IC packages such as PLCC, SOP, TSOP, QFP, PGA, ... etc.

Each HEAD has 40/48 gold-plated pins in various layout which can be inserted onto 48 pins ZIF socket on the Programming Module. Each HEAD has one (or more) programming file(s) that needs to be loaded to the same directory as HACCESS.



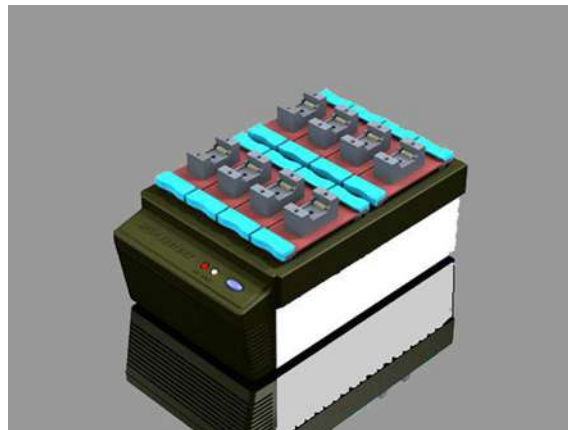
### 3.5.1 HEAD

- **S/W Installation:**

Copy the HEAD file(s) attached to the same directory as that for HACCESS file(s).

- **H/W Installation:**

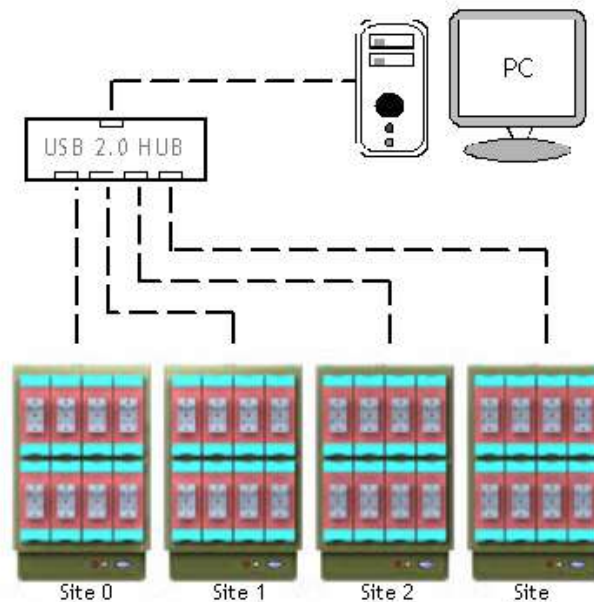
Insert the required HEADs onto the programmer; at most 8 HEADs can be inserted. See the figure below:



## 4. Multiple ALL-200G Installation

USB interface provides functions like “Plug-and-Play”, auto detection, high expansibility, and high transmission rate (480 Mb/s for 2.0 version). By using these functions, ALL-200G can be installed up to 8 sets and running simultaneously (Site No. 0~7).

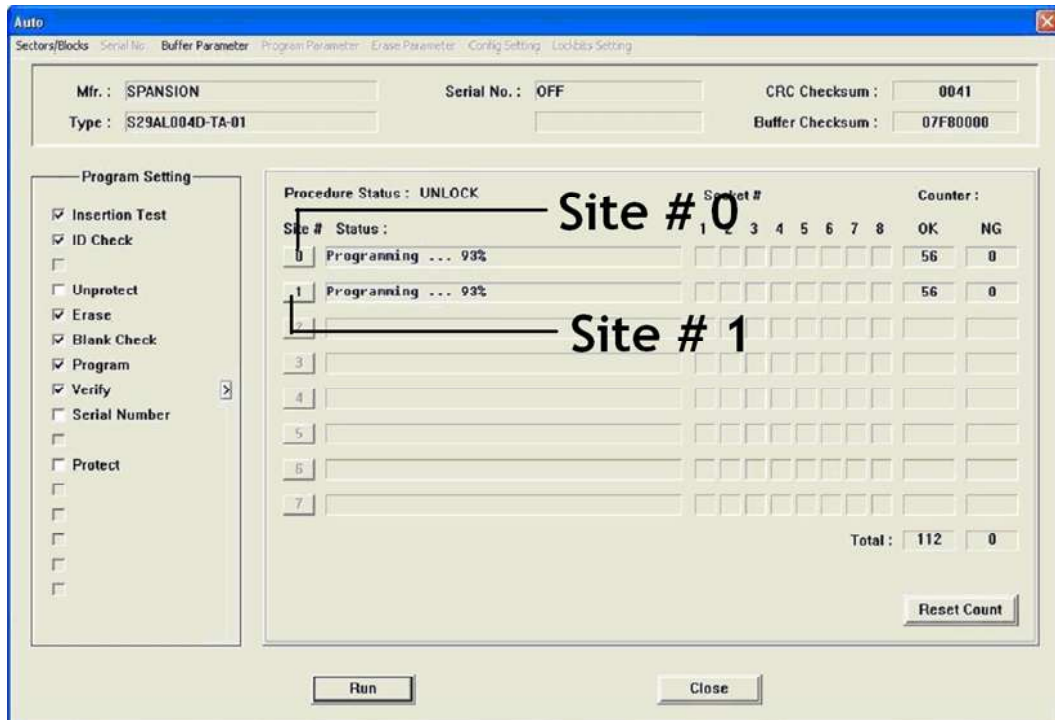
After single ALL-200G installation, more ALL-200G Programmings can be installed through either extra USB ports of your PC or USB 2.0 Hub connected as the following diagram. But USB Hubs not admitted by HI-LO are not suggested.



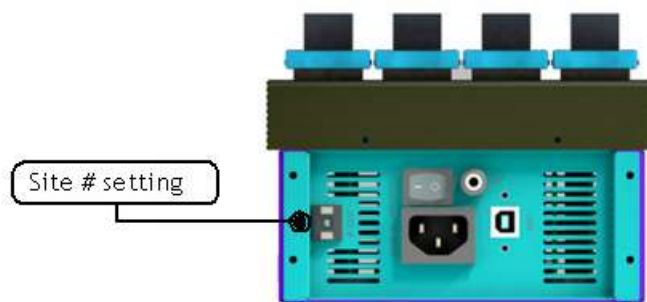
Multiple ALL-200G installations through USB 2.0 Hub

## 4.1 Multiple ALL-200G Operation

Multiple ALL-200G operations can increase programming throughput. With Multi-Thread methodology, each programmer can operate independently. The diagram below shows two ALL-200G Programmers are programming (Site #0 and Site #1).



Site # setting switch is located on the rear panel of ALL-200G Programmer. Valid Site # for Multiple ALL-200G setup is 0-7 and cannot have the same Site # in whole system.



When running ALL-200G programming software, system will automatically detect and load necessary driver S/W and also download programming code to ALL-200G via USB port. Operation of multiple ALL-200G sets is basically the same as that of single ALL-200G. There are two operation modes for multiple ALL-200G programming, synchronous operation and asynchronous operation; choose either of them after entering programming mode on screen.

■ **Synchronous operation:**

Run programming on all the ALL-200G at the same time. Put ICs onto sockets of each ALL-200G, press “Y” key on PC keyboard. ALL-200G will start programming simultaneously.

■ **Asynchronous operation:**

Run programming on each ALL-200G one after another. Put ICs onto sockets of the 1st ALL-200G, press <YES> on the programmer to start programming; then put other ICs onto sockets of the 2nd ALL-200G, press <YES> on the programmer to start programming; then the 3rd one ... and so on.

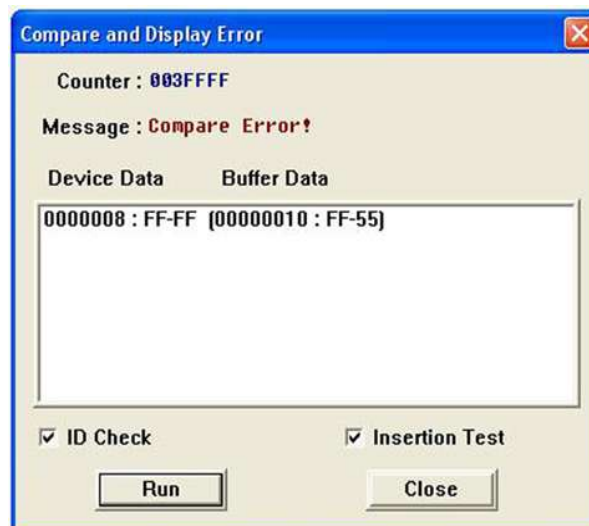


## 4.2 Read/Compare on Multiple ALL-200G

To perform Read/Compare function under Multiple ALL-200G setup, only the IC on socket # 1 of the minimum Site # (i.e. the 1st ALL-200G) is valid. This means that only IC on the 1st socket of the 1st ALL-200G can be read/Compared under Multiple ALL-200G setup <pls see 5.6.3 (1) for details of Read function>.



Read Dialogue Box

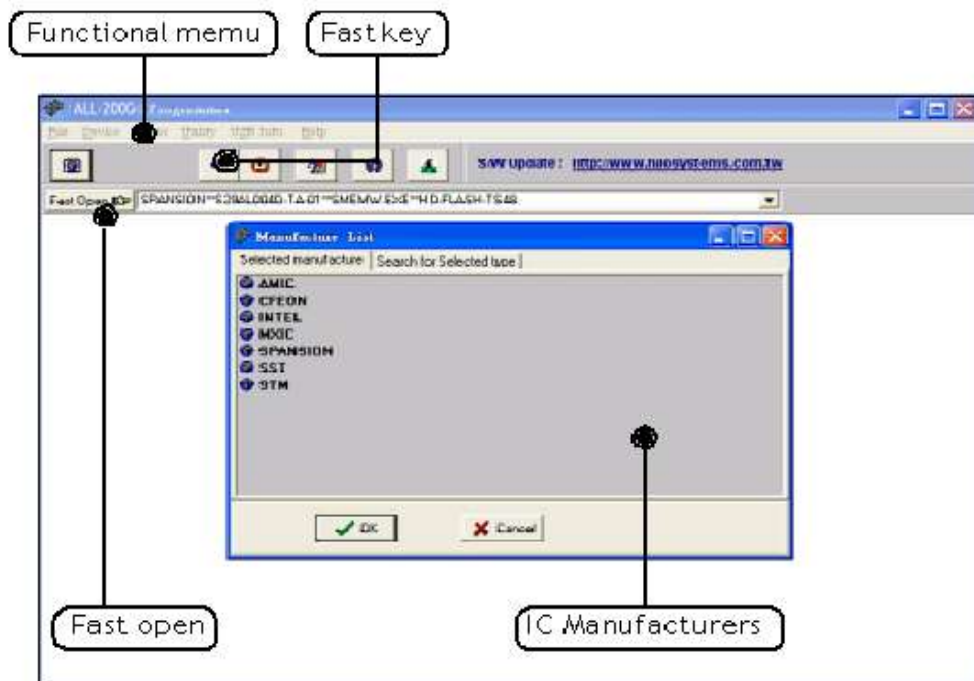


Compare Dialogue Box

## 5. ALL-200G Software

### 5.1 HACCESS User

System Software “HACCESS” is an interface guiding user to locate the driver software for product type to be programmed. HACCESS also provides File Management Utilities as well as Data Base of programmable IC products.



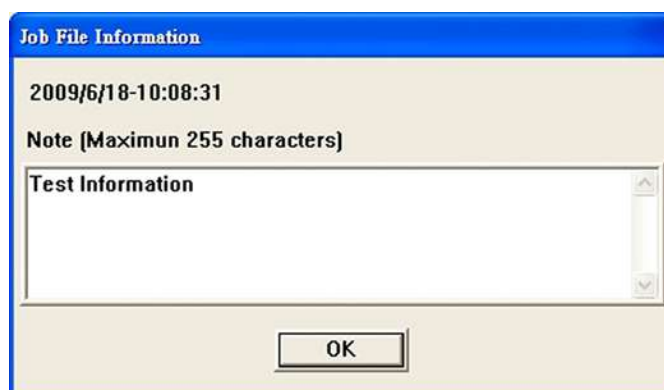
### 5.2 File

Under the “File” menu of HACCESS contains three special commands -- Save Programmer Configuration, Load Programmer Configuration and Enable Job Function.



### 5.2.1 Save Programmer Configuration

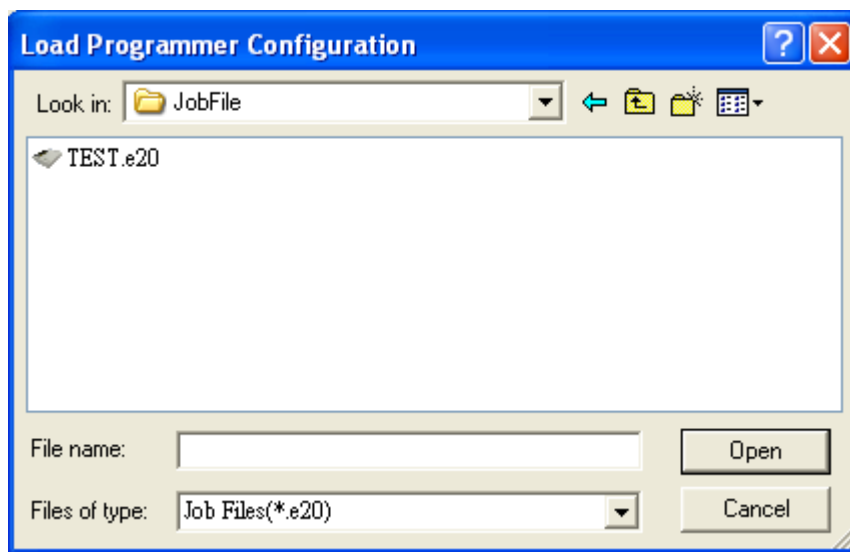
After all programming options are set, select “Save Programmer Configuration” to save all the info as a \*.g20 file (Job File), including Target Zone settings (Device Start, Device End, Buffer Start, and Buffer End), Mfr. and Type selections, Load File setting, Auto function settings and Other programming settings.



## 5.2.2 Load Programmer Configuration

After saving the programmer configuration, you may then use this command to load a desired configuration data file (Job file) back to the software driver whenever needed to reinstall all the programming data information (note: not all software contains Target Zone data) as well as “Auto” function with settings saved previously.

After loading Programmer Configuration, you only need to select “Auto” function to begin programming.



### 5.2.3 Enable Job Function

This command allows users to enable or disable the Job Function selection.

After Job Function is enabled, select a configuration data file (Job file) and click “Open” to download all the configuration setting previously saved back to programmer.

You can also install a short cut in the Startup directory of Windows so that the driver will automatically start when your PC is activated and the operation system runs.



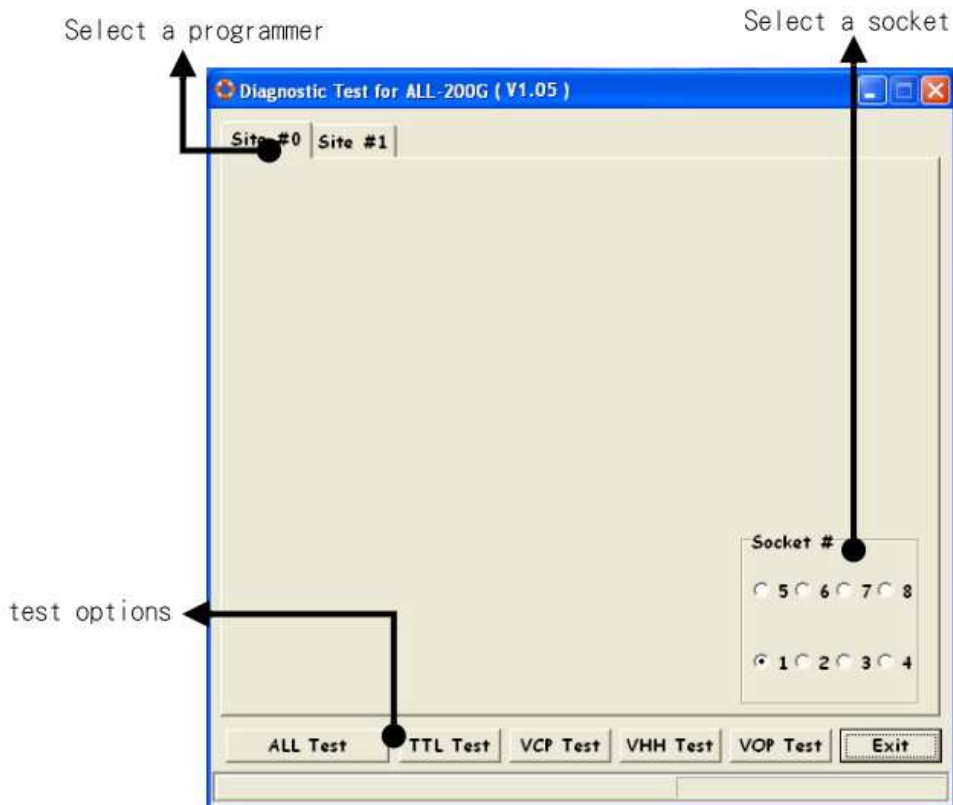
### 5.3 Diagnostic Tester

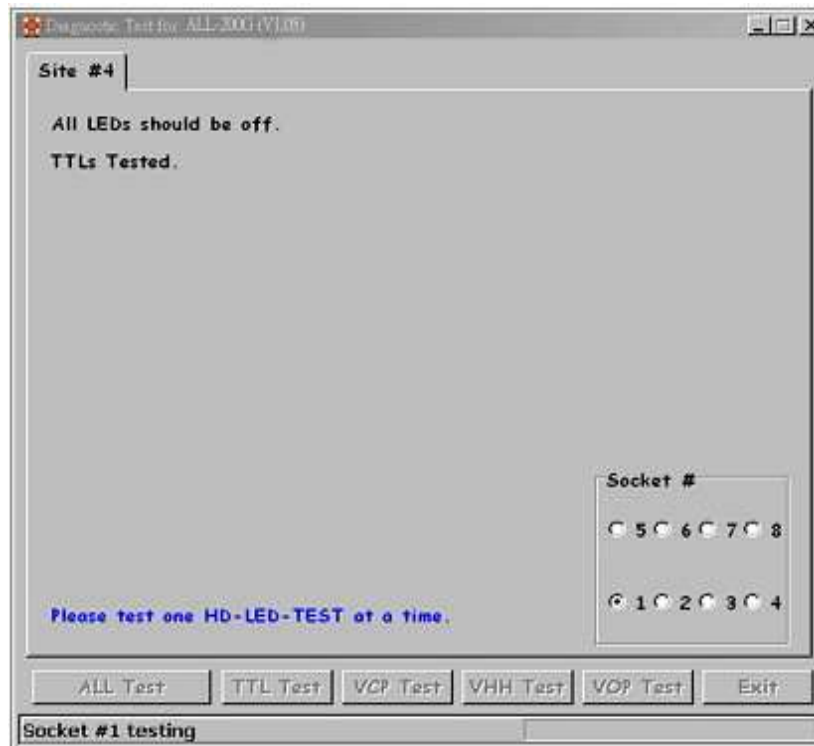
You can run “Diagnostic Tester” with HD-LED-TEST board to check each socket on ALL-200G.

#### 5.3.1 HD-LED-TEST for Diagnostic Tester

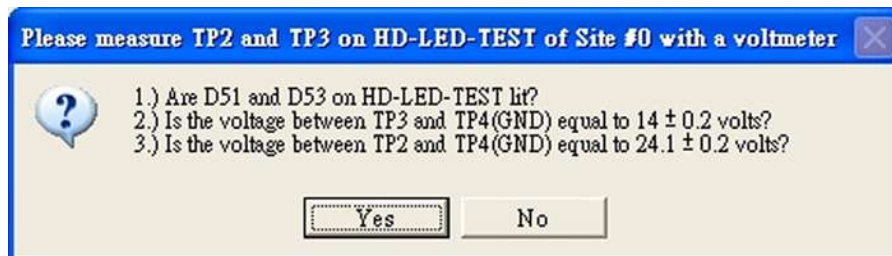
Insert HD-LED-TEST board onto a socket and run Diagnostic Tester (see the figure below). The diagnostic includes ALL Test, TTL Test, BVCP Test, VHH Test, VOP Test and voltage measurement.

- ✧ Be sure to remove any HEAD from programmer before running Diagnostic Tester; otherwise HEADs or IC on HEADs might be damaged.





- ✧ After TTL Test, the following message appears for voltage measurement; please check accordingly.

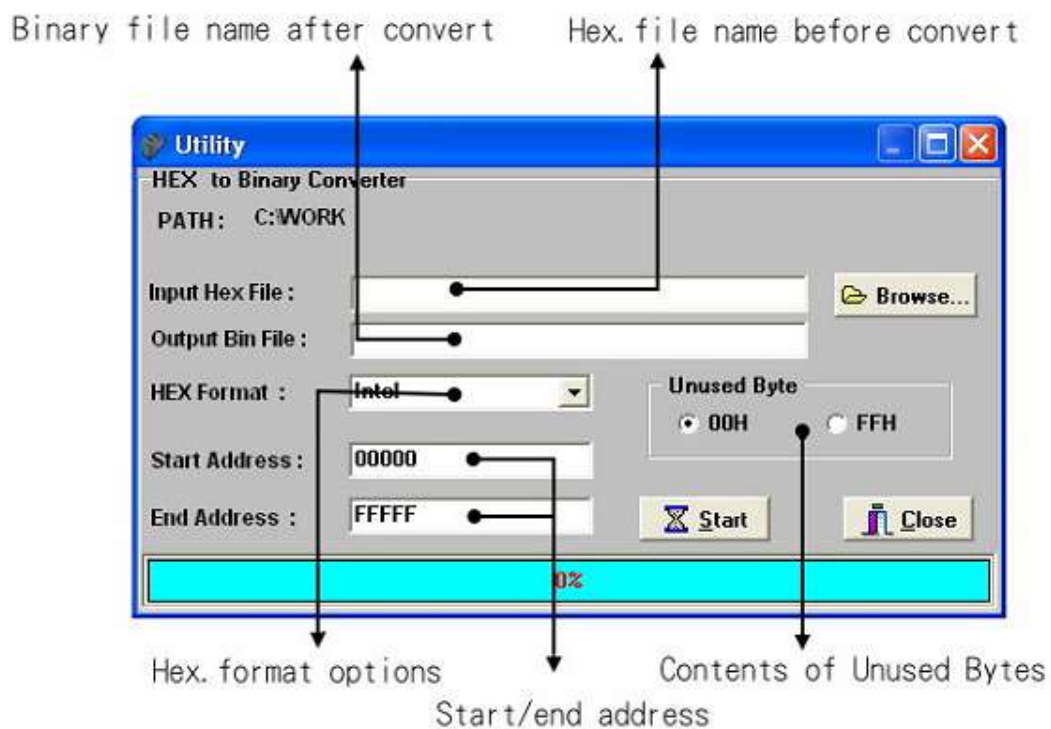


## 5.4 Utility

File Management Utilities include Hex to Bin Converter, Bin to Hex Converter, 2-way splitter, 4-way splitter, 2-way shffler, and 4-way shuffler, ...etc.

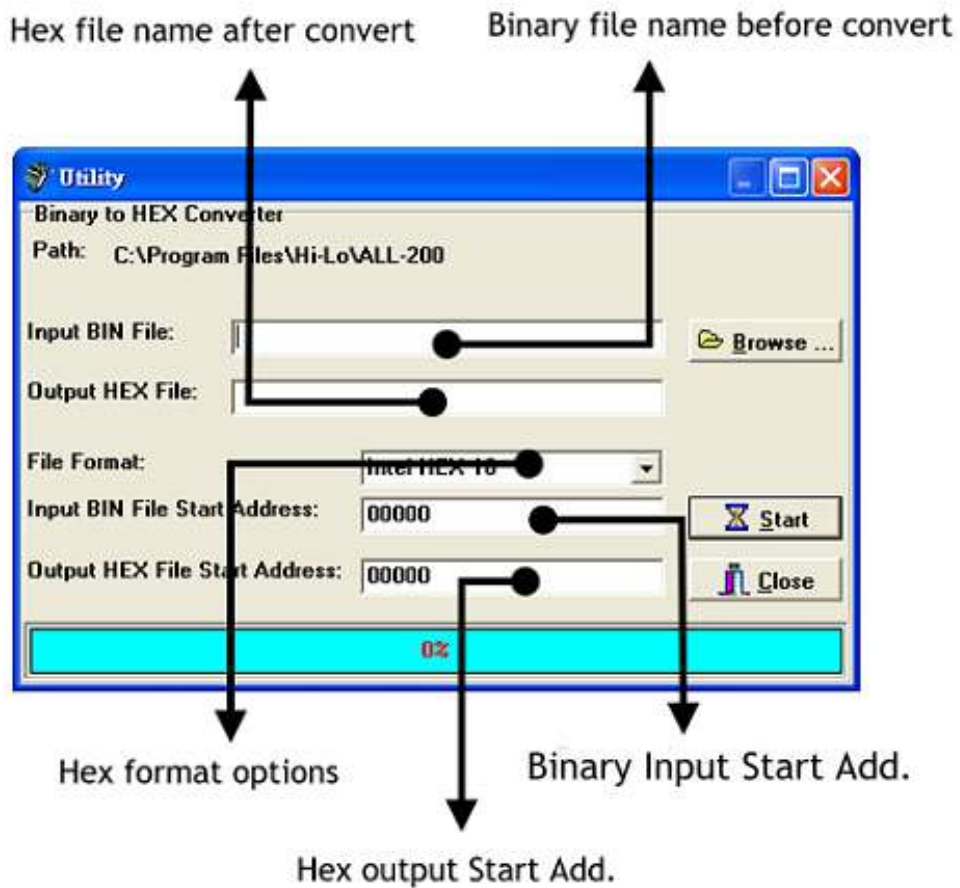
### 5.4.1 Hex to Bin Converter

Convert data from Hex format to Binary format for programmer Read/Write.



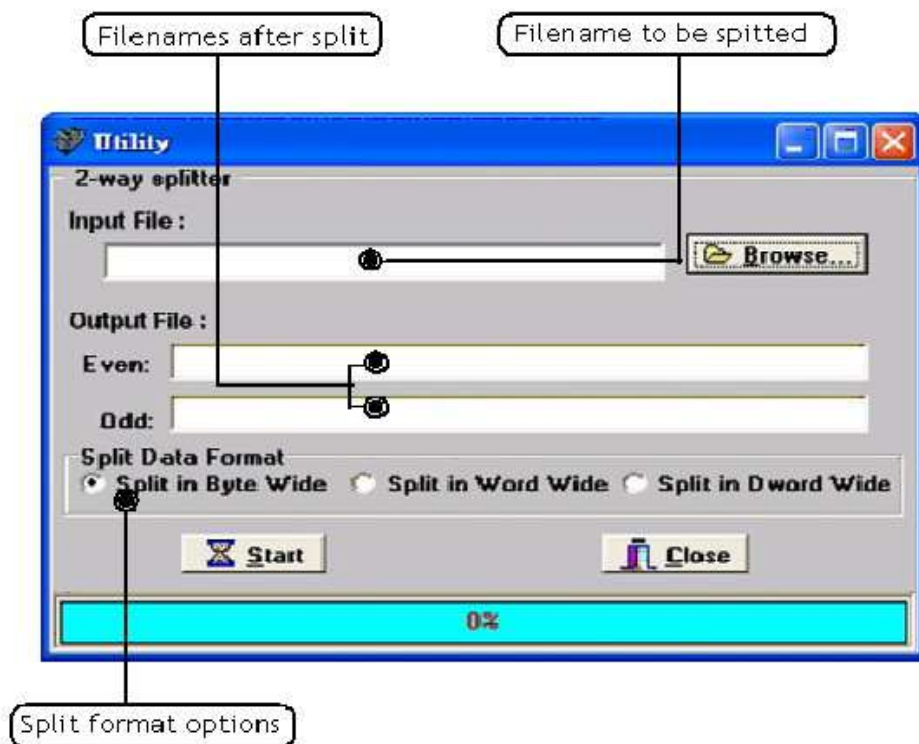
## 5.4.2 Bin to Hex Converter

Convert data from Binary format to Hex format.



### 5.4.3 2-Way Splitter

Split one file into two output files. One file contains odd-byte data of the original file and the other file contains even-byte data of the original file.



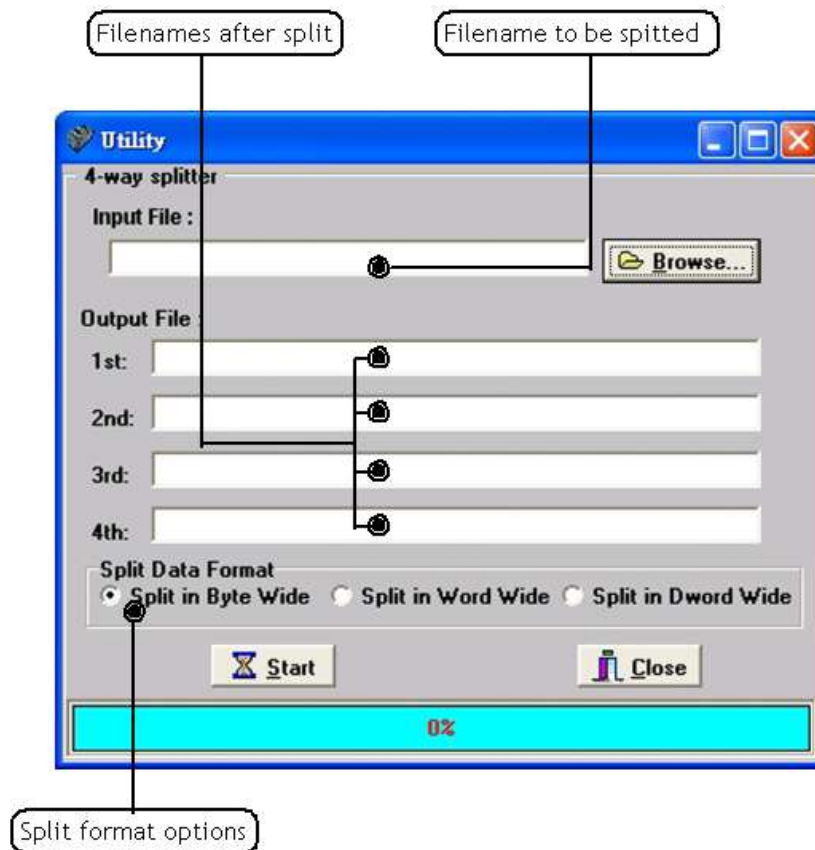
#### Split Data Format:

Normally the split data is in Byte Wide; however, user can choose Word Wide (two bytes) or Double Word Wide (four bytes) as unit of data split.



#### 5.4.4 4-Way Splitter

Split one file into four output files. The 1st file contains the 1st byte of every 4-byte data segment of the original file. The 2nd, 3rd, and 4th file contains the 2nd, 3rd, and 4th byte of every 4-byte data segment of the original file.

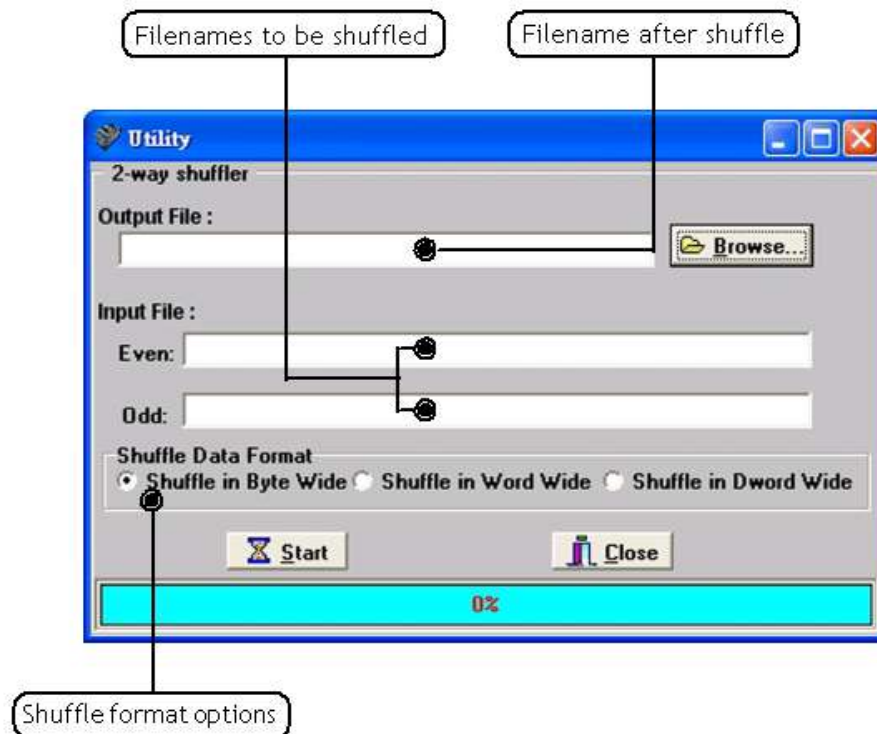


#### Split Data Format:

Normally the split data is in Byte Wide; however, user can choose Word Wide (two bytes) or Double Word Wide (four bytes) as unit of data split.

### 5.4.5 2-Way Shuffler

Combine two files into one. Insert data of Even file into even byte position of the combined file and insert data of Odd file into odd byte position of the combined file.

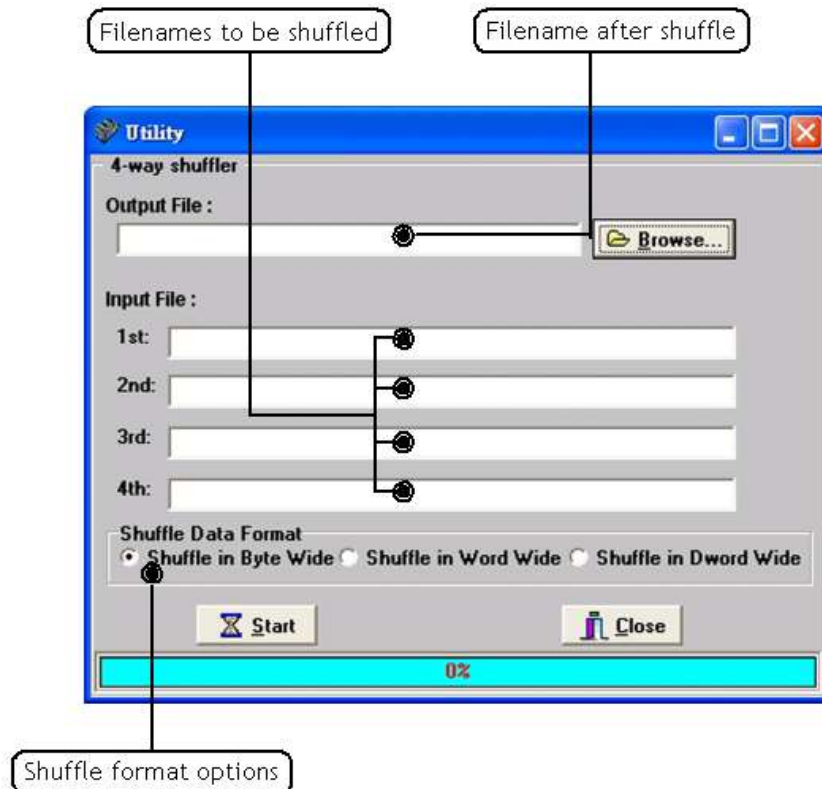


#### Shuffle Data Format:

Normally the shuffled data is in Byte Wide; however, user can choose Word Wide (two bytes) or Double Word Wide (four bytes) as unit of data shuffle.

### 5.4.6 4-Way Shuffler

Combine four files into one. Insert data of 1st file into the 1st byte of every 4-byte data segment of the combined file, insert data of 2nd, 3rd, and 4th file into the 2nd, 3rd, and 4th byte of every 4-byte data segment

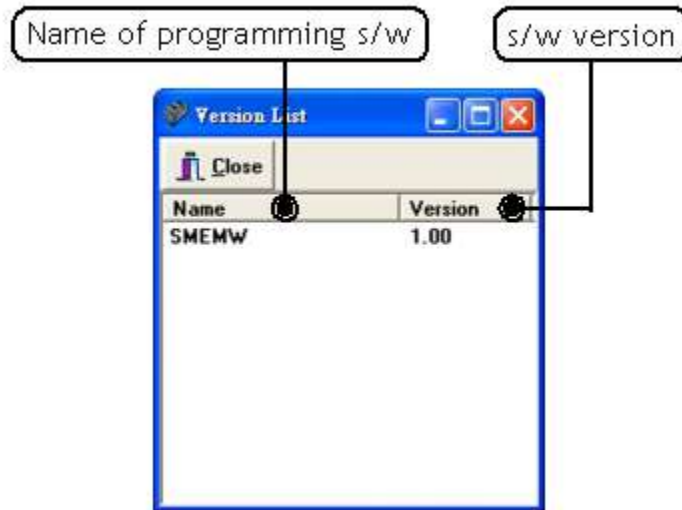


#### Shuffle Data Format:

Normally the shuffled data is in Byte Wide; however, user can choose Word Wide (two bytes) or Double Word Wide (four bytes) as unit of data shuffle.

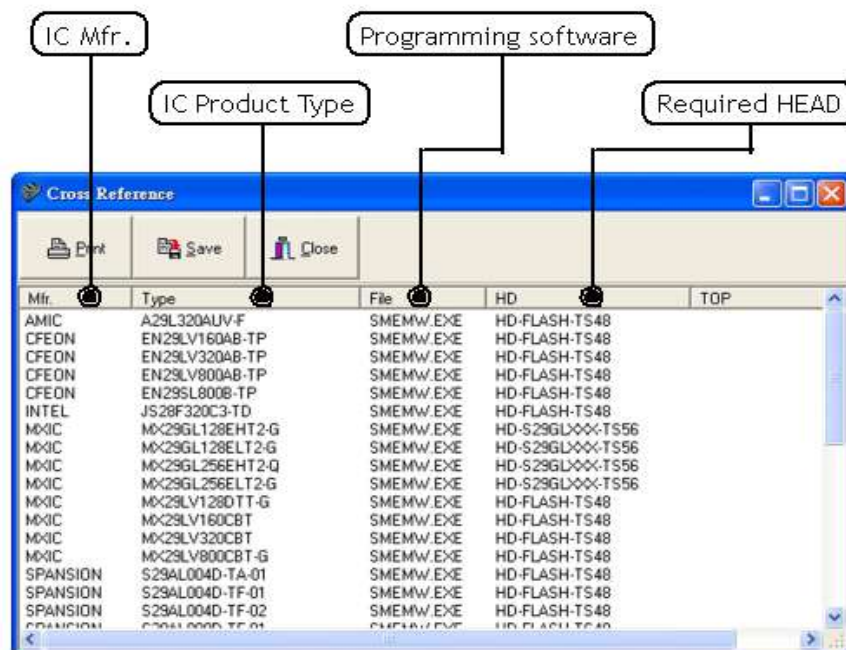
### 5.4.7 Version List

List version number of current programming software



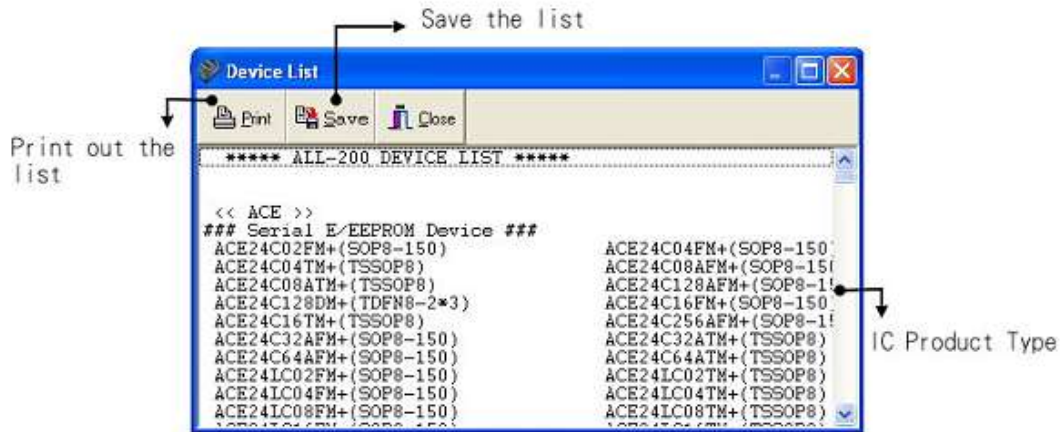
### 5.4.8 Cross Reference

List the Cross Reference of IC Mfr., Product Type, and the corresponding programming software.



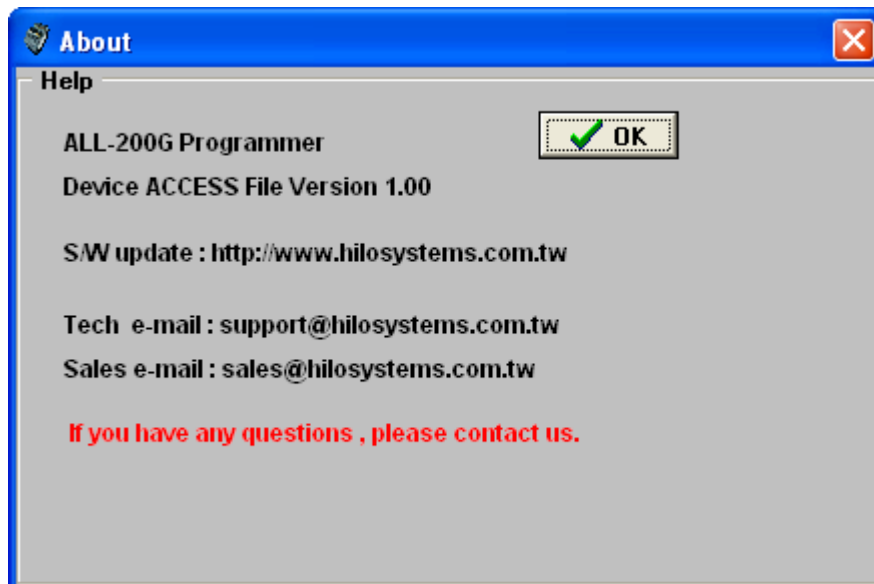
### 5.4.9 Device List

List all device types that are programmable at current version.



### 5.4.10 About

Provide information of current HACCESS file and contact addresses if any service needed.



## 5.5 Protect Mode

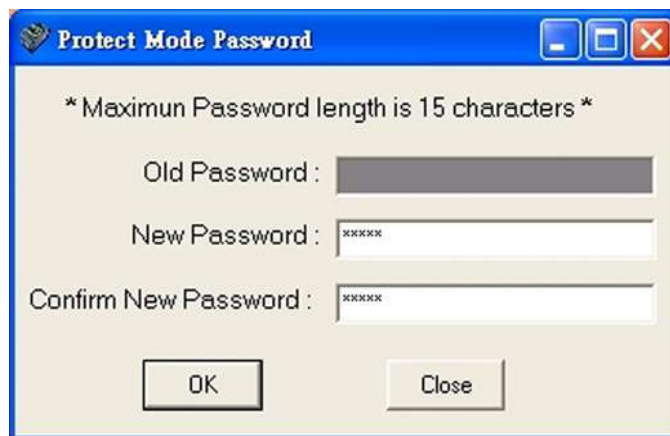
Protect Mode provides control of software access.



### 5.5.1 Protect Mode Password

For the first time to run Protect Mode, you need to key in new password and confirm again; then click "OK" to enter Protect Mode Option.

To change the password or re-starting Protect Mode, you need to key in the original password, and then new password and confirm again.

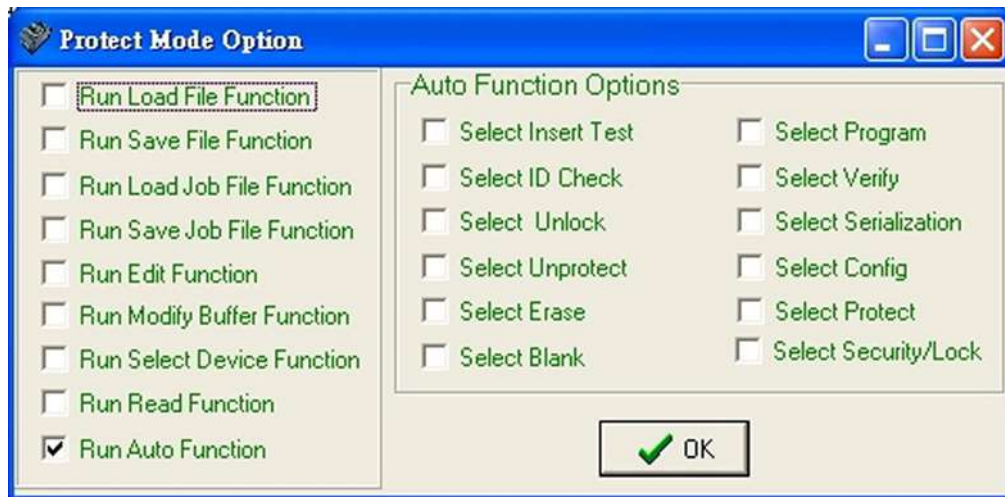


To cancel Protect Mode, the password is also essential.



### 5.5.2 Protect Mode Option

After s/w enters Protected Mode, a functional menu is available for user to select functions to run on the left and then select options to execute on the right.



### 5.5.3 Reset Protect Mode Option

When Protect Mode Option is reset, the following message will pop up. If you click "Yes", all the settings will be changed back to the original default settings.



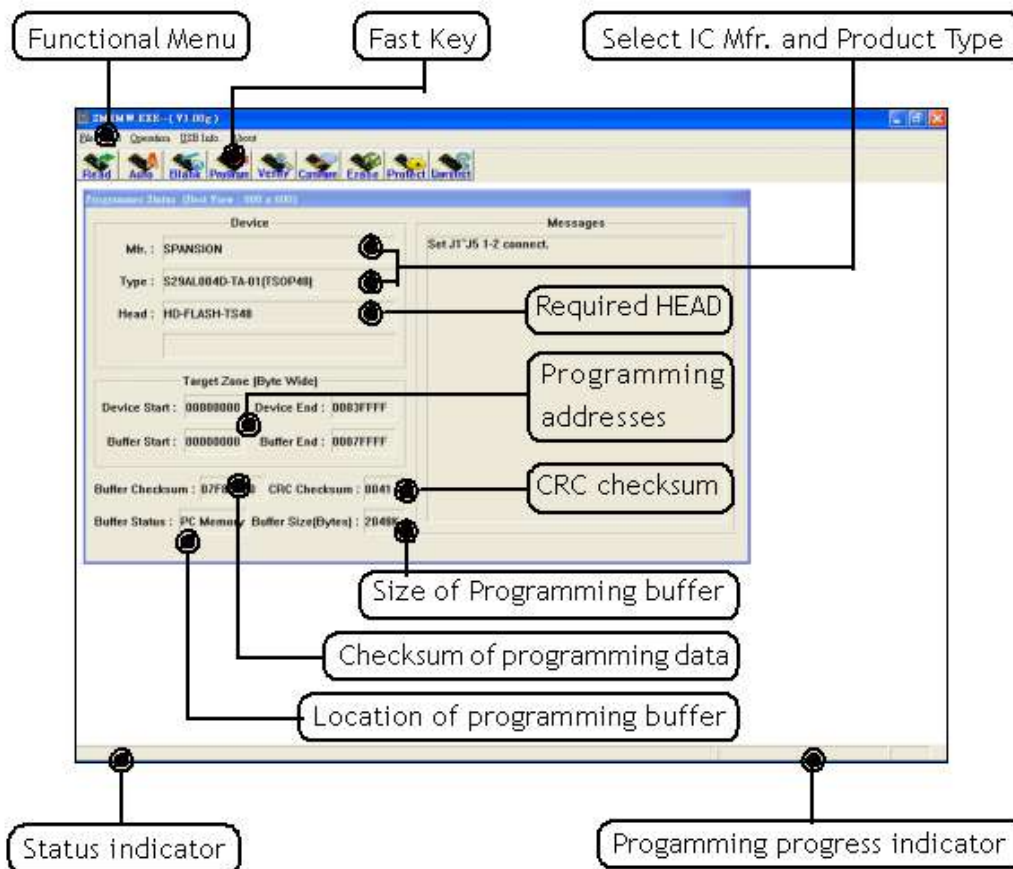


## 5.6 Programming Data / Function

Programming software includes loading of programming driver, utilities of data management, and control of programming function/operation.

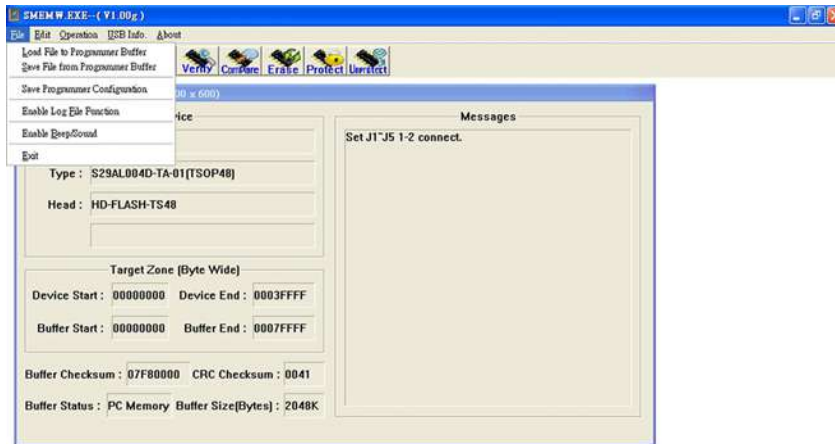
Each programmable IC needs correct functional setup before programming.

User needs to refer IC data sheet and/or application software for proper setup of programming function. See description below by taking SPANSION S29AL004D-TA-01 device as an example.

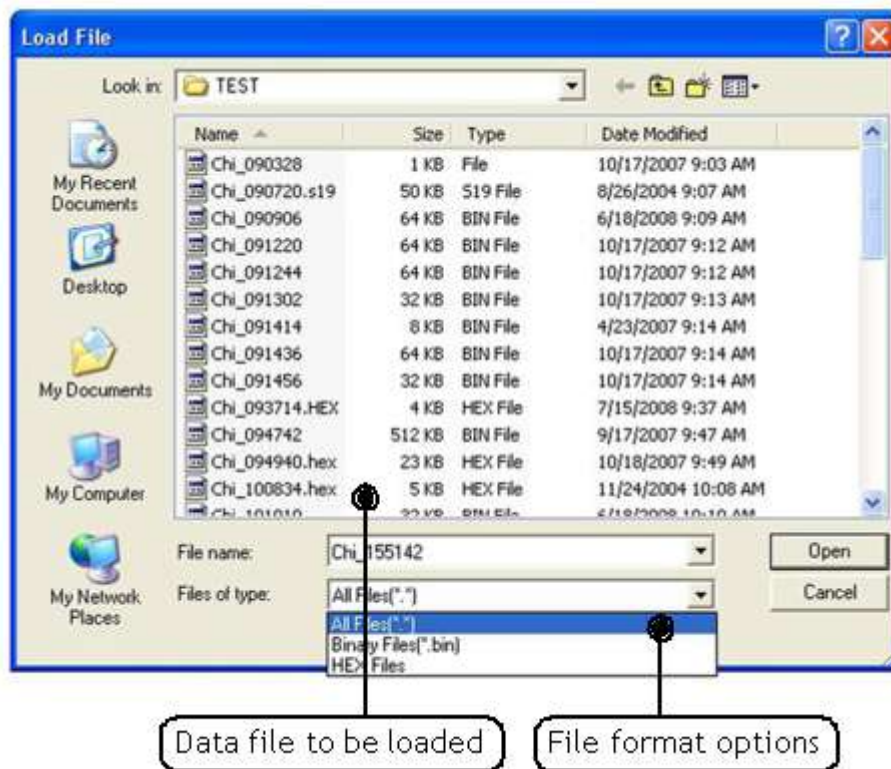


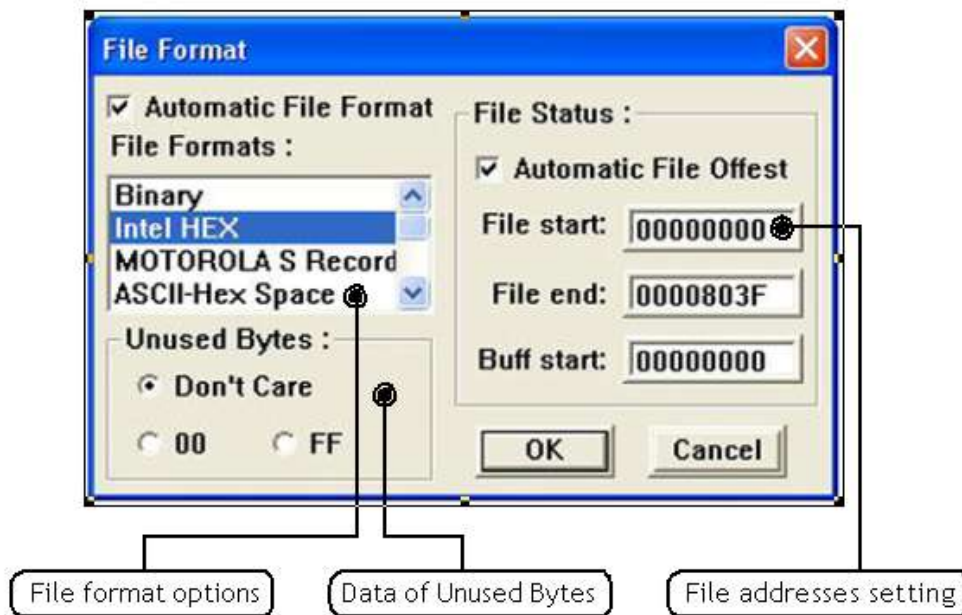
## 5.6.1 File

Load/Save of data file and configuration file. See dialogue boxes below.



(1) Click “Load File to Programmer Buffer” in File menu and the following dialogue box will show up.

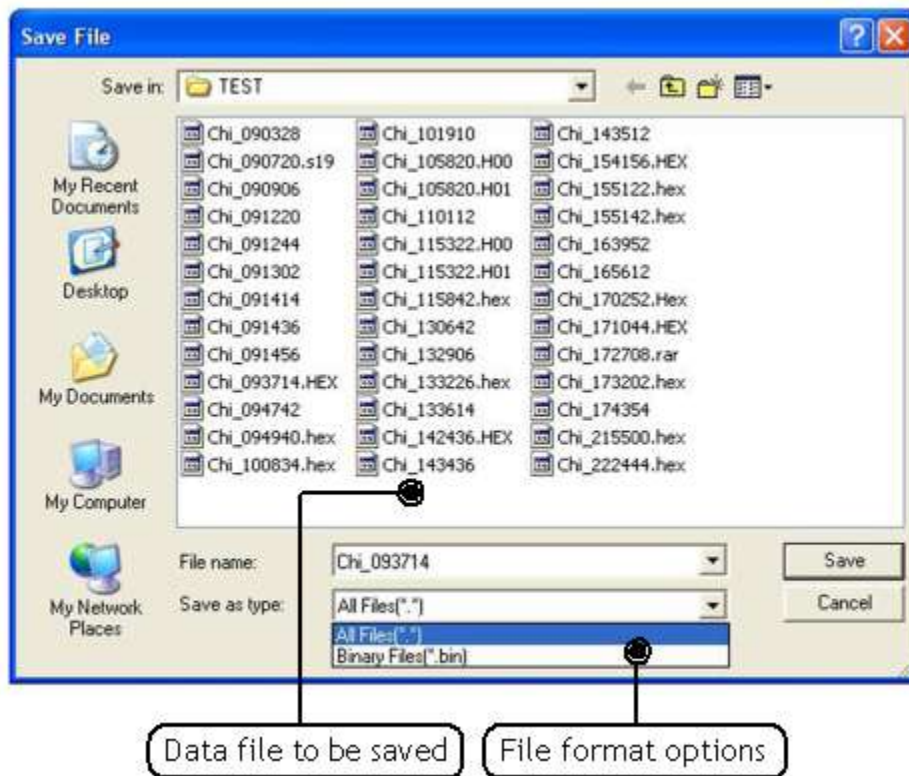




- **Automatic File Format:**  
Enable this option if you are not sure the format of data file and a proper file format will be selected automatically.
- **Automatic File Offset:**  
Enable this option if you are not sure the offset of data file and the File Start will be automatically adjusted.
- **Unused Bytes:**  
  - [Don't Care]: Keep the data as it was in unused bytes of buffer memory.
  - [FF]: Write "FF" to unused bytes of buffer memory.
  - [00]: Write "00" to unused bytes of buffer memory.

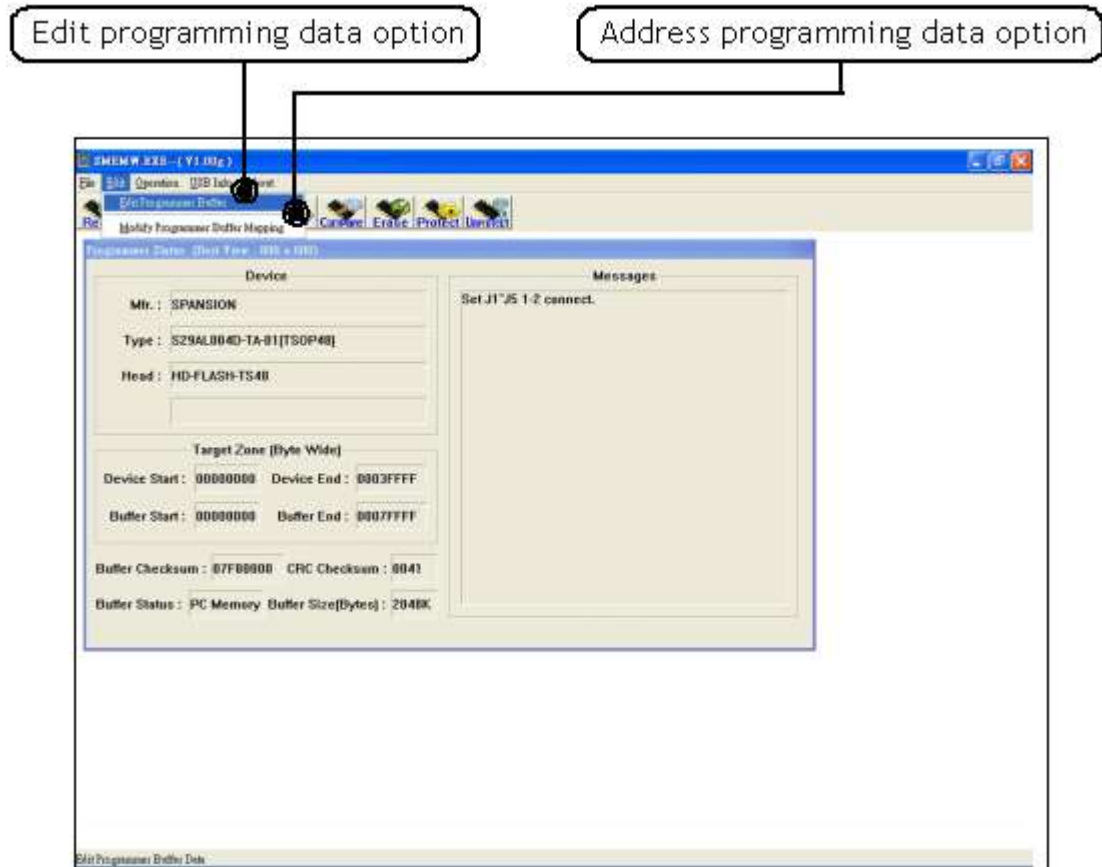
(2) Click “Save File from Programmer Buffer” in File menu to save a data file from programmer buffer as a Binary file; then the following dialogue box will show up.

But to save data files as other formats, please run “Utility” in main menu of HACCESS (see details in 5.4).



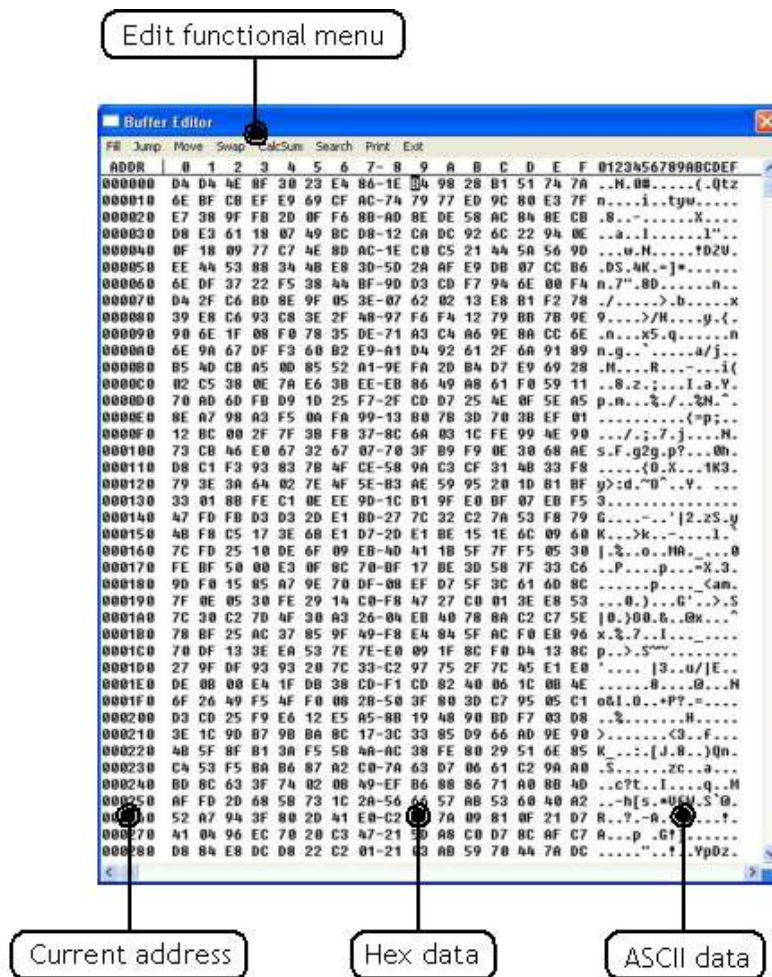
## 5.6.2 Edit

Includes programming data editing and addressing. See dialogue box below.



(1) Edit data:

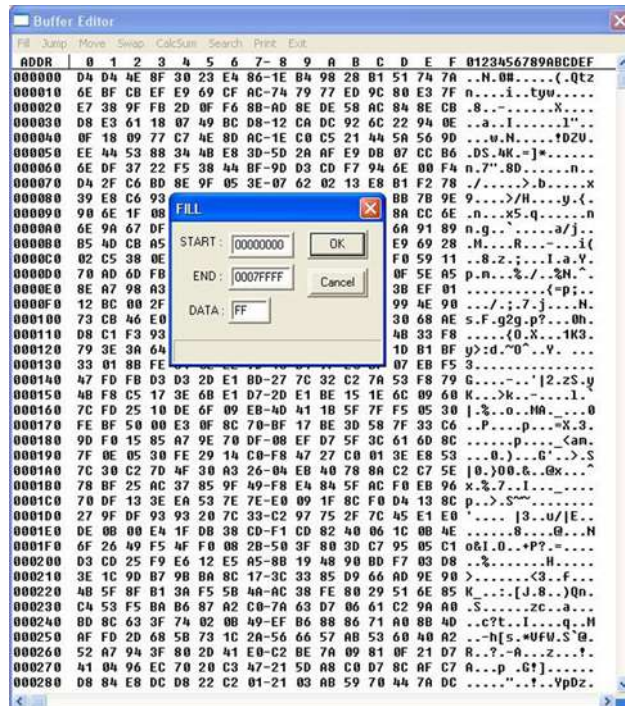
Provides functions such as Fill, Jump, Swap, Calc, SUM, Search,...etc. for users to edit programming data in Hex and ASC II expressions.





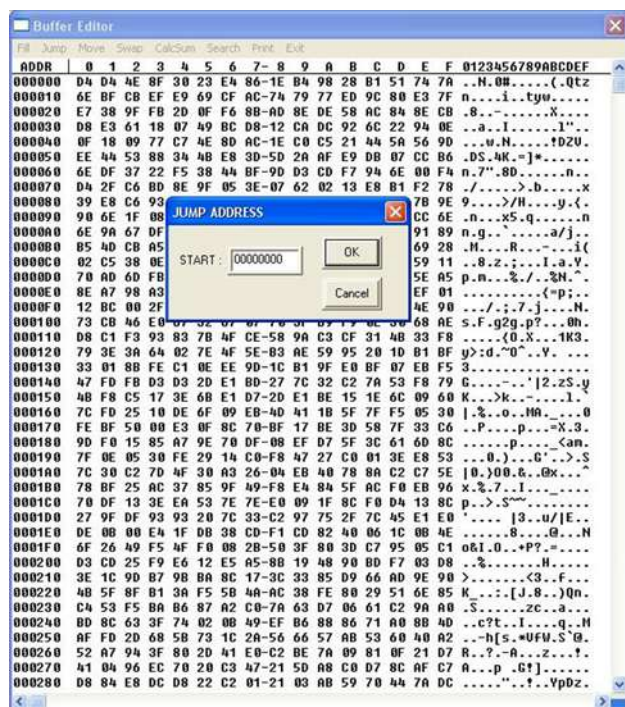
### (2) Fill:

Specify start and end addresses and data to be filled in; then click “OK” and data will be filled into the specified area.



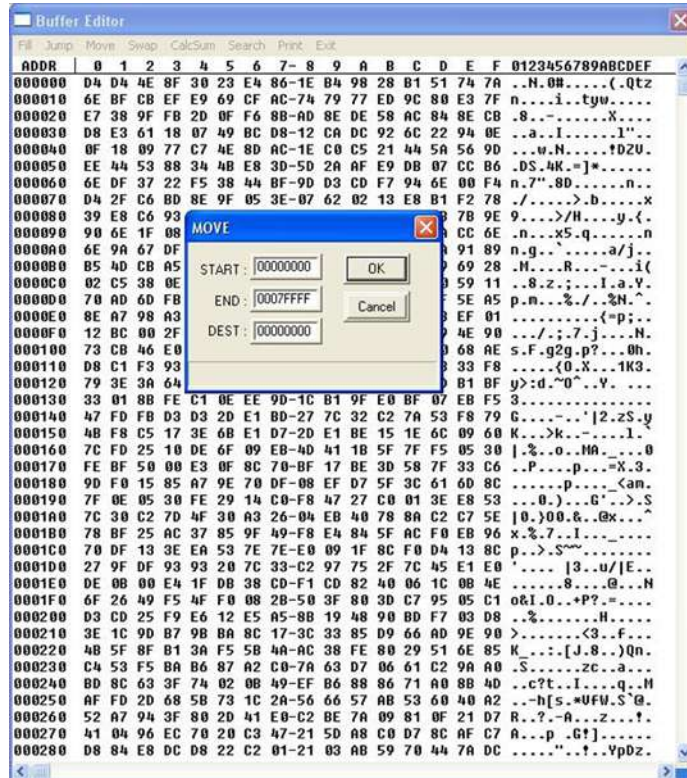
### (3) Jump:

Specify the start address to jump to, then click “OK”, system will jump to the specified address with data displayed.



#### (4) Move:

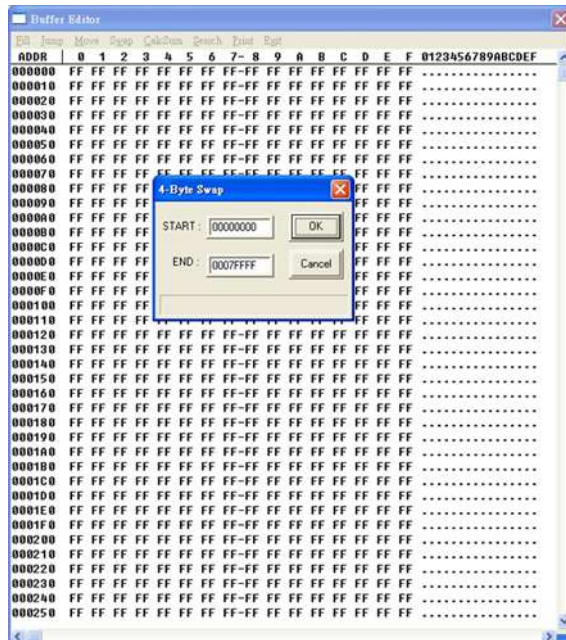
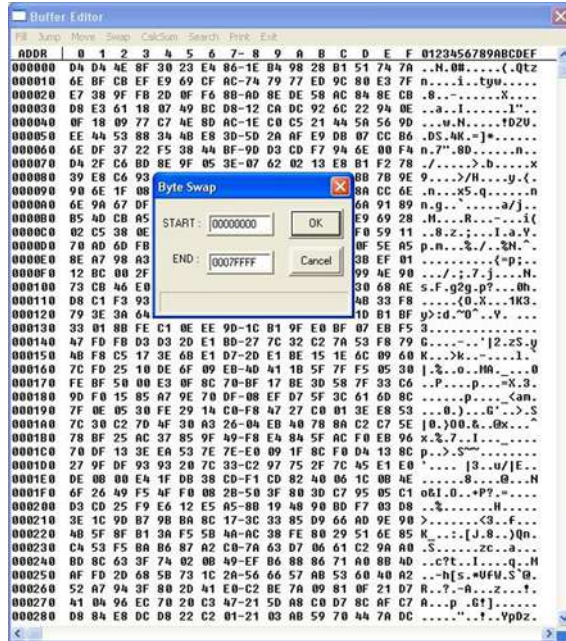
Specify start and end addresses in which data to be moved, and also specify the destination address to move to. Then click “OK”, and system will move data between specified addresses to the destination area.





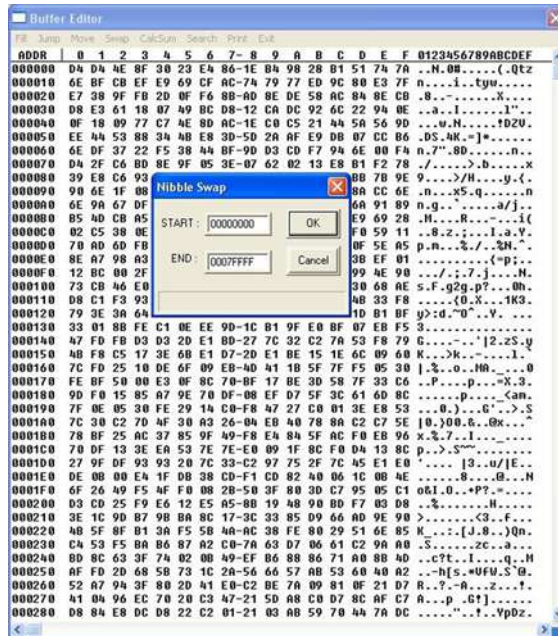
(5) Swap - Byte and 4 Bytes:

Specify start and end addresses in which data to be byte-swapped or 4-byte-swapped; then click “OK” to start swapping.



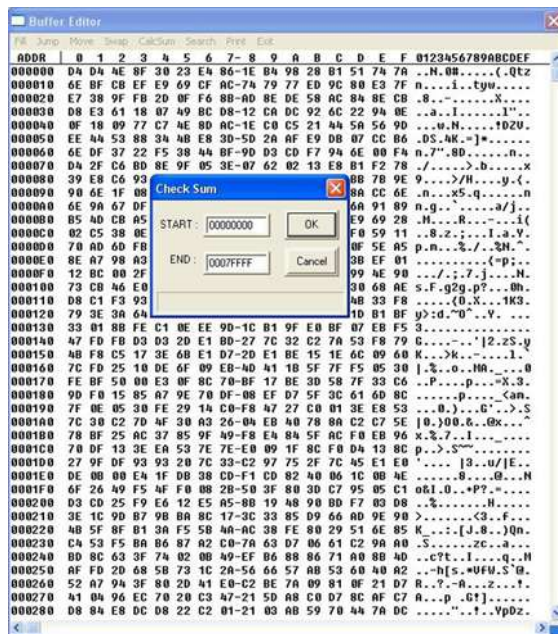
(6) Swap - Nibble:

Specify start and end addresses in which data to be nibble-swapped; then click “OK” to start Nibble-swapping.



(7) CalcSum:

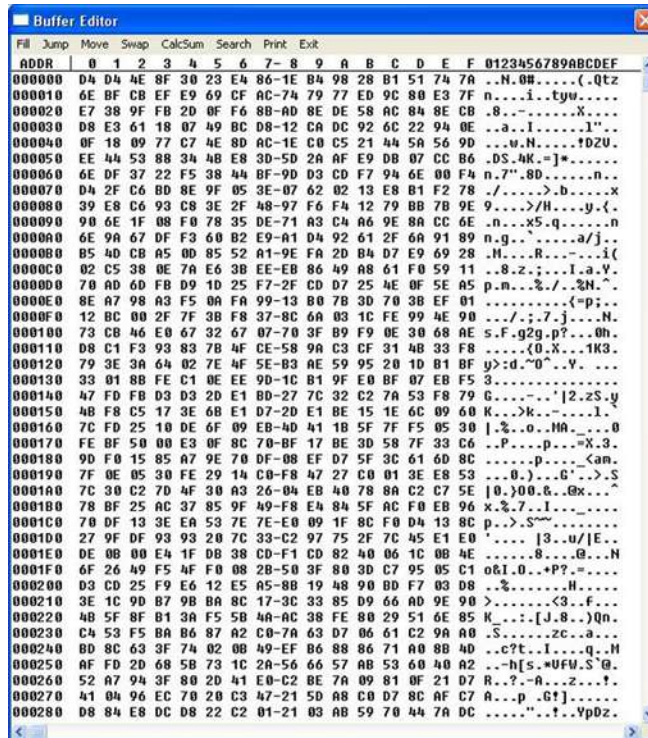
Specify start/end addresses in which data to be calculated for checksum; then click “OK” to calculate and display checksum on screen.



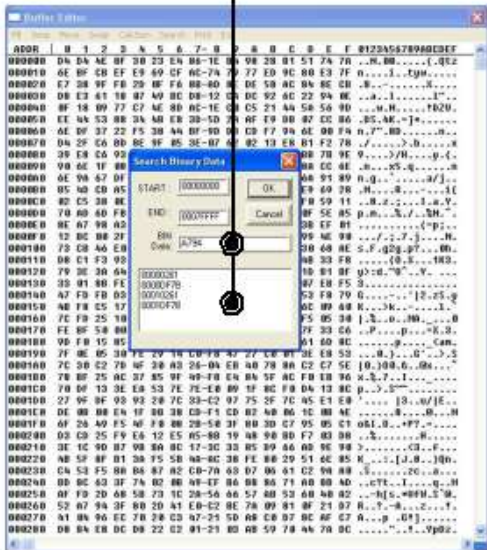
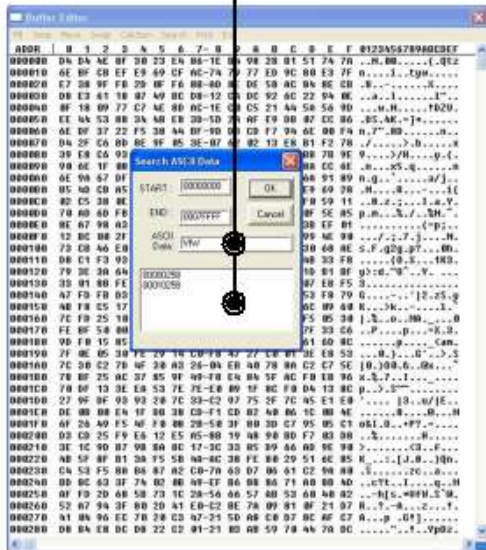


(8) Search:

Two options available, search ASC II data or search Binary data.

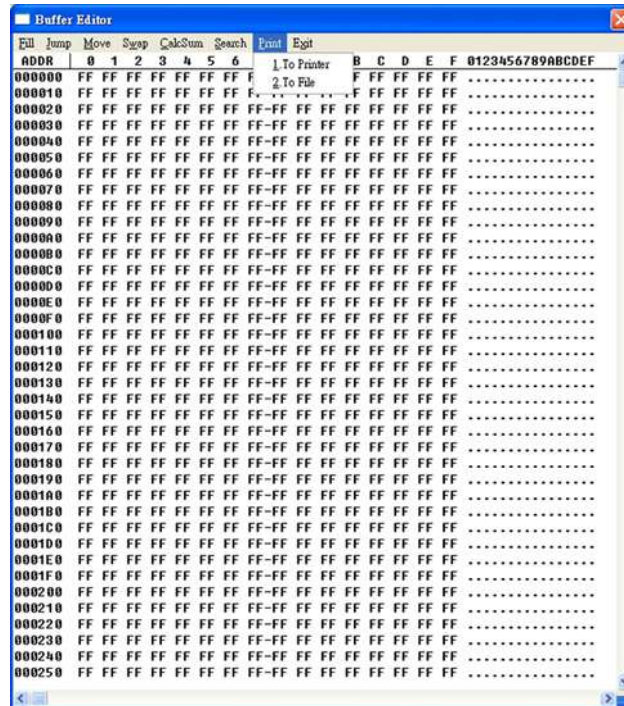


Specify start/end addresses and data to be searched, then click "OK", system will search data between specified addresses and list all addresses that match data to be searched.

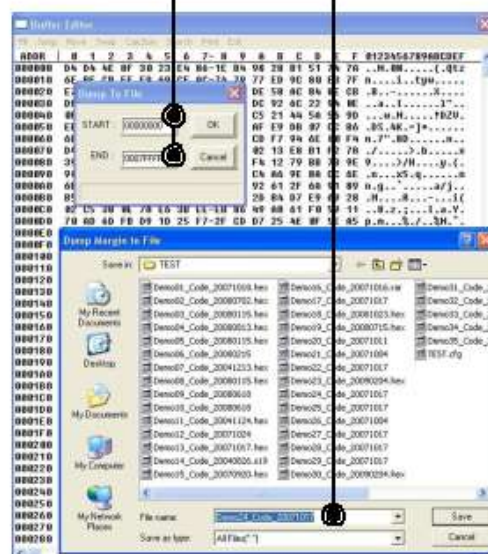
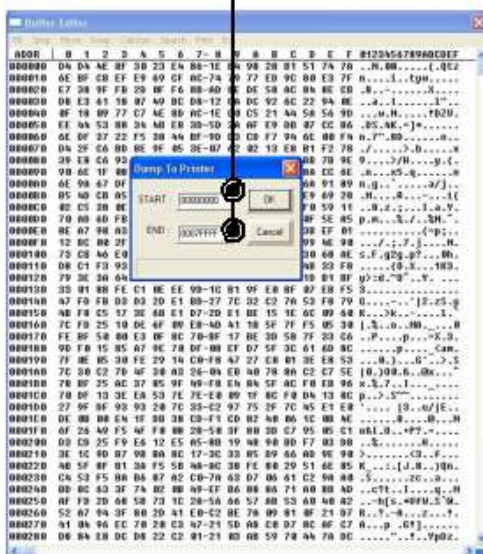


(9) Print:

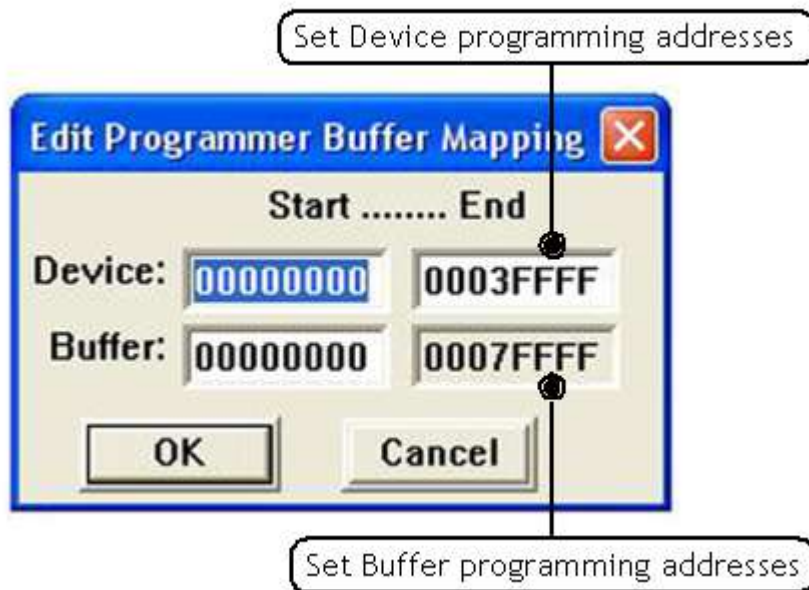
Two options available, print to printer or output to file.



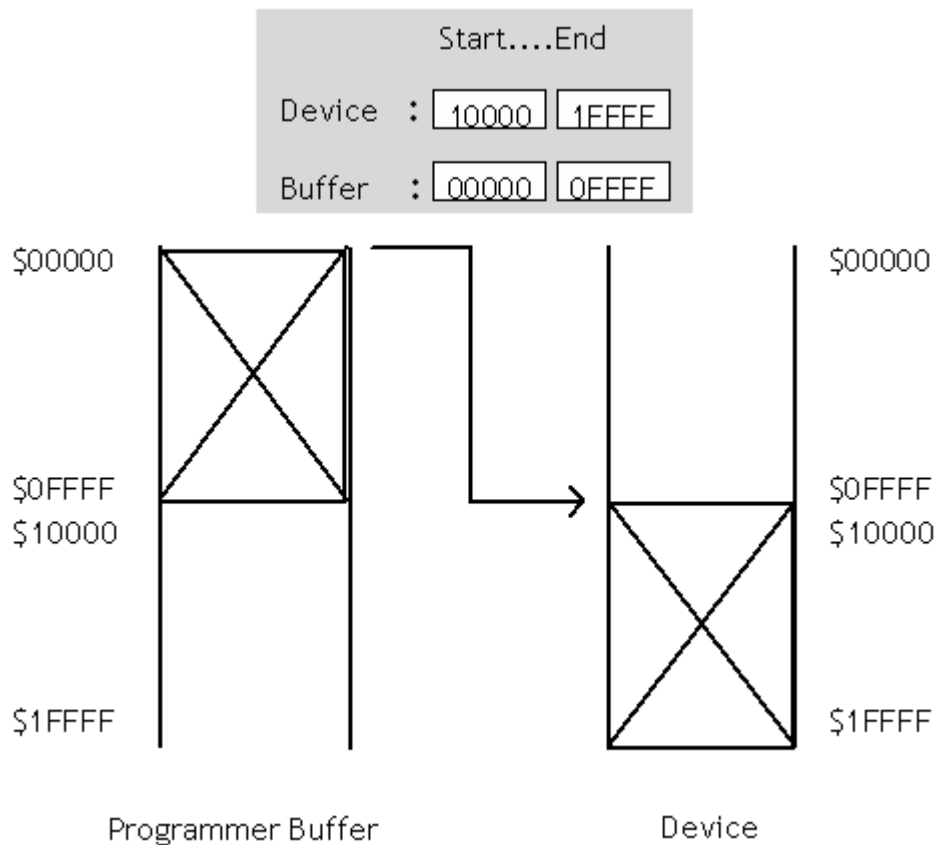
Specify start/end addresses and then click "OK", system will print/save data between specified addresses to printer/output file.



(10) Modify Programmer Buffer Mapping:



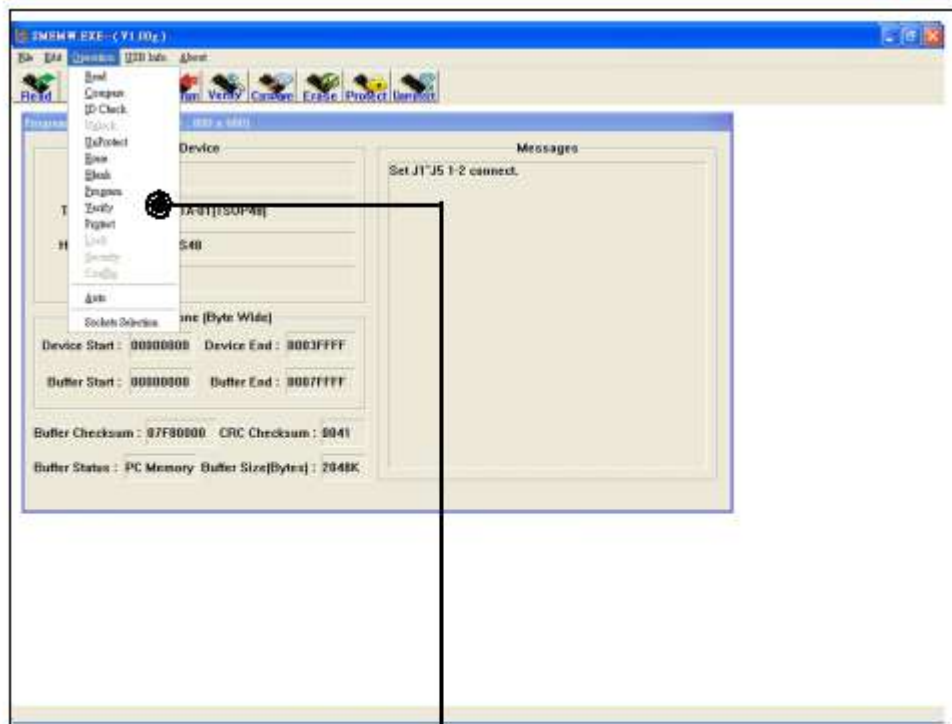
Eg.: Program data from Buffer address \$00000 - \$0FFFF to Device address \$10000 - \$1FFFF.





### 5.6.3 Operation

Different IC product type might have different options of programming functions. Basically, programming functions include options of Erase, Blank, Program, Verify, Lock, Auto, ...etc.



Functional options for Device S29GL004D-TA-01

User can also click <Fast Key> to execute program functions.  
See <Fast Key> indicated below:

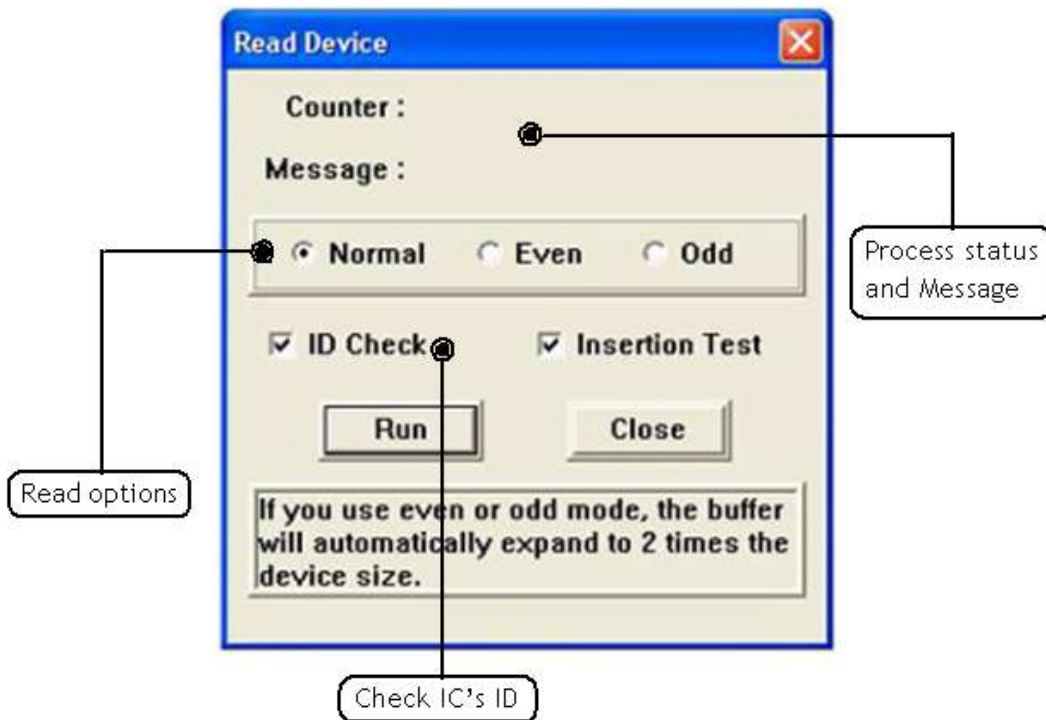


Fast key for Device S29GL004D-TA-01

(1) Read:

Read contents in IC memory. Read function is only valid for IC on following position.

- Single ALL-200G setup : IC on the 1st HEAD (HEAD # 1).
- Multiple ALL-200G setup : IC on the 1st HEAD (HEAD # 1) of the minimum site # programmer.

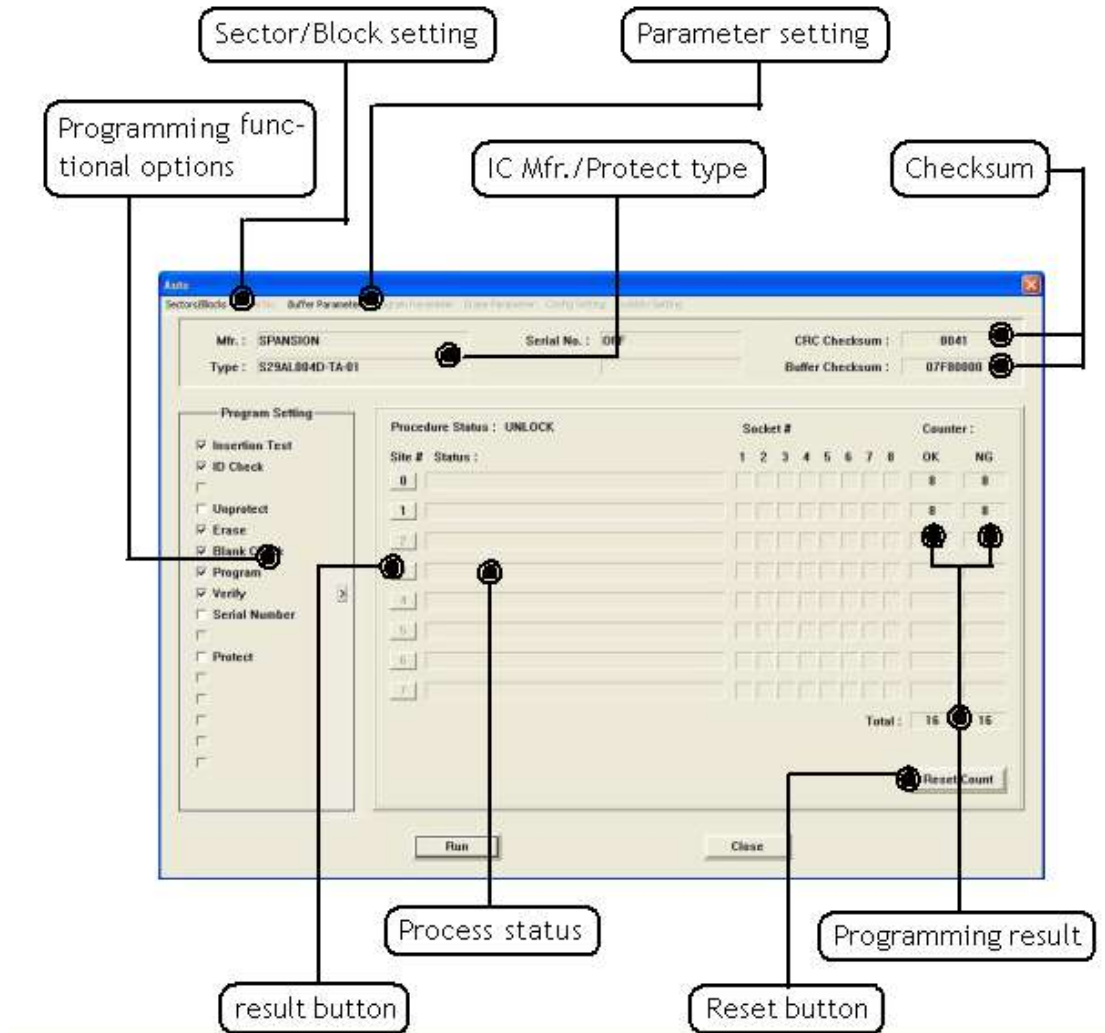


- ✧ After Read operation, suggest user to double-check to confirm checksum and the data read are all correct.

(2) Auto:

- Protect Mode inactivated

Enter programming mode with all programming functions activated.



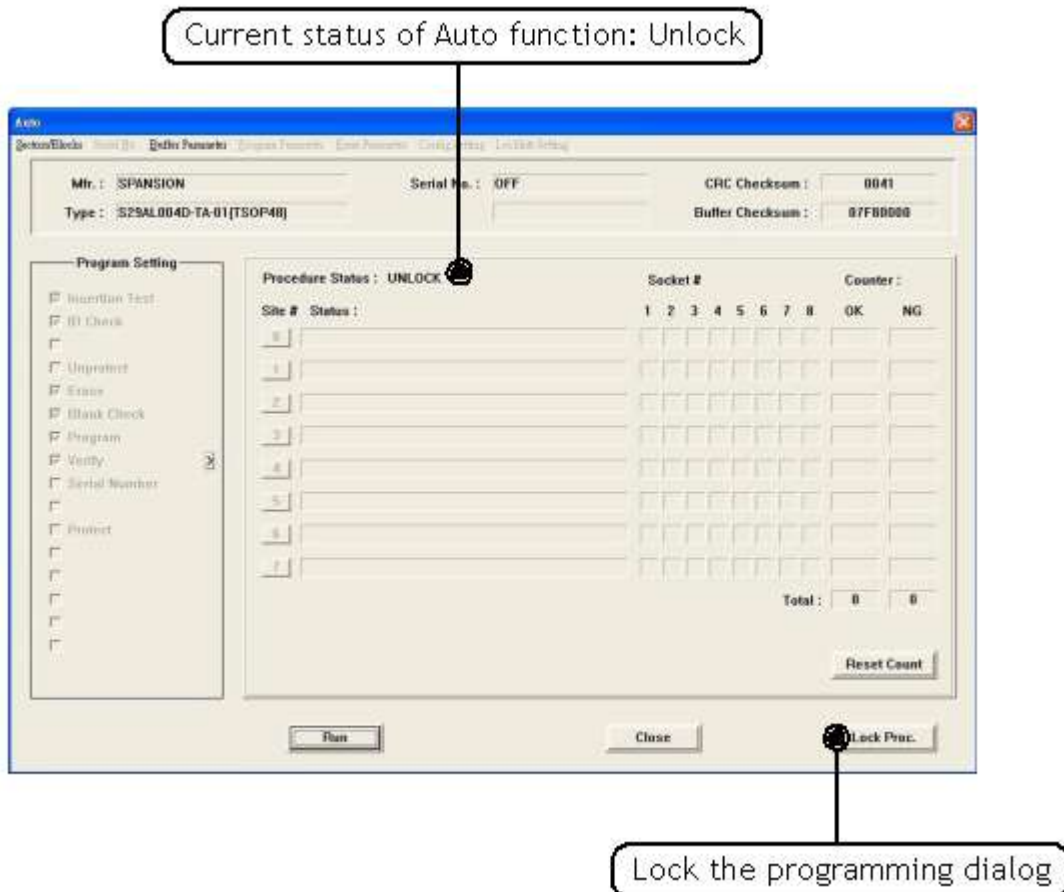
- ◇ Different IC product type might have different programming functional setting and parameter setting. User needs to refer IC data sheet for proper settings of programming functions and programming parameters.



■ Protect Mode activated:

After Protect Mode is activated in HACCESS, all programming settings of Auto function will run based on the setting of Protect Mode Option.

Besides, at the lower right corner of Auto dialog appears an additional button of "Lock Proc.", which locks the thorough programming dialog except the <RUN> button.



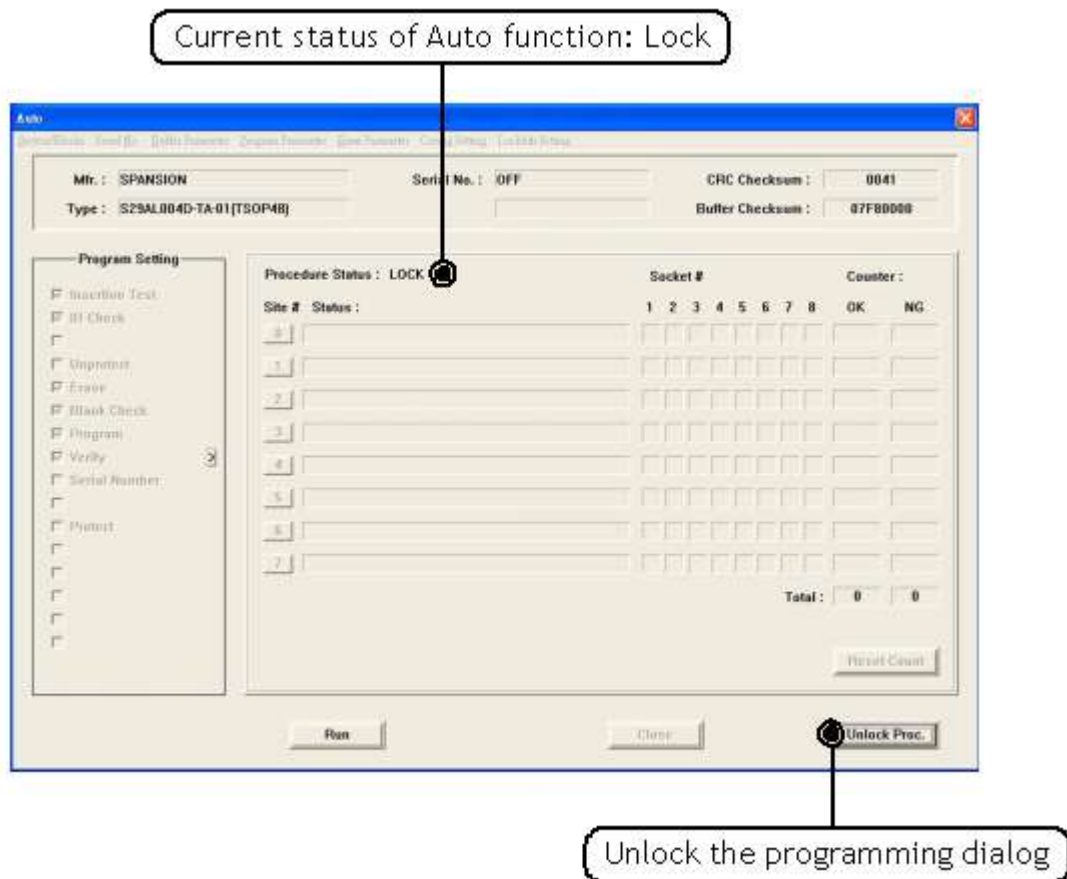
■ Lock Process:

After Protect Mode is activated in HACCESS, click <Lock Proc.> to lock the thorough programming dialog of Auto function but <RUN> button is excepted.

The procedure status will show "LOCK" and <Lock Proc.> will change into <Unlock Proc.>.

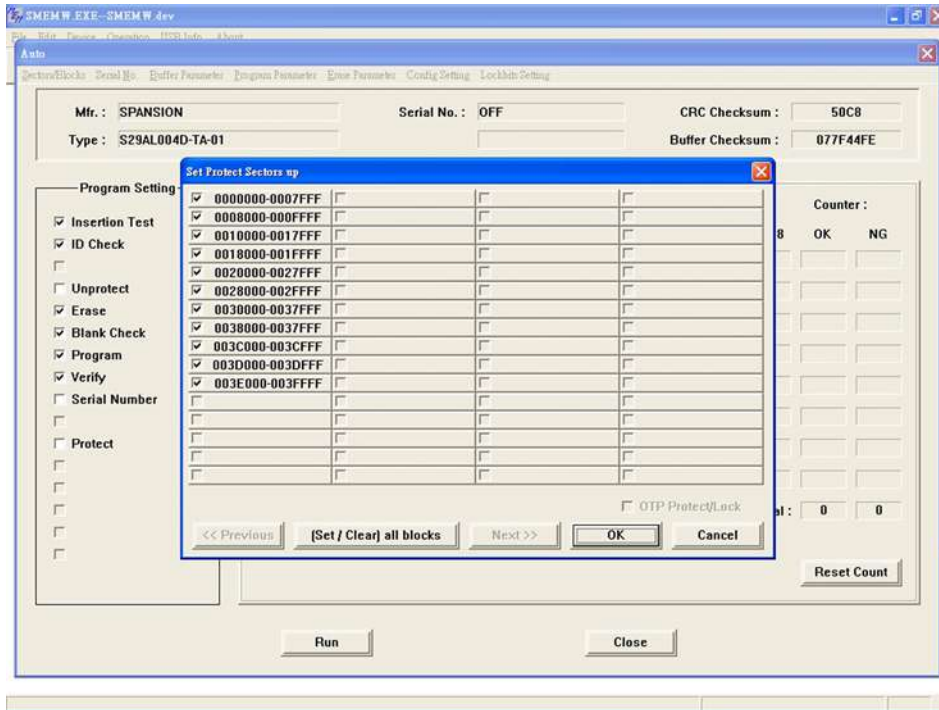
You can either click <RUN> to start programming or <Unlock Proc.> to unlock the dialog.

◇ Entering password is essential for Lock and Unlock process.



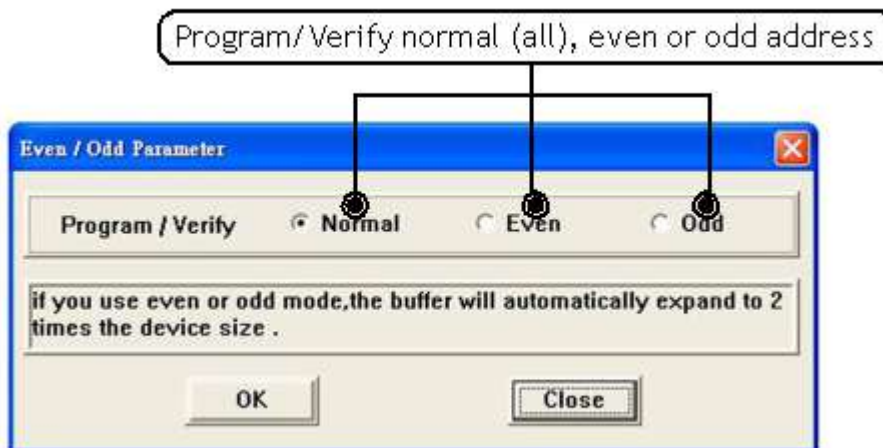
■ Sector/Block:

Specify sectors/blocks to protect if needed.



■ Parameter:

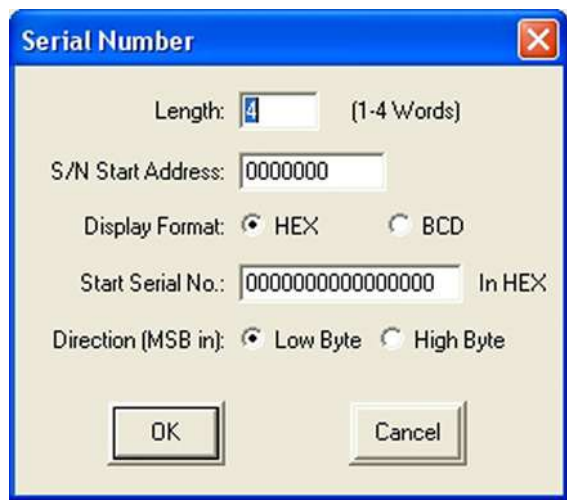
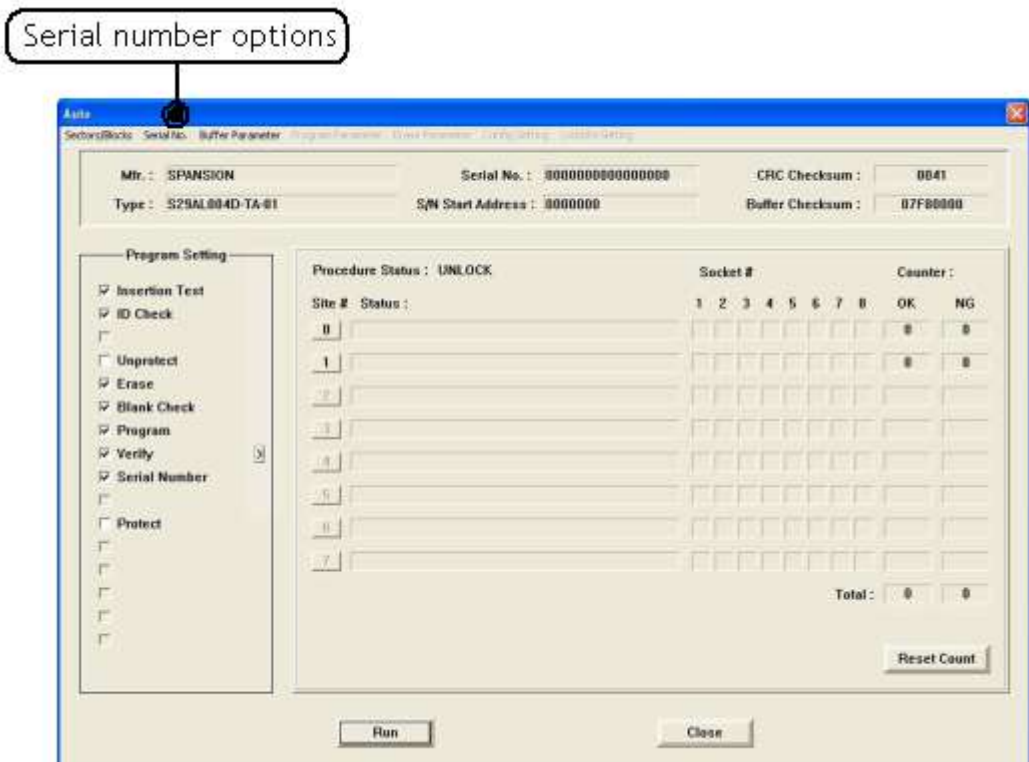
Special setting for programming if needed. If any of the options is shown in gray, it's not available.



■ Serial Number:

Select "Serial Number" option of the programming setting to enable "Serial No." of Auto function; then click "Serial No." to set the length, start address and format, etc.

✧ This feature is provided based on algorithm/application of IC product so it is not available for all ICs.



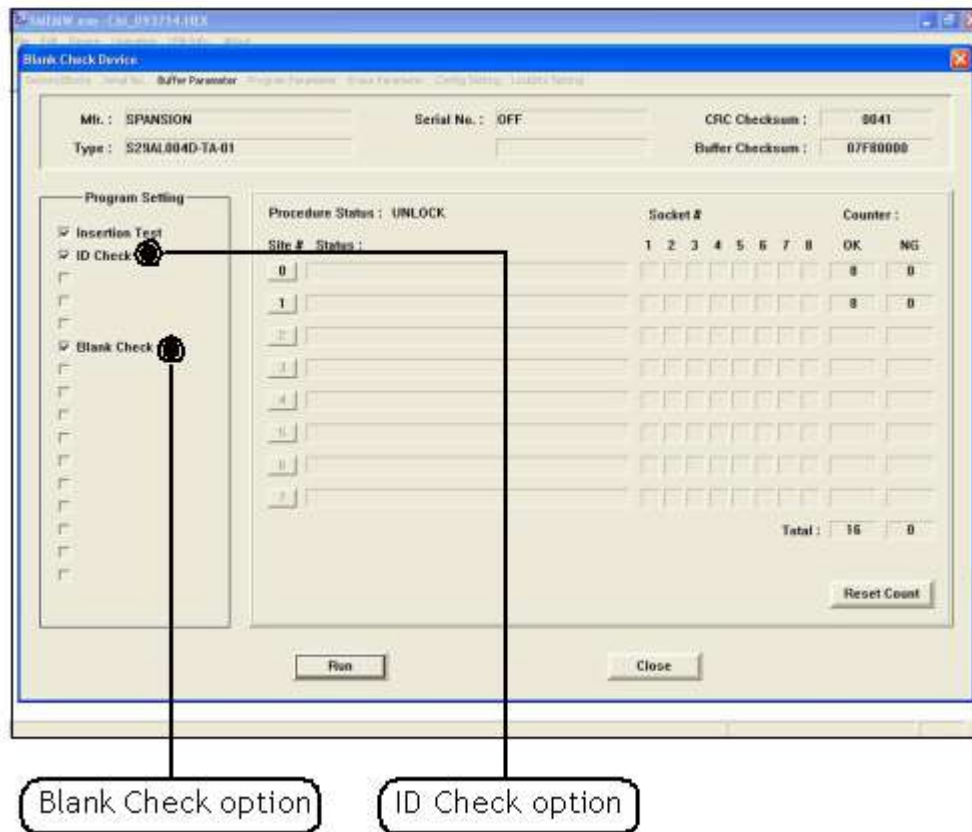
(3) Erase : Enter programming mode with Erase function activated.

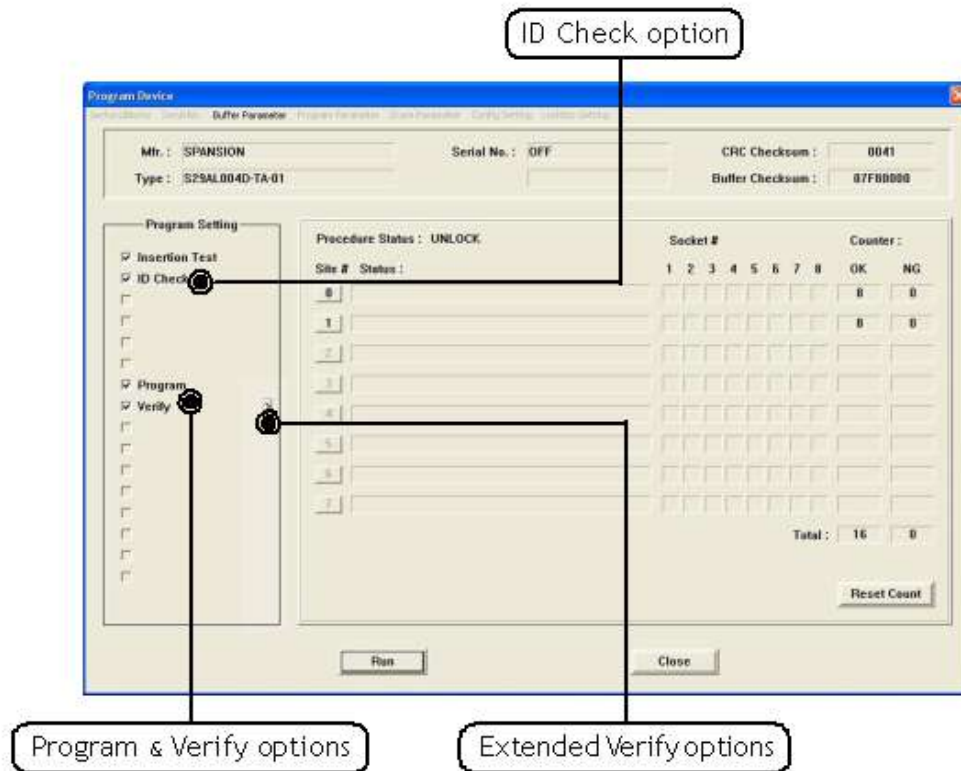
Blank : Enter programming mode with Blank check activated.


Program : Enter programming mode with Program function activated.

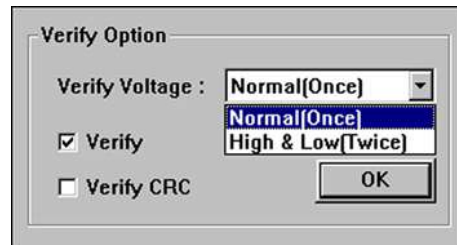
Verify : Enter programming mode with Verify function activated.

Protect : Enter programming mode with Protect function activated.

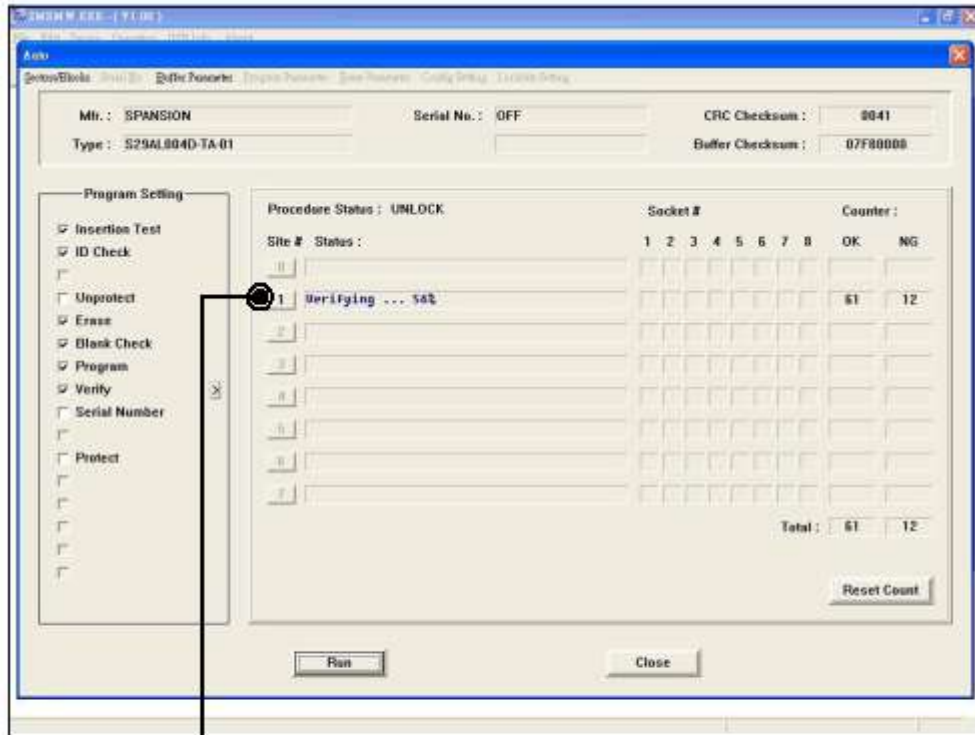




- Extended Verify options : Click the button  beside Verify function to view extended options below.



- Verify Voltage :  
 Normal [Once] : Verify with IC's standard voltage.  
 High & Low [Twice] : Verify with the range of high and low voltages.
- Verify : Verify programming result.
- Verify CRC : Verify CRC code after programming.



Result button, displaying programming result and elapsed time



Programming finished

Verify error at address 00000A9

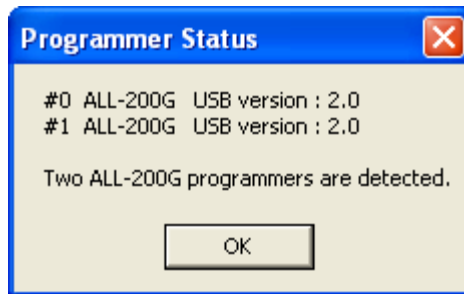
Means a HEAD is needed or a wrong HEAD is inserted

Program error at 0000006C



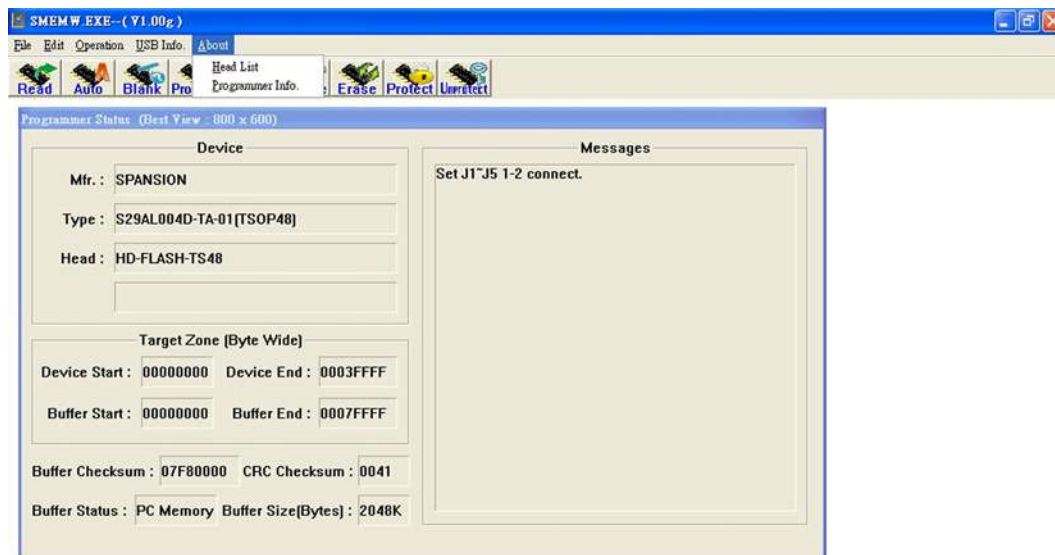
#### 5.6.4 USB Info.

Display current ALL-200G connection status through USB interface.



#### 5.6.5 About

Provide information of the required HEAD(s) and programmer hardware/software.

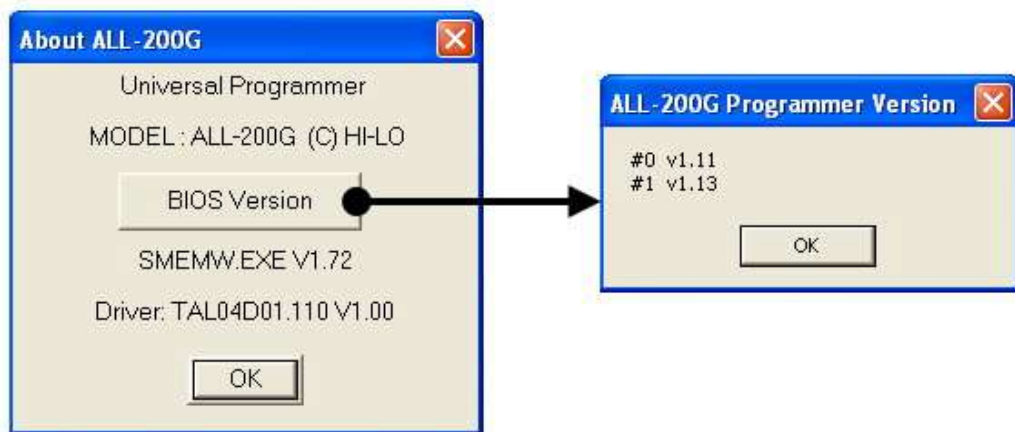




(1) HEAD List: Display the supported IC packages and required HEAD.



(2) Programmer Info.: Display model number of current programmer and version number of current programming driver.



## 6. ALL-200G Troubleshooting

After power on, ALL-200G will run self-diagnostics and USB connection check. Green/Red LED will display test result. Table listed below summarize test result with possible causes/dispositions for troubleshooting.

Condition	Possible cause/Dispositions
After power on, Green and Red LED off.	Poor contact / operation abnormal: <ol style="list-style-type: none"> <li>1. Check power cable connection.</li> <li>2. Need troubleshooting. Contact your local dealer for service.</li> </ol>
After power on, Green and Red LED flash.	Fail self diagnostic test: <ol style="list-style-type: none"> <li>1. Check if there is IC on socket (Should be removed).</li> <li>2. Need troubleshooting. Contact your local dealer for service.</li> </ol>
After power on, Green LED off but Red LED flash.	Fail ALL-200G memory diagnostic test: <ol style="list-style-type: none"> <li>1. Try to power on again.</li> <li>2. Need troubleshooting. Contact your local dealer for service.</li> </ol>
After power on or during programming, Green LED flash but Red LED off.	Fail USB connection check: <ol style="list-style-type: none"> <li>1. Check USB cable connection.</li> <li>2. Check PC BIOS setup to see if USB is disabled (Should be enabled).</li> <li>3. Need troubleshooting. Contact your local dealer for service.</li> </ol>
During programming, Green LED on but Red LED flash.	USB communication error: <ol style="list-style-type: none"> <li>1. Check USB cable connection.</li> <li>2. Need troubleshooting. Contact your local dealer for service.</li> </ol>

## 7. Glossary

### 7.1 EPROM, EEPROM, BPROM, and MPU

Programmable device: An integrated circuit (IC) that can be programmed.

#### ■ Bit, Nibble, Byte, Word, Double Word

Bit : A basic unit of binary data.

Nibble : A group of 4-bit binary data. A nibble ranges from 0H to FH.

Byte : A group of 8-bit binary data. A byte ranges from 0H to FFH.

Word : A group of 16-bit binary data. A word ranges from 0H to FFFFH.

Double word : A group of 32-bit binary data. A double word ranges from 0H to FFFFFFFFH.

#### ■ Buffer

There is 4 Mbit memory buffer in ALL-200G Programmer. IC driver file can automatically allocate/arrange these 4Mbit memory space for programming depending on IC memory size and read/write needs.

When data needs to be programmed to IC, data needs to be loaded to programmer buffer first and then program to IC. When data is read from Master IC, the data is also stored in programmer buffer, it can then be edited or saved to disk for future use.

- **Buffer Start and Buffer End Address**

It specifies the start and end addresses in programmer buffer in which data is to be programmed to IC in sequence. This is also the area that data is used for Checksum calculation.

- **CheckSum**

This is the SUM of all data contents between buffer start and buffer end addresses. All data are added and the least significant 16 bits (4 HEX) are displayed as the Checksum. (Some data in some ICs might not be covered in Checksum calculation.) Checksum will be calculated after IC reading, file loading, type changing, or buffer editing.

- **Bit Count of data**

A NIBBLE contains 4-bit data. A BYTE contains 8-bit data.  
A WORD contains 16-bit data.

MPU is normally in 8 or 16 bit width, but still have some in 12 or 14 bit width.

- **Device Start and Device End address**

It specifies the start and end addresses inside IC device. During IC programming, data stored in programmer buffer will be written to this specified area.

- **USB interface**

USB, Universal Serial Bus, is a high speed data transmission bus initiated by Intel and then supported by NEC, IBM, MicroSoft, Compaq,...etc. It is now a data transmission standard between PC and peripheral devices.

V1.1 USB Full-Speed : 12 Mb/s

V2.0 USB High-Speed : 480 Mb/s (Suggested)

- **Security fuse**

Security fuse is available in most of programmable ICs. Once the Security fuse has been blown, the data stored in IC cannot be read out correctly and IC cannot be programmed either. However, IC can still operate functionally no matter the Security fuse has been blown or not.

◇ Note: Once the Security fuse has been blown. IC data can no longer be read out or programmed correctly, please double-check before programming Security fuse.

- **Lock bits**

Some MCU/MPU use Lock bits to protect data programmed. Normally user has options to select individual Lock bit to protect different area of memory data. Please refer IC data sheet for definition of Lock bits.

- **Encryption**

Some MCU/MPU use Encryption code for data protection. If an IC has been programmed with Encryption code, then a correct decryption code must be given to read the correct data.

- **Protection Fuse**

Some FLASH memory use Protection fuse for data protection. It can prevent data change from accident programming. The Protection fuse must be reset to Unprotection state, if the programmed data need to be changed. The default state of Protection fuse is Unprotection.

## 7.2 PLD, PAL, GAL, PEEL, CPLD, EPLD, and FPGA Programmable Logic Device (PLD)

PLDs are usually grouped into following four categories:

PLD: A one time Programmable Logic Device such as PAL.

EPLD: A UV Erasable PLD such as EPLD, CPLD, and FPGA. These devices have transparent window on top of package for UV light exposure.

EEPLD: An Electrically Erasable PLD such as GAL, PEEL, CPLD.

CPLD: A more complex PLD device.

### ■ JEDEC fuse map file of PLD

JEDEC fuse map file is a standard format used for PLD programming. It contains fuse information and functional test vectors of PLD to be programmed. Most PLD assemblers or compilers such as PALASM, OPAL, CUPL, ABEL, AMAZE, and PDK-1, can create JEDEC fuse map file.

### ■ POF fuse map file of PLD

POF fuse map file is a format used for ALTERA PLD programming. POF file can store more programming data than JEDEC file.

### ■ Fuse blown and intact

Most of unprogrammed (blank) PLD have fuses in intact (connect) state. After programming, PLD fuses are blown to open state. For one time programmable PLD, once fuses are blown (opened), they cannot be changed back to intact (connect) state. However, the UV erasable PLD can be erased to change fuses back to intact (connect) state by UV light exposure and the electrically erasable PLD can be electrically erased to change fuses back to intact (connect) state by using Erase function on this Programmer.

- **Array fuse, Configuration fuse**

Array fuses are the main logic fuses in a PLD. Different types of PLD have different logic function arrangement. Configuration fuses define the I/O architecture of a PLD such as Combinatorial/Registered, Output feedback/Output enable, and so on. Generally, user do not have to understand the details of these fuses because logic compiler will automatically translate logic statements and equations into JEDEC format file.

- **Security fuse**

Most of PLD have Security fuse. Once Security fuse is blown, data in PLD can no longer be read out correctly. Generally, the PLD will be read as blank if Security fuse is blown.

**Note:**



