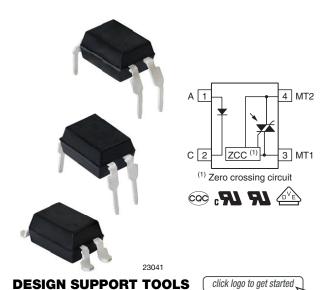


www.vishay.com

Vishay Semiconductors

Optocoupler, Phototriac Output, Zero Crossing, High dV/dt, Low Input Current



3D Models





DESCRIPTION

The VOT8024AD consists of a GaAs IRLED optically coupled to a photosensitive zero crossing TRIAC packaged in a board space saving DIP-4 package.

The VOT8024AD isolates low-voltage logic from 120 V_{AC} , 240 V_{AC} , and 380 V_{AC} lines to control resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.

FEATURES

- Space saving package
- High static dV/dt 1000 V/µs
- High input sensitivity I_{FT} = 5 mA
- 100 mA on-state current
- · Zero voltage crossing detector
- 800 V peak off-state blocking voltage
- Isolation rated voltage 5300 V_{RMS}
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE GREEN

APPLICATIONS

- Power TRIAC driver in solid-state relays
- 3-phase AC equipment
- Motor control
- Industrial control
- · White goods / household equipment

AGENCY APPROVALS

- UL 1577
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option "V"
- CQC

| ORDERING INFORMATION | | | |
|--------------------------|---|--|--|
| V O T 8 0 2 A | PACKAGE VDE TAPE AND OPTION OPTION REEL | | |
| AGENCY CERTIFIED/PACKAGE | TRIGGER CURRENT, I _{FT} (mA) | | |
| UL, cUL | 5 | | |
| DIP-4 | VOT8024AD | | |
| DIP-4, 400 mil | VOT8024AG | | |
| SMD-4 | VOT8024AB-T ⁽¹⁾ | | |
| SMD-4, 90° orientation | VOT8024AB-T1 | | |
| SMD-4, 180° orientation | VOT8024AB-T2 | | |
| SMD-4, 270° orientation | VOT8024AB-T3 | | |
| VDE, UL, cUL | 5 | | |
| DIP-4 | VOT8024AD-V | | |
| DIP-4, 400 mil | VOT8024AG-V | | |
| SMD-4 | VOT8024AB-VT ⁽¹⁾ | | |
| SMD-4, 90° orientation | VOT8024AB-VT1 | | |
| SMD-4, 180° orientation | VOT8024AB-VT2 | | |
| SMD-4, 270° orientation | VOT8024AB-VT3 | | |

Note

(1) Also available in tubes; do not add T to end

VOT8024AB, VOT8024AD, VOT8024AG

Vishay Semiconductors

| ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified) | | | | |
|--|--------------------|---------------------|-------------|------|
| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT |
| INPUT | | | | |
| Reverse voltage | | V_R | 6 | V |
| Forward current | | I _F | 50 | mA |
| Power dissipation | | P _{diss} | 120 | mW |
| Junction temperature | | Tj | 125 | °C |
| OUTPUT | | | | |
| Peak off-state voltage | | V_{DRM} | 800 | V |
| Power dissipation | | P _{diss} | 150 | mW |
| On-state current | | I _{T(RMS)} | 100 | mA |
| Peak repetitive surge current | PW = 1 ms, 120 pps | I _{TSM} | 1 | A |
| Junction temperature | | Tj | 125 | °C |
| COUPLER | | | | |
| Storage temperature range | | T _{stg} | -55 to +150 | °C |
| Ambient temperature range | | T _{amb} | -55 to +110 | °C |
| Total power dissipation | | P _{diss} | 250 | mW |
| Soldering temperature | For 10 s | T _{sld} | 260 | °C |

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.
 This phototriac should not be used to drive a load directly. It is intended to be a trigger device only

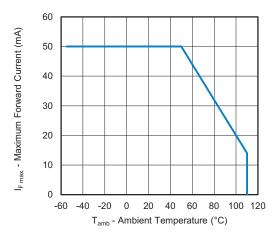


Fig. 1 - Maximum Forward Current vs. Ambient Temperature

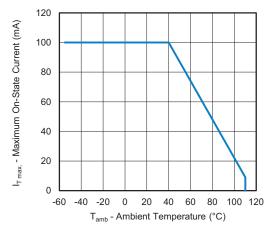


Fig. 2 - Maximum On-State Current vs. Ambient Temperature

VOT8024AB, VOT8024AD, VOT8024AG

Vishay Semiconductors

| ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified) | | | | | | |
|--|--|----------------------|------|------|------|------|
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| INPUT | | | | | | |
| Forward voltage | I _F = 20 mA | V_{F} | - | 1.2 | 1.4 | V |
| Reverse current | V _R = 3 V | I _R | - | - | 10 | μA |
| ОUТРUТ | | | | | | |
| Off-state current | V _{DRM} = 800 V | I _{DRM} | - | - | 1 | μA |
| On-state voltage | I _T = 100 mA peak | V_{TM} | - | - | 3 | V |
| Holding current | | lμ | - | 400 | - | μA |
| Zero cross inhibit voltage | I _F = rated I _{FT} | V_{INH} | - | 5 | 20 | V |
| Critical rate of rise of off-state voltage | | dV/dt ⁽¹⁾ | 1000 | - | - | V/µs |
| Leakage in inhibit state | I_F = rated I_{FT} , rated V_{DRM} , off-state | I _{DRM2} | - | - | 500 | μΑ |
| COUPLER | | | | | | |
| Trigger current | V _{TM} = 3 V | I _{FT} | - | - | 5 | mA |

Notes

⁽¹⁾ Static dV/dt

| SAFETY AND INSULATION RATINGS (T _{amb} = 25 °C, unless otherwise specified) | | | | |
|---|--|-------------------|--------------------|-------------------|
| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT |
| Climatic classification | According to IEC 68 part 1 | | 55 / 110 / 21 | |
| Comparative tracking index | Insulation group IIIa | CTI | 175 | |
| Maximum rated withstanding isolation voltage | According to UL1577, t = 1 min | V _{ISO} | 5300 | V _{RMS} |
| Maximum transient isolation voltage | According to DIN EN 60747-5-5 | V _{IOTM} | 8000 | V _{peak} |
| Maximum rapatitiva paak isolation valtaga | According to DIN EN 60747-5-5, DIP-4, SMD-4 | V _{IORM} | 890 | V _{peak} |
| Maximum repetitive peak isolation voltage | According to DIN EN 60747-5-5, DIP-4, 400 mil | V _{IORM} | 1140 | V _{peak} |
| Isolation resistance | V _{IO} = 500 V, T _{amb} = 25 °C | R _{IO} | ≥ 10 ¹² | Ω |
| isolation resistance | V _{IO} = 500 V, T _{amb} = 100 °C | R _{IO} | ≥ 10 ¹¹ | Ω |
| Output safety power | | P _{SO} | 600 | mW |
| Input safety current | | I _{SI} | 400 | mA |
| Input safety temperature | | T _S | 175 | °C |
| Creepage distance | DID 4 CMD 4 | | ≥ 7 | mm |
| Clearance distance | DIP-4, SMD-4 | | ≥ 7 | mm |
| Creepage distance | DIP-4. 400 mil | | ≥ 8 | mm |
| Clearance distance | DIF-4, 400 Mill | | ≥ 8 | mm |
| Insulation thickness | | DTI | ≥ 0.4 | mm |

Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements

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Vishay Semiconductors

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

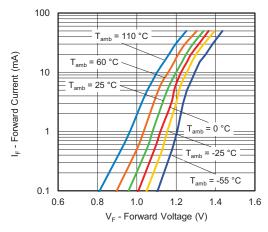
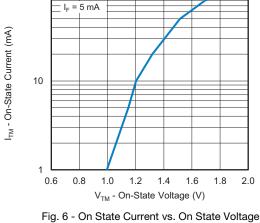


Fig. 3 - Forward Current vs. Forward Voltage



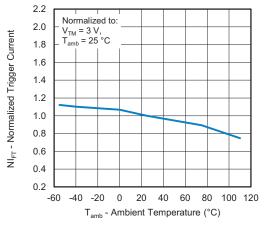


Fig. 4 - Normalized Trigger Current vs. Ambient Temperature

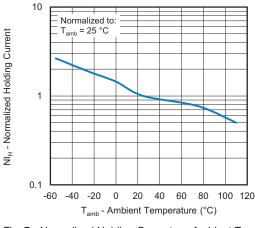


Fig. 7 - Normalized Holding Current vs. Ambient Temperature

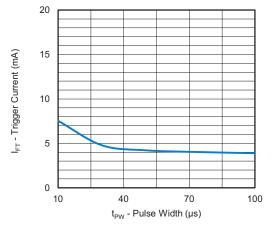


Fig. 5 - Trigger Current vs. Pulse Width

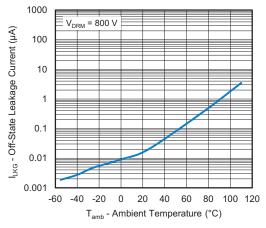


Fig. 8 - Off-State Leakage Current vs. Ambient Temperature

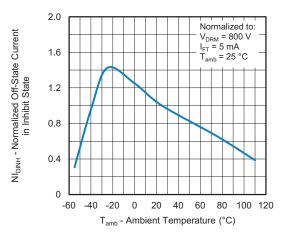


Fig. 9 - Normalized Off-State Current in Inhibit State vs.
Ambient Temperature

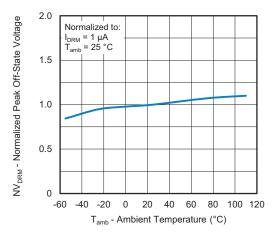


Fig. 10 - Normalized Peak Off-State Voltage vs. Ambient Temperature

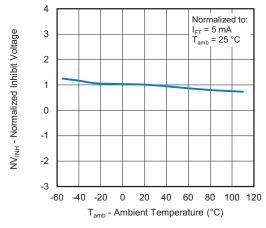
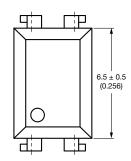
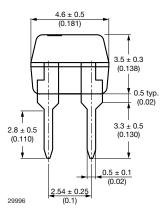


Fig. 11 - Normalized Inhibit Voltage vs. Ambient Temperature

PACKAGE DIMENSIONS (in millimeters)

DIP-4





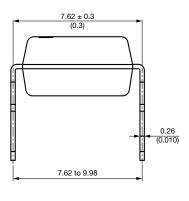
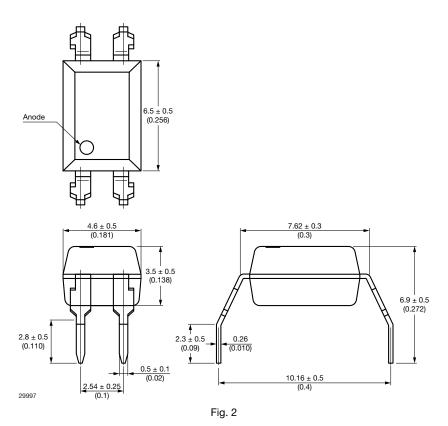
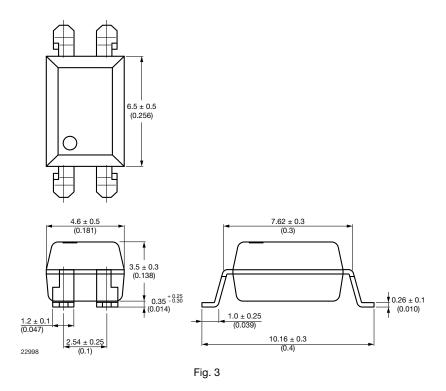


Fig. 1

DIP-4, 400 mil



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PACKAGE MARKING



Fig. 12 - Example of VOT8024AD-VT

Notes

- "YWW" is the date code marking (Y = year code, WW = week code)
- VDE logo is only marked on VDE option parts
- Tape and reel suffix (T) is not part of the package marking

PACKAGING INFORMATION (in millimeters)

| DEVICES PER TUBE | | | |
|------------------|------------|-----------|-----------|
| TYPE | UNITS/TUBE | TUBES/BOX | UNITS/BOX |
| DIP-4 | 100 | 40 | 4000 |
| DIP-4, 400 mil | 100 | 40 | 4000 |

DIP-4 Tube

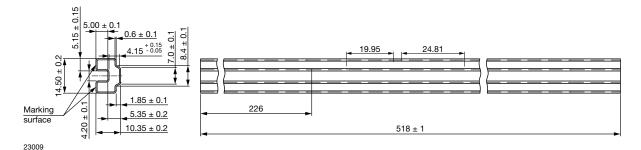


Fig. 13 - LTK-O-092

SMD-4 Tape

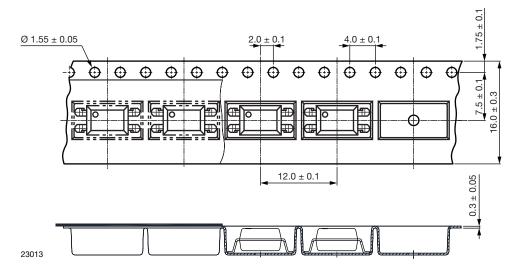


Fig. 14 - Tape and Reel Packaging (1000 pieces on reel)

SMD-4 Tape, 90° Orientation

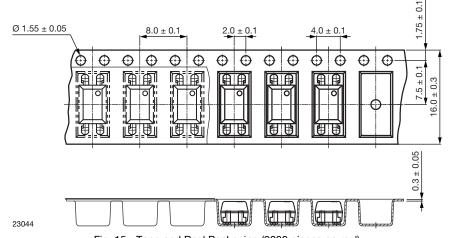


Fig. 15 - Tape and Reel Packaging (2000 pieces on reel)

SMD-4 Tape, 180° Orientation

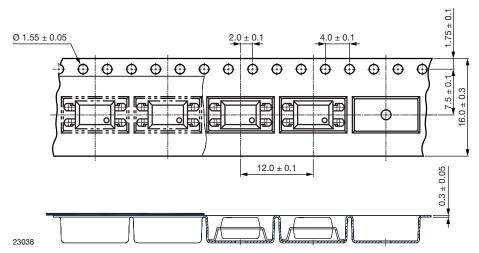
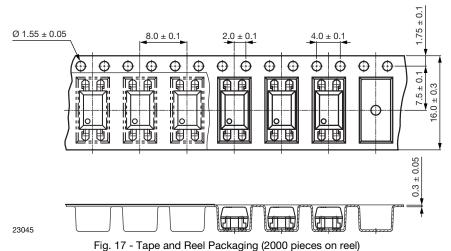


Fig. 16 - Tape and Reel Packaging (1000 pieces on reel)

SMD-4 Tape, 270° Orientation



Reel

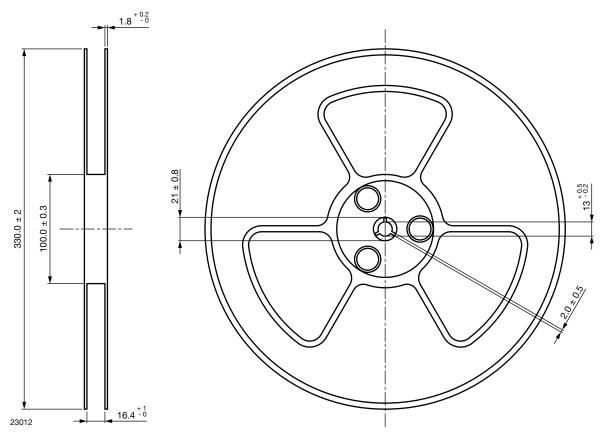


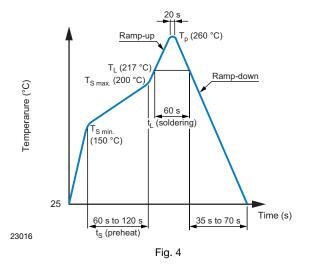
Fig. 18 - Tape and Reel Shipping Medium

SOLDER PROFILES

IR Reflow Soldering (JEDEC® J-STD-020C compliant)

One time soldering reflow is recommended within the condition of temperature and time profile shown below. Do not solder more than three times.

| PROFILE ITEM | CONDITIONS |
|--|------------------|
| Preheat | |
| - Temperature minimum (T _{S min.}) | 150 °C |
| - Temperature maximum (T _{S max.}) | 200 °C |
| - Time (min. to max.) (t _S) | 90 s ± 30 s |
| Soldering zone | |
| - Temperature (T _L) | 217 °C |
| - Time (t _L) | 60 s |
| Peak temperature (T _p) | 260 °C |
| Ramp-up rate | 3 °C/s max. |
| Ramp-down rate | 3 °C/s to 6 °C/s |



Wave Soldering (JEDEC JESD22-A111 compliant)

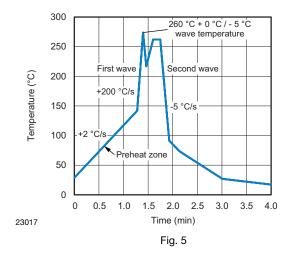
One time soldering is recommended within the condition of temperature.

Temperature: 260 °C + 0 °C / - 5 °C

Time: 10 s

Preheat temperature: 25 °C to 140 °C

Preheat time: 30 s to 80 s



Hand Soldering by Soldering Iron

Allow single lead soldering in every single process. One time soldering is recommended.

Temperature: 380 °C + 0 °C / - 5 °C

Time: 3 s max.

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020

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Vishay

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