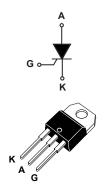


TN2015H-6I

Datasheet

20 A 600 V high temperature SCR thyristors in insulated TO-220



TO-220AB insulated

Features

- High junction temperature: T_i max. = 150 °C
- High static immunity dV/dt = 750 V/µs up to 150 °C
- Peak off-state voltage V_{DRM}/V_{RRM} = 600 V
- High turn-on current rise dl/dt = 100 A/µs
- Insulated package TO-220AB:
 - Insulated voltage: 2500 V_{RMS}
 - Complies with UL 1557 (File ref : E81734)
- ECOPACK2 compliant
- Halogen-free molding, lead-free plating

Applications

- General purpose AC line load switching
- Motor control circuits and starters
- Inrush current limiting circuits
- Heating resistor control, solid state relays

Description

Thanks to its operating junction temperature up to 150°C, the TN2015H-6I offers high thermal performance operation up to 20 A rms.

Its trade-off noise immunity (dV/dt = 750 V/ μ s) versus its gate triggering current (I_{GT} = 15 mA) and its turn-on current rise (dI/dt = 100 A/ μ s) allows to design robust and compact control circuit for voltage regulator in motorbikes and industrial drives, overvoltage crowbar protection, motor control circuits in power tools and kitchen appliances and inrush current limiting circuits.

Product status				
TN2015H-6I				
Product summary				
Order code	TN2015H-6I			
Package	TO-220AB Ins.			
I _{T(RMS)}	20 A			
V _{DRM} /V _{RRM}	600 V			
T _j max.	150 °C			

1 Characteristics

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Symbol	Pa	Value	Unit		
I _{T(RMS)}	RMS on-state current (180 ° conduct	T _c = 113 °C	20	Α	
	Average on-state current (180 ° conduction angle)		T _c = 112 °C	13	
I _{T(AV)}			T _c = 130 °C	8	Α
			T _c = 139 °C	5	1
	I _{TSM} Non repetitive surge peak on-state current (T _i initial = 25 °C)		t _p = 8.3 ms	197	
ITSM			t _p = 10 ms	180	A
l ² t	$I^{2}t$ value for fusing, (T _j initial = 25 °C) t_{p} =			162	A ² s
dl/dt	$I_G = 2 \times I_{GT}, tr \le 100 \text{ ns}$			100	A/110
ai/at	Critical rate of rise of on-state current f = 60 Hz			100	A/µs
V _{DRM} /V _{RRM}	Repetitive peak off-state voltage			600	V
V _{DSM} /V _{RSM}	Non Repetitive peak off-state voltage		t _p = 10 ms	V _{DRM} /V _{RRM} + 100 V	V
I _{GM}	Peak gate current	t _p = 20 μs	T _j = 150 °C	4	Α
P _{G(AV)}	Average gate power dissipation	! 	T _j = 150 °C	1	W
V _{RGM}	Maximum peak reverse voltage	5	V		
T _{stg}	Storage junction temperature range	-40 to +150	°C		
Тj	Maximum operating junction temperature			-40 to +150	°C
Τ _Ι	Maximum lead temperature soldering during 10 s			260	°C
V _{iso}	Insulation rms voltage, 1 minute			2500	V

Table 1. Absolute maximum ratings (limiting values), T_j = 25 °C unless otherwise specified

Table 2. Electrical characteristics (T_j = 25 °C unless otherwise specified)

Symbol	Test conditions				
lor			Тур.	6	mA
GI	I_{GT} $V_D = 12 V, R_L = 33 \Omega$				IIIA
V_{GT}			Max.	1.3	V
V _{GD}	$V_{\rm D} = V_{\rm DRM}, R_{\rm L} = 3.3 \ {\rm k}\Omega$ $T_{\rm j} = 150 \ {\rm ^{\circ}C}$				V
Ι _Η	I _T = 500 mA, gate open				mA
١L	I _G = 1.2 x I _{GT} Ma				mA
dV/dt	V_D = 402 V, gate open T_j = 150 °C				V/µs
t _{gt}	I_T = 40 A, V_D = 402 V, I_G = 20 mA, (dI _G /dt) max = 0.2 A/µs Typ			1.9	μs
tq	I_T = 20 A, V_D = 402 V, (dI _G /dt) max = 30 A/µs, V_R = 25 V, dV _D /dt = 40 V/µs T_j = 150 °C T_y			70	μs

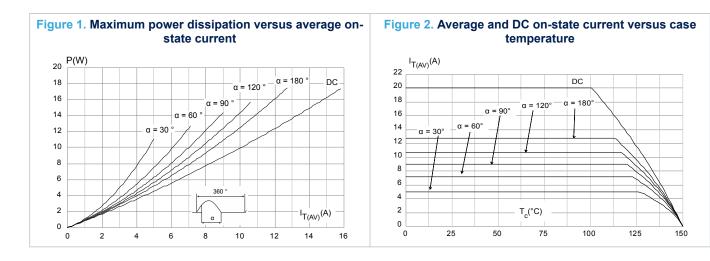
Table 3. Static characteristics

Symbol	Test conditions			Value	Unit
V _{TM}	I _{TM} = 40 A, t _p = 380 μs	T _j = 25 °C	Max.	1.60	V
V _{TO}	Threshold voltage	T _j = 150 °C	Max.	0.82	V
R _D	Dynamic resistance	T _j = 150 °C	Max.	17.5	mΩ
I _{DRM} , I _{RRM} V _D = V _{DRM} ; V		T _j = 25 °C	Max.	5	μA
	$v_D - v_{DRM}, v_R - v_{RRM}$	T _j = 150 °C		2	mA

Table 4. Thermal parameters

Symbol	Parameter	Value	Unit	
R _{th(j-c)}	Junction to case (DC)	Max.	2.1	°C/W
R _{th(j-a)}	Junction to ambient (DC)	Тур.	60	C/VV

1.1 Characteristics curves



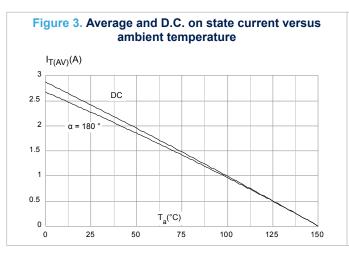


Figure 5. Relative variation of gate triggering current and gate voltage versus junction temperature (typical values) I_{GT}, V_{GT} [T_j] / I_{GT}, V_{GT} [T_j = 25 °C] 2.5 I_{GT} 2.0 1.5 1.0 V_{GT} 0.5 T_i(°C) 0.0 50 -25 0 25 75 100 -50 125 150

Figure 4. Relative variation of thermal impedance junction to case and junction to ambient versus pulse duration

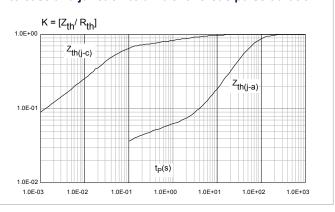
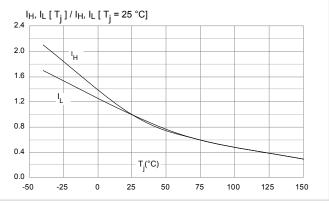
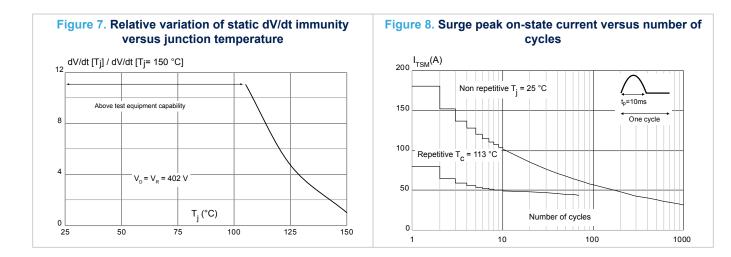
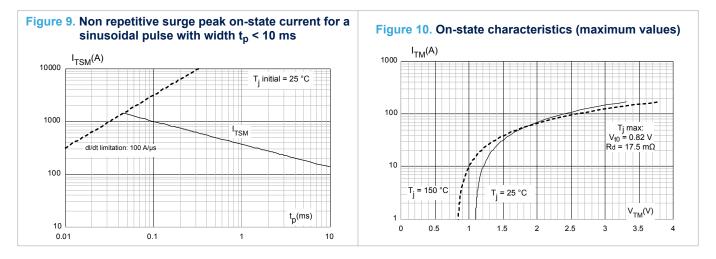


Figure 6. Relative variation of holding and latching current versus junction temperature (typical values)

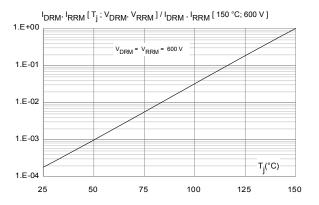












2 Package information

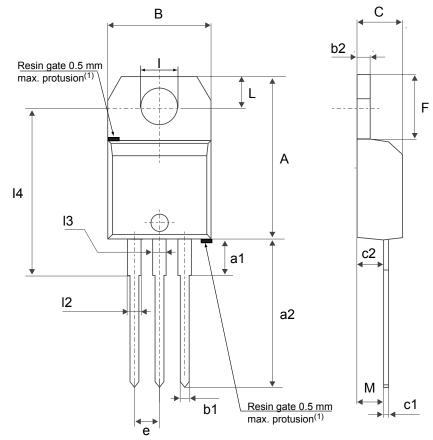
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In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

2.1 TO-220AB insulated package information

- Molding compound resin is halogen-free and meets flammability standard UL94 level 0
- Lead-free package leads finishing
- ECOPACK2 compliant
- Recommended torque: 0.4 to 0.6 N.m

Figure 12. TO-220AB insulated package outline



(1)Resin gate position accepted in one of the two positions or in the symmetrical opposites.

Dimensions						
Ref.	Millimeters		Inches ⁽¹⁾			
	Min.	Тур.	Max.	Min.	Тур.	Max.
А	15.20		15.90	0.5984		0.6260
a1		3.75			0.1476	
a2	13.00		14.00	0.5118		0.5512
В	10.00		10.40	0.3937		0.4094
b1	0.61		0.88	0.0240		0.0346
b2	1.23		1.32	0.0484		0.0520
С	4.40		4.60	0.1732		0.1811
c1	0.49		0.70	0.0193		0.0276
c2	2.40		2.72	0.0945		0.1071
е	2.40		2.70	0.0945		0.1063
F	6.20		6.60	0.2441		0.2598
I	3.73		3.88	0.1469		0.1528
L	2.65		2.95	0.1043		0.1161
12	1.14		1.70	0.0449		0.0669
13	1.14		1.70	0.0449		0.0669
14	15.80	16.40	16.80	0.6220	0.6457	0.6614
М		2.6			0.1024	

Table 5. TO-220AB insulated package mechanical data

1. Inch dimensions are for reference only.



3 Ordering information

Figure 13. Ordering information scheme

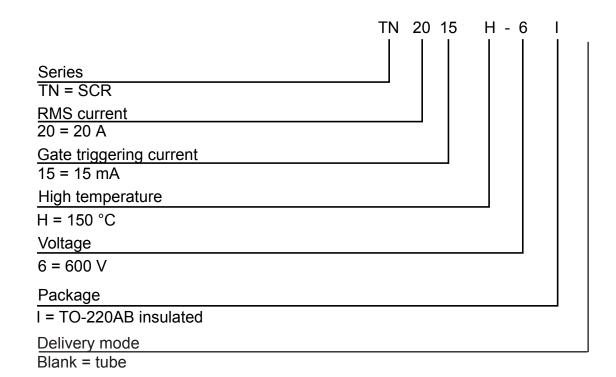


Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
TN2015H-6I	TN2015H6I	TO-220AB Ins.	2.3 g	50	Tube

Revision history

Table 7. Document revision history

Date	Revision	Changes
16-Dec-2019	1	Initial release.



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