TOSHIBA Photocoupler IRED & Photo-Thyristor

TLP748JF

Office Machine Household Use Equipment Solid State Relay Switching Power Supply

The TOSHIBA TLP748JF consists of a photo–thyristor optically coupled to an infrared emitting diode in a six lead plastic DIP package.

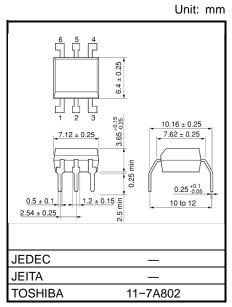
- Peak OFF-state voltage: 600 V (min)
- Trigger LED current: 10 mA (max)
- ON-state current: 150 mA (max)
- Isolation voltage: 4000 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349
- VDE-approved: EN 60747-5-5 (Note 1)

Note 1: When a VDE approved type is needed,

please designate the **Option(D4)**.

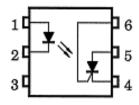
10.16 mm pitch TLPxxxxF type

Creepage distance: 8.0 mm (min)
 Clearance: 8.0 mm (min)
 Insulation thickness: 0.4 mm (min)



Weight: 0.42 g (typ.)

Pin Configuration (top view)



- 1 : ANODE
- 2: CATHODE
- 3 : N.C.
- 4: CATHODE
- 5 : ANODE
- 6 : GATE

Start of commercial production 2008-12

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Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
D	Forward current	l _F	50	mA
	Forward current derating (Ta ≥ 53 °C)	ΔI _F / °C	-0.7	mA / °C
	Peak forward current (100 μs pulse, 100 pps)	IFP	1	Α
LED	Reverse voltage	VR	5	V
	Diode power dissipation	P _D	100	mW
	Diode power dissipation derating (Ta ≥ 53°C)	△P _D /°C	-1.4	mW/°C
	Peak forward voltage (R _{GK} = 27 kΩ)	V_{DRM}	600	V
	Peak reverse voltage (R _{GK} = 27 kΩ)	VRRM	600	V
	ON-state current	IT(RMS)	150	mA
jo	ON-state current derating (Ta ≥ 25°C)	ΔIT / °C	-2.0	mA / °C
Detector	Peak ON-state current (100 μs pulse, 120 pps)	I _{TP}	3	Α
ă	Peak one cycle surge current	ITSM	2	Α
	Peak reverse gate voltage	VGM	5	V
	Output power dissipation	Ро	150	mW
	Output power dissipation derating (Ta ≥ 25°C)	ΔP _o /°C	-1.5	mW / °C
Storage temperature range		T _{stg}	−55 to 125	°C
Operating temperature range		Topr	-40 to 100	°C
Lead soldering temperature (10 s)		T _{sol}	260	°C
Isolatio	on voltage (AC, 60 s, R.H.≤ 60 %) (Note 1)	BVs	4000	V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device Considered a two terminal device: pins 1, 2 and 3 shorted together and pins 4, 5 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	VAC	_	_	240	Vac
Forward current	lF	15	_	25	mA
Operating temperature	T _{opr}	-25	_	85	°C
Gate to cathode resistance	R _{GK}	_	10	27	kΩ
Gate to cathode capacity	C _{GK}	_	0.01	0.1	μF

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

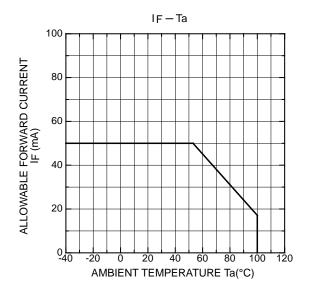


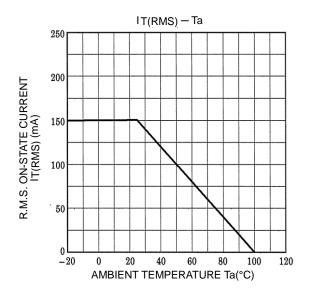
Individual Electrical Characteristics (Ta = 25°C)

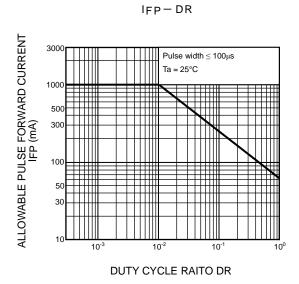
Characteristic		Symbol	Test Condition		Min	Тур.	Max	Unit
	Forward voltage	VF	I _F = 10 mA		1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V		_	_	10	μА
	Capacitance	Ст	VF = 0 V, f = 1 MHz		_	30	_	pF
	OFF-state current	IDRM	VAK = 600 V, RGK = 27 kΩ		_	_	5	μА
	Reverse current	IRRM	V _K A = 600 V, R _G K = 27 kΩ		_	_	5	μА
'n	ON-state voltage	V _{TM}	I _{TM} = 100 mA		_	_	1.45	V
Detector	Holding current	lн	R _{GK} = 27 kΩ		_	_	1	mA
De	OFF-state dv / dt	dv / dt	V _A K = 420 V, R _G K = 27 kΩ		5	_	_	V/μs
		V = 0 V,	Anode to gate	_	5	_		
	Capacitance	Cj	f = 1 MHz	Gate to cathode	_	500	_	pF

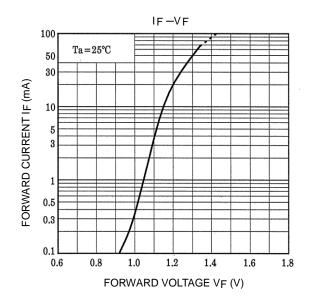
Coupled Characteristics (Ta = 25°C)

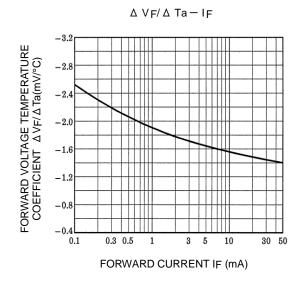
Characteristic	aracteristic Symbol Test Condition		Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	$V_{AK} = 6 \text{ V}, R_{GK} = 27 \text{ k}\Omega$	_	_	10	mA
Turn-on time	ton	$I_F = 30$ mA, $V_{AA} = 50$ V $R_{GK} = 27$ k Ω	_	15	_	μS
Capacitance (input to output)	Cs	Vs = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	Rs	Vs = 500 V, R.H.≤ 60 %	1×10 ¹²	10 ¹⁴	_	Ω
Isolation voltage	BVs	AC, 60 s	4000	_	_	V _{rms}

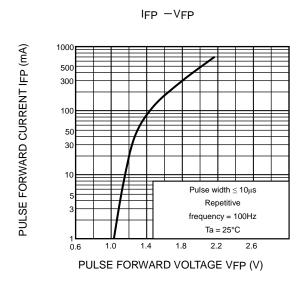




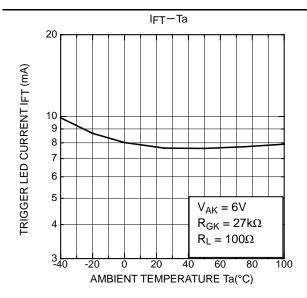


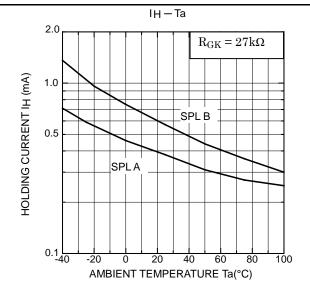


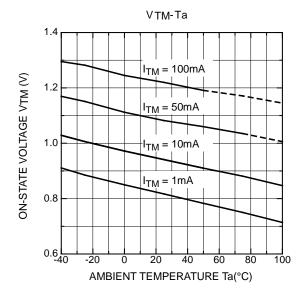




NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.







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EN 60747-5-5 Option (D4) Specification

Types : TLP48JF

Type designations for "option: (D4)", which are tested under EN 60747 requirements.

Ex.: TLP748JF (D4,F) D4 : EN 60747 option

F: [[G]]/RoHS COMPATIBLE (Note 1)

Note: Use TOSHIBA standard type number for safety standard application.

Ex.: TLP748JF (D4,F) \rightarrow TLP748JF

Note 1: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

EN 60747 Isolation Characteristics

Description			Rating	Unit	
Application classification					
for rated mains voltage≤300V _{rms} for rated mains voltage≤600V _{rms}			I-IV I-III	_	
Climatic classification			40/ 100 / 21	_	
Pollution degree		2	_		
TLPxxx type			890	Vpk	
Maximum operating insulation voltage	TLPxxxFtype	VIORM	1130	vpk	
Input to output test voltage, method A	TLPxxx type	- Vpr	1424	Vpk	
Vpr=1.6×V _{IORM} , type and sample test tp=10 s, partial discharge<5pC	TLPxxxFtype		1808		
Input to output test voltage, method B	TLPxxx type		1670		
Vpr=1.875×V _{IORM} , 100% production test t _p =1 s, partial discharge<5pC	TLPxxxFtype	Vpr	2120	Vpk	
Highest permissible overvoltage (transient overvoltage, t _{pr} = 60 s)			8000	Vpk	
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve) current (input current I_F , $P_{si} = 0$) power (output or total power dissipation) temperature			400 700 150	mA mW °C	
Insulation resistance, input-output Vio =500V, Ta=25°C Vio =500V, Ta=100°C Vio =500V, Ta=Ts			≥10 ¹² ≥10 ¹¹ ≥10 ⁹	Ω	

Insulation Related Specifications

		7.62mm pitch TLPxxx type	10.16mm pitch TLPxxxF type	
Minimum creepage distance	Cr	7.0mm	8.0mm	
Minimum clearance	CI	7.0mm	8.0mm	
Minimum insulation thickness ti		0.4mm		
Comperative tracking index	CTI	175		

- 1. If a printed circuit is incorporated, the creepage distance and clearance may be reduced below this value. (e.g. at a standard distance between soldering eye centres of 7.5mm). If this is not permissible, the user shall take suitable measures.
- 2. This photocoupler is suitable for 'safe electrical isolation' only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuits.

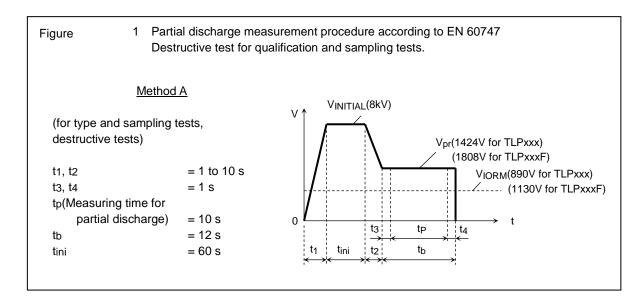
Note: The above marking is applied to the photocouplers that have been qualified according to option (D4) of EN 60747.

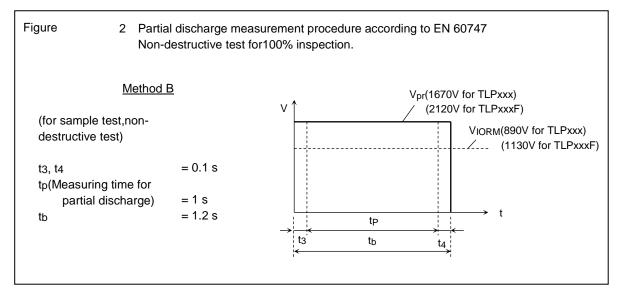


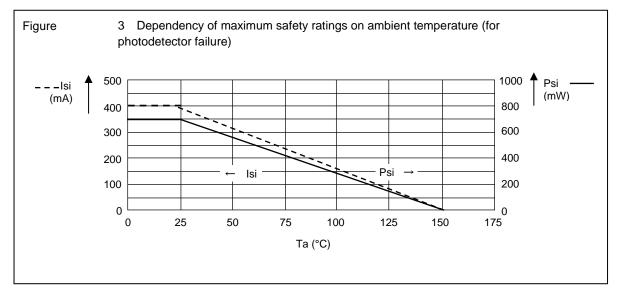
Marking on product for EN 60747:



Marking on Packing for EN 60747:







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