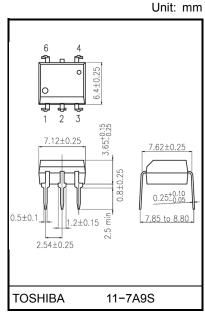
TOSHIBA Photocoupler IRED & Photo-Triac

TLP3064(S)

Office Machine
Household Use Equipment
Triac Driver
Solid State Relay

The TOSHIBA TLP3064(S) consists of a zero voltage crossing turn—on photo—triac optically coupled to an infrared emitting diode in a six lead plastic DIP package.

- Peak off-state voltage: 600V(min.)
- Trigger LED current: 3mA(max.)
- On-state current: 100mA(max.)
- Isolation voltage: 5000Vrms(min.)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349
- VDE-approved: EN 60747-5-5, EN 62368-1 (Note 1)



Weight: 0.39 g(typ.)

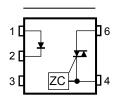
Note 1: When a VDE approved type is needed, please designate the **Option(D4)**.

7.62mm pitch 10.16mm pitch standard type (LF2)type

• Creepage distance: 7.0mm(min.) 8.0mm(min.)

Clearance: 7.0mm(min.) 8.0mm(min.)
Insulation thickness: 0.5mm(min.) 0.5mm(min.)

Pin Configurations(top view)



- 1: ANODE
- 2: CATHODE
- 3: N.C.
- 4: TERMINAL 1
- 6: TERMINAL 2

(ZC: Zero-cross Circuit)

Start of commercial production 1993-05

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic		Symbol	Rating	Unit	
	Forward current	lF	30	mA		
	Forward current derating (Ta ≥	ΔI _F / °C	-0.3	mA / °C		
Operati	Peak forward current (100μs pu	IFP	1	Α		
	Reverse voltage	V _R	5	V		
	Input power dissipation	PD	100	mW		
	Input power dissipation derating	ΔPD /°C	-1.0	mW/°C		
	Junction temperature	Tj	125	°C		
	Off-state output terminal voltage	V _{DRM}	600	V		
	On-state RMS current	Ta=25°C	l=(p, (o)	100	mA	
		Ta=70°C	I _{T(RMS)}	50		
<u></u>	On-state current derating (Ta ≥ 25°C)	ΔI _T / °C	-1.1	mA / °C		
Detector	Peak on-state current (100μs p	I _{TP}	2	Α		
	Peak nonrepetitive surge current (Pw=10ms, DC=10%)	I _{TSM}	1.2	А		
	Output power dissipation	РО	300	mW		
	Output power dissipation derati	⊿PO/°C	-3.0	mW/°C		
	Junction temperature	Tj	115	°C		
Storag	Storage temperature range			−55 to 150	°C	
Operat	Operating temperature range			-40 to 100	°C	
Lead s	Lead soldering temperature (10 s)			260	°C	
	solation voltage (AC, 60 s., R.H. ≤ 60 %) (Note 1)		BVS	5000	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Device considered a two terminal device=Pins 1, 2 and 3 shorted together and pins 4 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V _A C	_	_	240	Vac
Forward current	lF	4.5	6	7.5	mA
Peak on-state current	I _{TP}	_	_	1	Α
Operating temperature	Topr	-10	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

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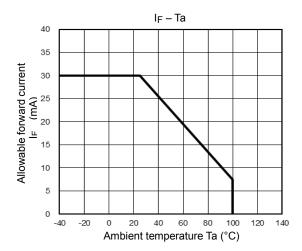
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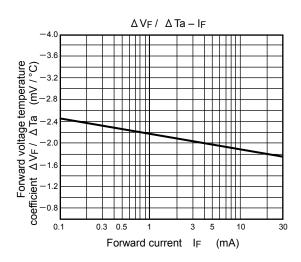
Individual Electrical Characteristics (Ta = 25°C)

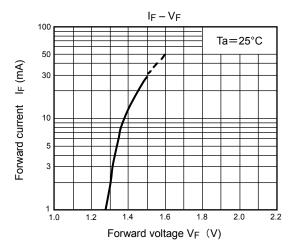
	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
LED	Forward voltage	VF	I _F = 10 mA	1.2	1.4	1.7	V
	Reverse current	I _R	V _R = 3 V	_	_	10	μА
	Capacitance	Ст	V=0 V, f = 1 MHz	_	30	_	pF
Detector	Peak off-state current	I _{DRM}	V _{DRM} = 600 V	_	10	1000	nA
	Peak on-state voltage	V _{TM}	I _{TM} = 100 mA	_	_	3.0	V
	Holding current	lн	_	_	0.6	_	mA
	Critical rate of rise of off–state voltage	dv / dt	V _{in} = 240 rms Ta = 85 °C	200	500	_	V / μs
	Critical rate of rise of commutating voltage	dv / dt(c)	V _{in} = 60 Vrms I _T = 15 mArms	_	0.2	_	V / μs

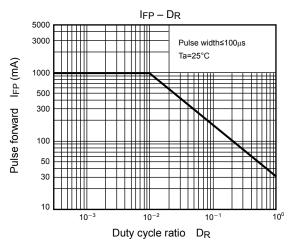
Coupled Electrical Characteristics (Ta = 25°C)

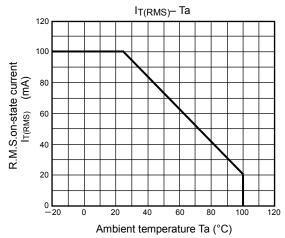
Characteristics	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	lfT	V _T = 3 V, resistive load	_	_	3	mA
Inhibit voltage	VIH	IF = rated IFT		1	50	V
Leakage in inhibited state	lін	I _F = rated I _{FT} V _T = rated V _{DRM}		_	600	μА
Capacitance input to output	Cs	V _S = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	Rs	V _S = 500V, R.H. ≤ 60 %	1×10 ¹²	10 ¹⁴	_	Ω
Isolation voltage	BVS	AC, 60 s	5000	ı	_	Vrms



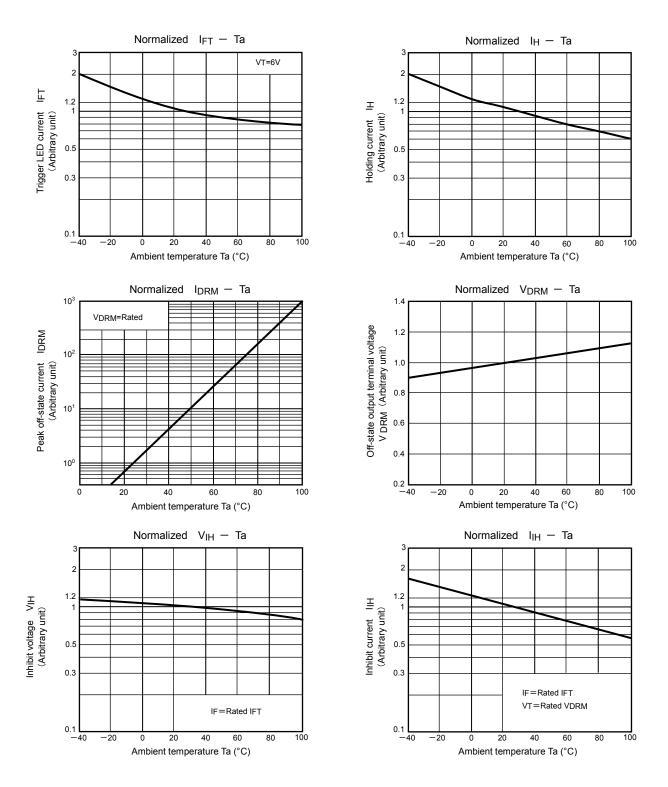








NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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