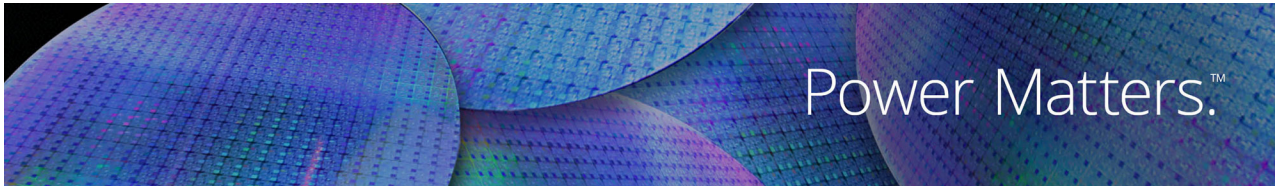


- [Quality](#)
- [Investors](#)
- [Sales Contacts](#)
- [Parametric Search](#)

Login

[Products & Services](#) [Applications](#) [Ordering](#) [Company](#) [Partners](#) [Support](#)



1. [Home](#)
2. [FPGA & SoC](#)
3. [Boards and Kits](#)
4. [M2S-HELLO-FPGA-KIT](#)

M2S-HELLO-FPGA-KIT

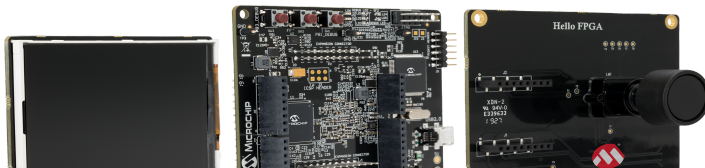
Product Status

In Production

[Overview](#) [Resources](#) [Ordering](#) [Support](#)

Say Hello to FPGAs

The Hello FPGA kit is a low cost, compact-sized, entry-level platform targeted towards end-users with low to medium FPGA knowledge. The kit supports powerful demos in Image Processing, Signal Processing, Artificial Intelligence, and can measure the live FPGA core power consumption while running designs. The kit also allows users to freeze the design while maintaining the i/o state for low power applications, using Flash Freeze mode. The kit features Arduino & Mikrobus connectors for flexibility when it comes to prototyping, and expansion kits, allowing for easy adoption in future projects.



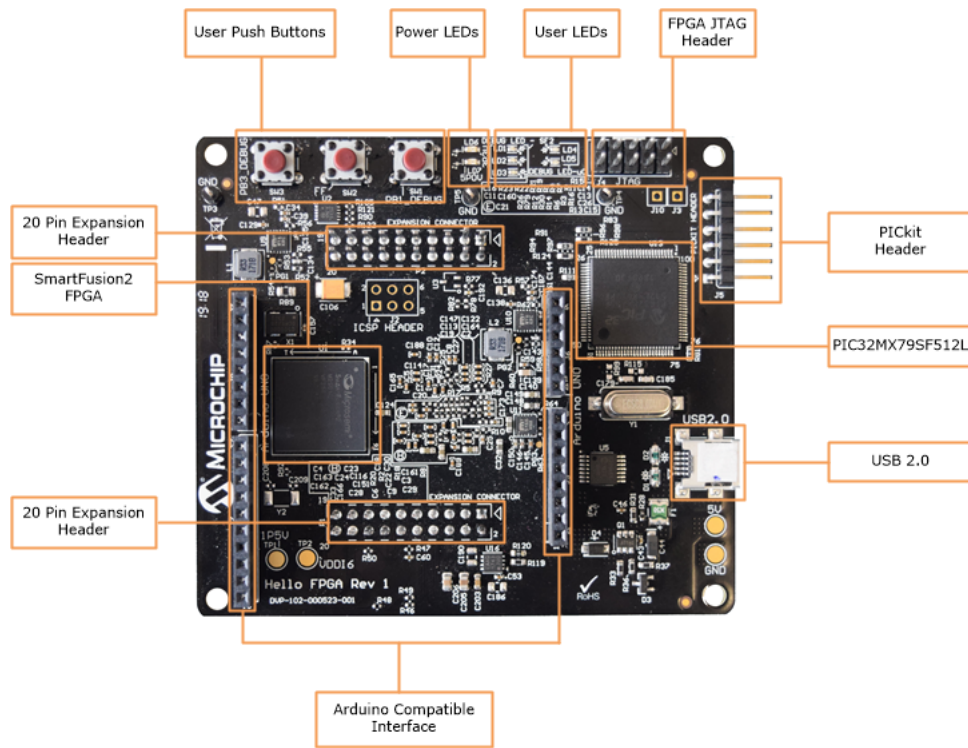
The Hello FPGA Kit demonstrates the following features:

- Artificial Intelligence demo: Uses Parallel Processing to do real-time hand-written digit recognition
- Digital Signal Processing demo: Creating FIR/FFT filters
- Power monitor GUI: Shows how fast the instant-on wake-up time is while displaying the power consumption for both operational and Flash*Freeze modes

Hello FPGA Board

Stay Informed





Kit Contents – M2S-HELLO-FPGA-KIT

Quantity	Description
1	FPGA mainboard with SmartFusion2 M2S010-1VFG256
1	Camera Sensor Board (LI-0V7725-MICRO v1.0)
1	LCD Board
1	USB 2.0 A to Mini-B cable
1	QuickStart Card

Reserve your Kit Now

The Hello FPGA kit will be available soon, fill out the form below to reserve your kit.

First Name: * **Last Name:** * **Email:** *

Job Title: * **Country:** *



Package **Box**
Carrier:

This part can be found in the following product categories:

- [FPGA & SoC](#) ▶ [Boards and Kits](#)

FPGA & SoC

- [FPGA & SoC Design Tools](#)
- [FPGAs](#)
- [Boards and Kits](#)
- [Rad-Tolerant FPGAs](#)
- [Getting Started](#)
- [SoC FPGAs](#)
- [Antifuse FPGAs](#)
- [FPGA Applications](#)
- [Key Technologies & Solutions](#)
- [FPGA & SoC Partners](#)
- [Mi-V RISC-V Ecosystem](#)
- [Low Power Leadership](#)

[Products](#) | [Applications](#) | [Company](#) | [Careers](#) | [Investors](#) | [Survey](#) | [Privacy Policy](#) | [Terms & Conditions](#) | [Product Disclaimer](#) | [Terms of Use](#) | [Blog](#) |  

© 2020 Microsemi, a wholly owned subsidiary of Microchip Technology Inc. All rights reserved.

Microsemi and the Microsemi logo are registered trademarks of Microsemi Corporation. All other trademarks and service marks are the property of their respective owners.

Stay Informed

