



HMC936LP6E

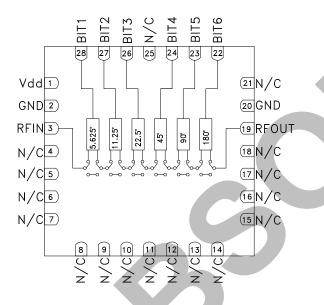
GaAs MMIC 6-BIT DIGITAL PHASE SHIFTER, 1.2 - 1.4 GHz

Typical Applications

The HMC936LP6E is ideal for:

- EW Receivers
- Weather & Military Radar
- Satellite Communications
- Beamforming Modules
- Phase Cancellation

Functional Diagram



Features

Low RMS Phase Error: 1.2° Low Insertion Loss: 5 dB High Linearity: +45 dBm Positive Control Logic 360° Coverage, LSB = 5.625° 28 Lead 6x6mm SMT Package: 36mm²

General Description

The HMC936LP6E is a 6-bit digital phase shifter which is rated from 1.2 to 1.4 GHz, providing 360 degrees of phase coverage, with a LSB of 5.625 degrees. The HMC936LP6E features very low RMS phase error of 1.2 degrees and extremely low inser-tion loss variation of ± 0.5 dB across all phase states. This high accuracy phase shifter is controlled with positive control logic of 0/+5V and requires no negative supply voltage. The HMC936LP6E is housed in a compact 6x6 mm plastic leadless SMT package and is internally matched to 50 Ohms with no external components.

Electrical Specifications $T_A = +25^{\circ}$ C, Vdd= +5V, Control Voltage = 0/ +5V, 50 Ohm System

| Parameter | Min. | Тур. | Max. | Units |
|---|------|------|------|-------|
| Frequency Range | 1.2 | | 1.4 | GHz |
| Insertion Loss | | 5 | 7 | dB |
| Input Return Loss | | 16 | | dB |
| Output Return Loss | | 17 | | dB |
| Phase Error | | ±5 | ± 10 | deg |
| RMS Phase Error | | 1.2 | | deg |
| Insertion Loss Variation | | ±0.5 | | dB |
| Input Power for 1 dB Compression | | 29 | | dBm |
| Input Third Order Intercept | | 45 | | dBm |
| Control Voltage Current | | 35 | 100 | μA |
| Bias Control Current | | 3 | 8 | mA |
| Switching Time (50% Vctl to 90% RF Amplitude) | | 250 | | ns |

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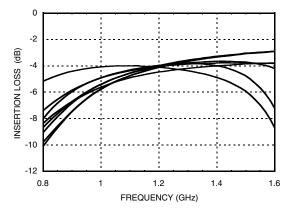
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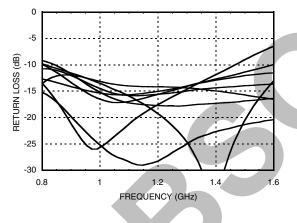
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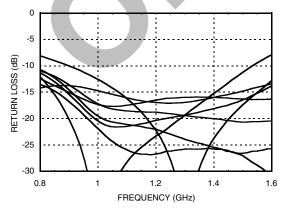
Insertion Loss, Major States Only



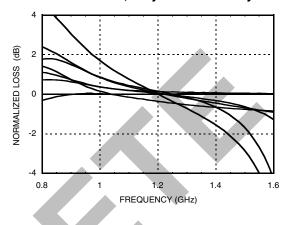
Input Return Loss, Major States Only



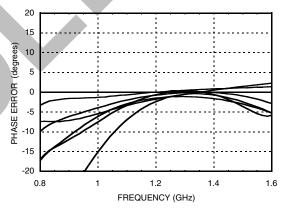
Output Return Loss, Major States Only



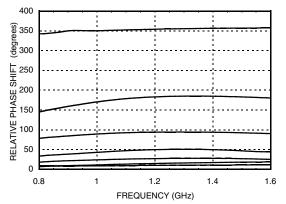
Normalized Loss, Major States Only



Phase Error, Major States Only



Relative Phase Shift Major States Including All Bits



PHASE SHIFTERS - DIGITAL - SMT

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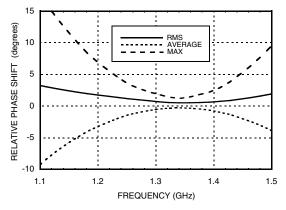
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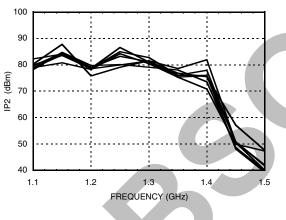




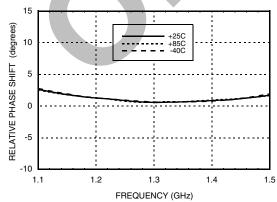
Relative Phase Shift, RMS, Average, Max, All States



Input IP2, Major States Only



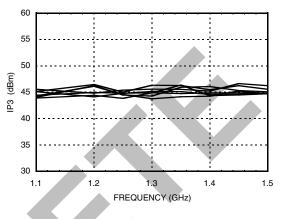
RMS Phase Error vs. Temperature



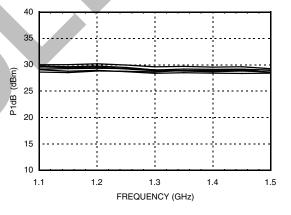
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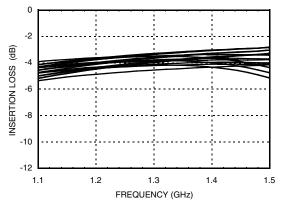
Input IP3, Major States Only



Input P1dB, Major States Only



Insertion Loss vs. Temperature, Major States Only



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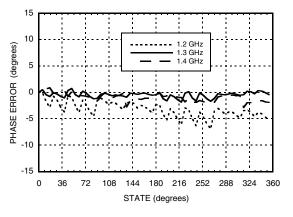
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ROHS V

Phase Error vs. State



Absolute Maximum Ratings

| Input Power (RFIN) | 33 dBm (T= +85 °C) |
|--|--------------------|
| Bias Voltage Range (Vdd) | -0.2 to +12V |
| Channel Temperature (Tc) | 150 °C |
| Thermal Resistance (channel to ground paddle) | 100 °C/W |
| Storage Temperature | -65 to +150 °C |
| Operating Temperature | -40 to +85 °C |
| ESD Sensitivity (HBM) | Class 1A |



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

Truth Table

| | | Control V | /oltage Inp | out | | Phase Shift |
|----------|-----------|------------|-------------|-----------------------------|-------|---------------------------|
| Bit 1 | Bit 2 | Bit 3 | Bit 4 | Bit 5 | Bit 6 | (Degrees) RFIN - RFOUT |
| 1 | 1 | 1 | 1 | 1 | 1 | Reference* |
| 0 | 1 | 1 | 1 | 1 | 1 | 5.625 |
| 1 | 0 | 1 | 1 | 1 | 1 | 11.25 |
| 1 | 1 | 0 | 1 | 1 | 1 | 22.5 |
| 1 | 1 | 1 | 0 | 1 | 1 | 45.0 |
| 1 | 1 | 1 | 1 | 0 | 1 | 90.0 |
| 1 | 1 | 1 | 1 | 1 | 0 | 180.0 |
| 0 | 0 | 0 | 0 | 0 | 0 | 354.375 |
| approxir | nately eq | ual to the | sum of the | will provid e bits selec | • | shift |
| *Referer | nce corre | sponds to | monoton | ic setting | | |

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Bias Voltage & Current

| Vdd | Idd |
|-----|------|
| 5.0 | 3 mA |

Control Voltage

| State | | Bias Condition |
|-------|----------|---------------------------|
| | Low (0) | 0 to 0.2 Vdc |
| | High (1) | Vdd ±0.2 Vdc @ 35 μΑ Typ. |

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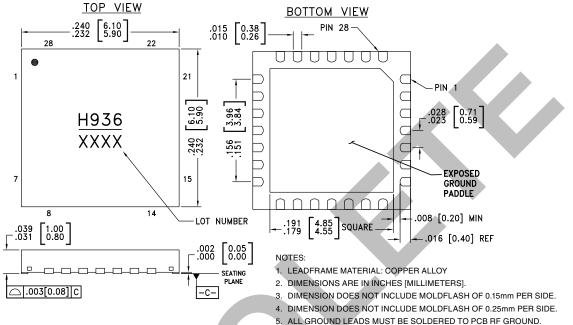
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Outline Drawing



6. CLASSIFIED AS MOISTURE SENSITIVITY LEVEL (MSL) 1.

Package Information

| Part Number | Package Body Material | Lead Finish | MSL Rating | Package Marking ^[1] |
|-------------|--|---------------|---------------------|--------------------------------|
| HMC936LP6E | RoHS-compliant Low Stress Injection Molded Plastic | 100% matte Sn | MSL1 ^[2] | <u>H936</u> XXXX |

[2] Max peak reflow temperature of 260 °C

[1] 4-Digit lot number XXXX

Pin Descriptions

| Pin Number | Function | Description | Interface Schematic |
|---------------------|---------------------------------------|--|---------------------|
| 1 | Vdd | Voltage supply. | |
| 2, 20 | GND | These pins and exposed ground paddle must be connected to RF/DC ground. | |
| 3 | RFIN | This port is DC coupled and matched to 50 Ohms. | |
| 4 - 18, 21, 25 | N/C | The pins are not connected internally; however, all data shown herein was measured with these pins connected to RF/DC ground externally. | |
| 19 | RFOUT | This port is DC coupled and matched to 50 Ohms. | |
| 22 - 24, 26 - 28 | BIT6, BIT5, BIT4, BIT3, BIT2, BIT1 | Control Input. See truth table and control voltage tables. | |

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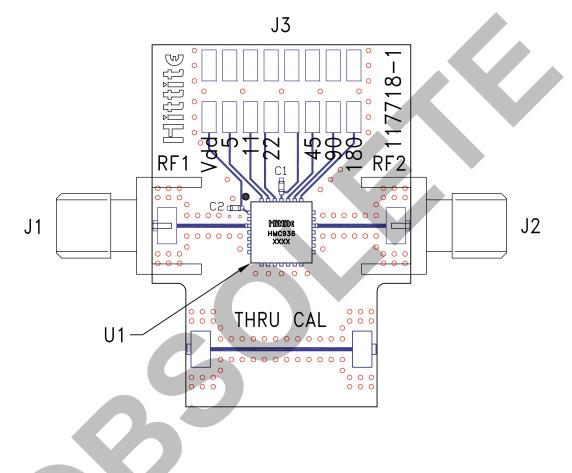


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Evaluation PCB



List of Materials for Evaluation PCB 117720 [1][3]

| Item | | Description |
|---------|--|--|
| J1 - J2 | | PCB Mount SMA RF Connector |
| J3 | | Header 2mm, 16 Pin |
| C1, C2 | | 1000 pF Capacitor, 0402 Pkg. |
| U1 | | HMC936LP6E 6-Bit Digital Phase Shifter |
| PCB [2] | | 117718 Evaluation PCB |

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

[3] Please refer to part's pin description and functional diagram for pin out assignments on evaluation board. The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.

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