

Is Now Part of



## **ON Semiconductor**®

# To learn more about ON Semiconductor, please visit our website at <u>www.onsemi.com</u>

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="https://www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to <a href="https://www.onsemi.com">Fairchild\_questions@onsemi.com</a>.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or unavteries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is and its officers, employees, even if such claim any manner.

April 2013



## **FJPF2145** ESBC<sup>™</sup> Rated NPN Power Transistor

## ESBC Features (FDC655 MOSFET)

V <sub>CS(ON)</sub>	I <sub>C</sub>	Equiv. R <sub>CS(ON)</sub> <sup>(1)</sup>
0.21 V	2 A	0.105 Ω

- · Low Equivalent On Resistance
- Very Fast Switch: 150 kHz
- Wide RBSOA: Up to 1100 V
- Avalanche Rated
- Low Driving Capacitance, no Miller Capacitance
- Low Switching Losses
- · Reliable HV Switch: No False Triggering due to High dv/dt Transients

## Applications

- High-Voltage, High-Speed Power Switches
- Emitter-Switched Bipolar/MOSFET Cascode (ESBC<sup>™</sup>)
- Smart Meters, Smart Breakers, SMPS, **HV Industrial Power Supplies**
- Motor Drivers and Ignition Drivers

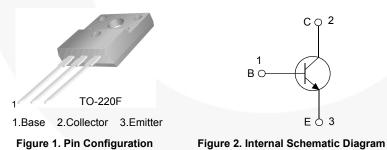
## Description

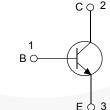
The FJPF2145 is a low-cost, high-performance power switch designed to provide the best performance when used in an ESBC<sup>™</sup> configuration in applications such as: power supplies, motor drivers, smart grid, or ignition switches. The power switch is designed to operate up to 1100 volts and up to 5 amps, while providing exceptionally low on-resistance and very low switching losses.

The ESBC<sup>™</sup> switch can be driven using off-the-shelf power supply controllers or drivers. The ESBC<sup>™</sup> MOS-FET is a low-voltage, low-cost, surface-mount device that combines low-input capacitance and fast switching. The ESBC<sup>™</sup> configuration further minimizes the required driving power because it does not have Miller capacitance.

The FJPF2145 provides exceptional reliability and a large operating range due to its square reverse-bias-safe-operating-area (RBSOA) and rugged design. The device is avalanche rated and has no parasitic transistors, so is not prone to static dv/dt failures.

The power switch is manufactured using a dedicated high-voltage bipolar process and is packaged in a highvoltage TO-220F package.





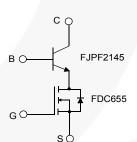


Figure 3. ESBC Configuration<sup>(2)</sup>

## **Ordering Information**

Part Number	Marking	Package	Packing Method
FJPF2145TU	J2145	TO-220F	TUBE

1

#### Notes:

- 1. Figure of Merit.
- 2. Other Fairchild MOSFETs can be used in this ESBC application.

www.fairchildsemi.com

## Absolute Maximum Ratings<sup>(3)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted..

Symbol	Parameter	Value	Units	
V <sub>CBO</sub>	Collector-Base Voltage	1100	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	800	V	
V <sub>EBO</sub>	Emitter-Base Voltage	7	V	
۱ <sub>C</sub>	Collector Current (DC)	5	А	
I <sub>B</sub>	Base Current	1.5	A	
P <sub>C</sub>	Collector Dissipation ( $T_C = 25^{\circ}C$ )	40	W	
ТJ	Operating and Junction Temperature Range	-55 to +125	°C	
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C	
EAR <sup>(4)</sup>	Avalanche Energy (T <sub>J</sub> = 25°C, 1.2 mH)	15	mJ	

Notes:

3. Pulse test is pulse width  $\leq$  5 ms, duty cycle  $\leq$  10%.

4. Lab characterization data only for reference.

## **Thermal Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Units
R <sub>θjc</sub>	Thermal Resistance, Junction to Case	3.125	°C/W
R <sub>θja</sub>	Thermal Resistance, Junction to Ambient	70.44	°C/W

## Electrical Characteristics<sup>(5)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Volt- age	I <sub>C</sub> = 1 mA, I <sub>E</sub> = 0	1100			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 5 mA, I <sub>B</sub> = 0	800			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 1 mA, I <sub>C</sub> = 0	7			V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> = 800 V, I <sub>E</sub> = 0			10	μA
I <sub>EBO</sub>	Emitter Cut-off Current	V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0			10	μA
h <sub>FE1</sub>	DC Current Gain	$V_{CE} = 5 V, I_{C} = 0.2 A$	20		40	
h <sub>FE2</sub>		V <sub>CE</sub> = 5 V, I <sub>C</sub> = 1 A	8			
		I <sub>C</sub> = 0.25 A, I <sub>B</sub> = 0.05 A		0.051		V
V (pot)	Collector-Emitter Saturation Volt- age	I <sub>C</sub> = 0.5 A, I <sub>B</sub> = 0.167 A		0.055		V
V <sub>CE</sub> (sat)		I <sub>C</sub> = 1 A, I <sub>B</sub> = 0.33 A		0.085		V
		I <sub>C</sub> = 1.5 A, I <sub>B</sub> = 0.3 A		0.159	2.000	V
		I <sub>C</sub> = 500 mA, I <sub>B</sub> = 50 mA		0.756		V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = 1.5 A, I <sub>B</sub> = 0.3 A		0.840	1.500	V
		I <sub>C</sub> = 2 A, I <sub>B</sub> = 0.4 A		0.863		V
CIB	Input Capacitance	V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0, f = 1 MHz		1.618		pF
C <sub>OB</sub>	Output Capacitance	V <sub>CB</sub> = 200 V, I <sub>E</sub> = 0, f = 1 MHz		11.39		pF
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 0.2 A		15		MHz

### Note:

5. Pulse test is pulse width  $\leq$  5 ms, duty cycle  $\leq$  10%.

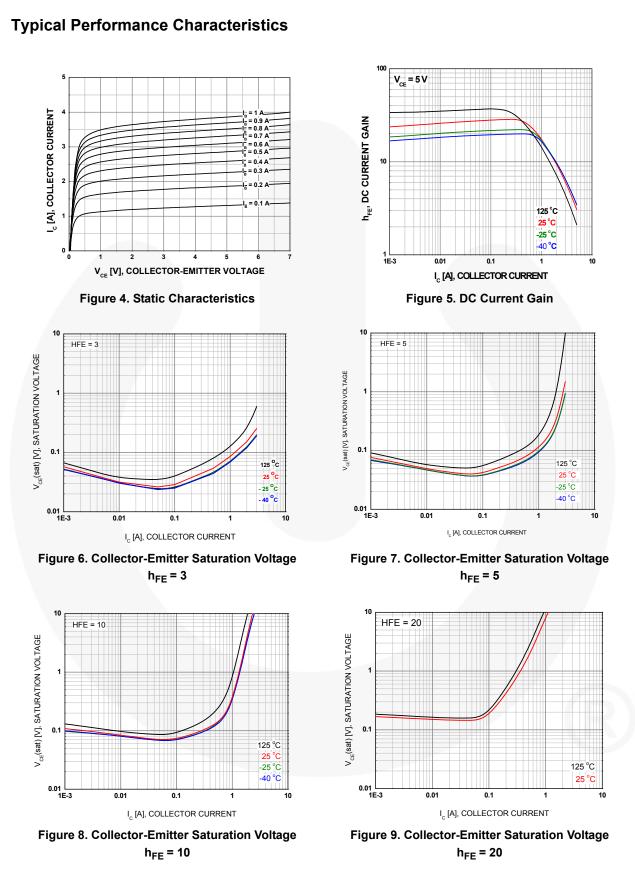
## ESBC-Configured Electrical Characteristics<sup>(6)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
f <sub>T</sub>	Current Gain Bandwidth Product	I <sub>C</sub> = 0.1 A, V <sub>CE</sub> = 10 V		28.40		MHz
lt <sub>f</sub>	Inductive Current Fall Time			95		ns
t <sub>s</sub>	Inductive Storage Time	$V_{CC}$ = 100 V, $V_{GS}$ = 10 V, $R_{G}$ = 4 7 $\Omega$ ,		0.13		ns
Vt <sub>f</sub>	Inductive Voltage Fall Time	$V_{Clamp}$ = 500 V, I <sub>C</sub> = 0.5 A, I <sub>B</sub> = 0.05 A, H <sub>FE</sub> = 10, L <sub>C</sub> = 166 µH,		135		ns
Vt <sub>r</sub>	Inductive Voltage Rise Time	SRF = 684 kHz		80		ns
t <sub>c</sub>	Inductive Crossover Time			115		ns
lt <sub>f</sub>	Inductive Current Fall Time			50		ns
t <sub>s</sub>	Inductive Storage Time	$V_{CC}$ = 100 V, $V_{GS}$ = 10 V, $R_{G}$ = 47 $\Omega$ ,		0.34		ns
Vt <sub>f</sub>	Inductive Voltage Fall Time	V <sub>Clamp</sub> = 500 V, I <sub>C</sub> = 1 A, I <sub>B</sub> = 0.2 A, H <sub>FE</sub> = 5, L <sub>C</sub> = 166 μH,		150		ns
Vt <sub>r</sub>	Inductive Voltage Rise Time	SRF = 684 kHz		60		ns
t <sub>c</sub>	Inductive Crossover Time			95		ns
V <sub>CSW</sub>	Maximum Collector- Source Voltage at Turn-off without Snubber	h <sub>FE</sub> = 5, I <sub>C</sub> = 2 A	1100			V
I <sub>GS(OS)</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20 V		1		nA
		$V_{GS}$ = 10 V, I <sub>C</sub> =2 A, I <sub>B</sub> = 0.67 A, h <sub>FE</sub> = 3		0.209		V
M	Collector-Source On Voltage	$V_{GS}$ = 10 V, $I_{C}$ = 1 A, $I_{B}$ = 0.33 A, $h_{FE}$ = 3		0.114		V
V <sub>CS(ON)</sub>		$V_{GS}$ = 10 V, I <sub>C</sub> = 0.5 A,I <sub>B</sub> = 0.17 A, h <sub>FE</sub> = 3		0.068		V
		$V_{GS}$ = 10 V, I <sub>C</sub> = 0.3 A, I <sub>B</sub> = 0.06 A, h <sub>FE</sub> = 5		0.062		V
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{BS} = V_{GS, I_B} = 250 \ \mu A$		1.9		V
C <sub>iss</sub>	Input Capacitance $(V_{GS} = V_{CB} = 0)$	V <sub>CS</sub> = 25 V, f = 1 MHz		470		pF
Q <sub>GS(tot)</sub>	Gate-Source Change V <sub>CB</sub> = 0	$V_{GS}$ = 10 V, I <sub>C</sub> = 6.3 A, V <sub>CS</sub> = 25 V		9		nC
	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6.3 \text{ A}$			21		mΩ
R <sub>DS(ON)</sub>	Static Drain-to-Source On Resistance	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 5.5 A		26		mΩ
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.3 A, T <sub>J</sub> = 125°C		30		mΩ

#### Note:

6. A typical FDC655 MOSFET was used for the specifications above. Values could vary if other Fairchild MOSFETs are used.

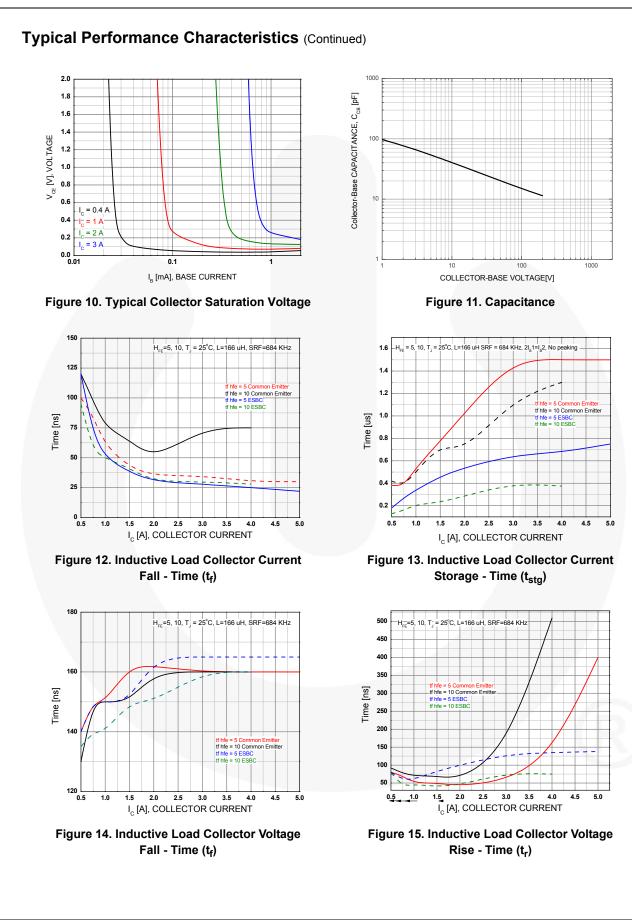


© 2013 Fairchild Semiconductor Corporation FJPF2145 Rev. 1.0.0

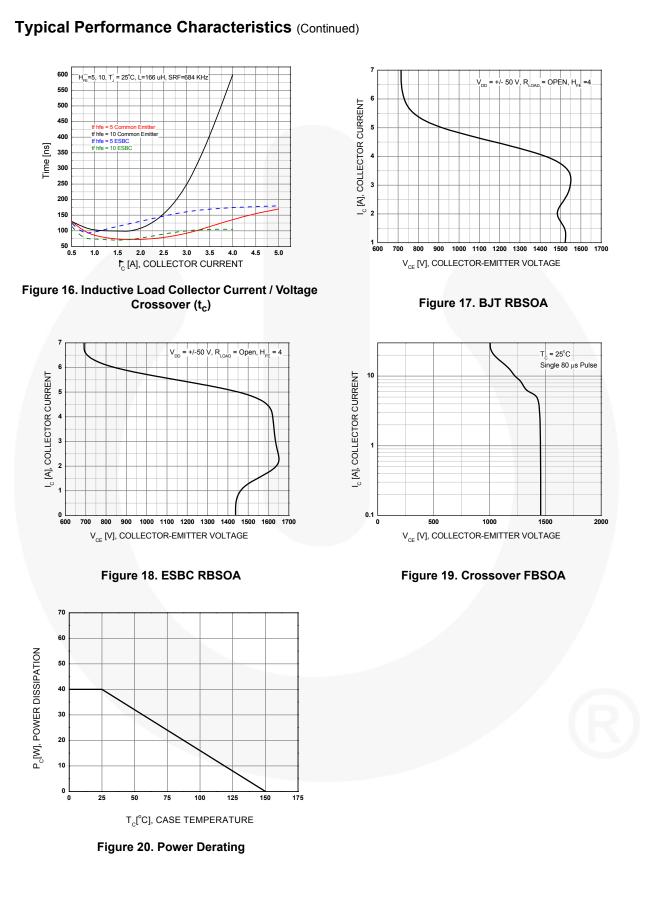
FJPF2145 —

ESBC<sup>™</sup> Rated NPN Power Transistor

FJPF2145 — ESBC<sup>™</sup> Rated NPN Power Transistor

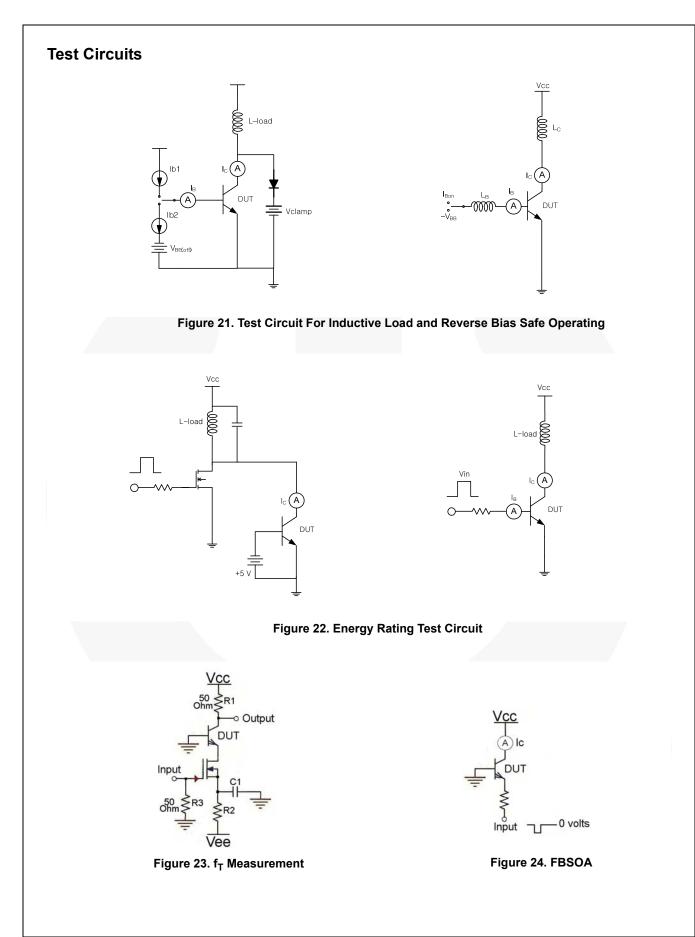


© 2013 Fairchild Semiconductor Corporation FJPF2145 Rev. 1.0.0

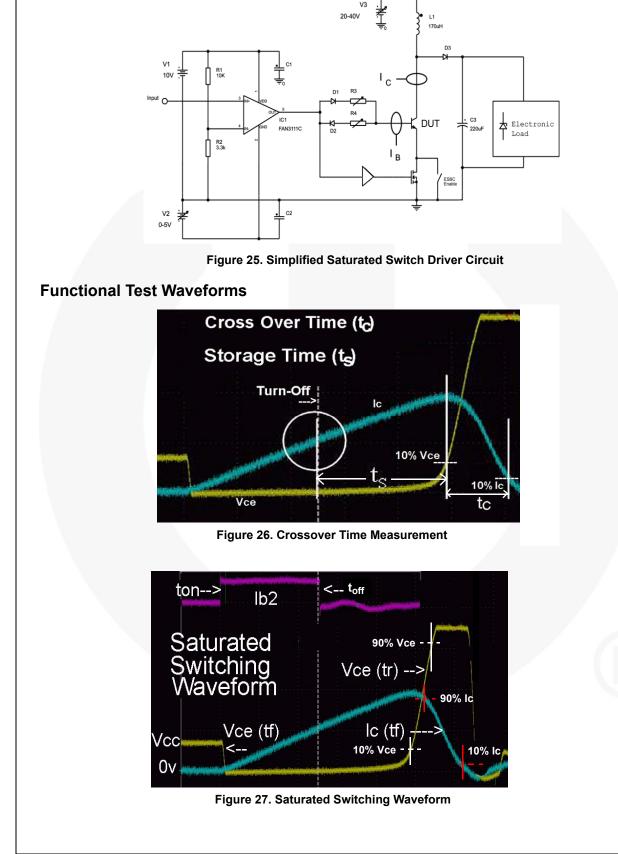


FJPF2145 — ESBC<sup>™</sup> Rated NPN Power Transistor

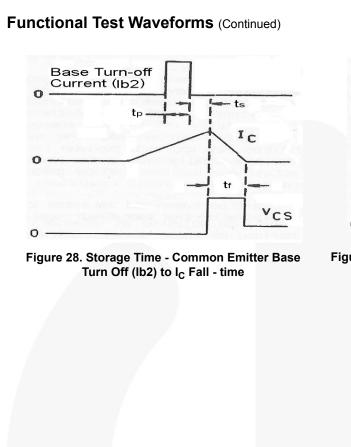
© 2013 Fairchild Semiconductor Corporation FJPF2145 Rev. 1.0.0



© 2013 Fairchild Semiconductor Corporation FJPF2145 Rev. 1.0.0



Test Circuits (Continued)



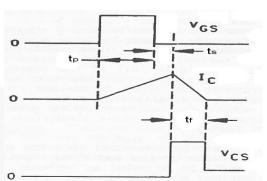
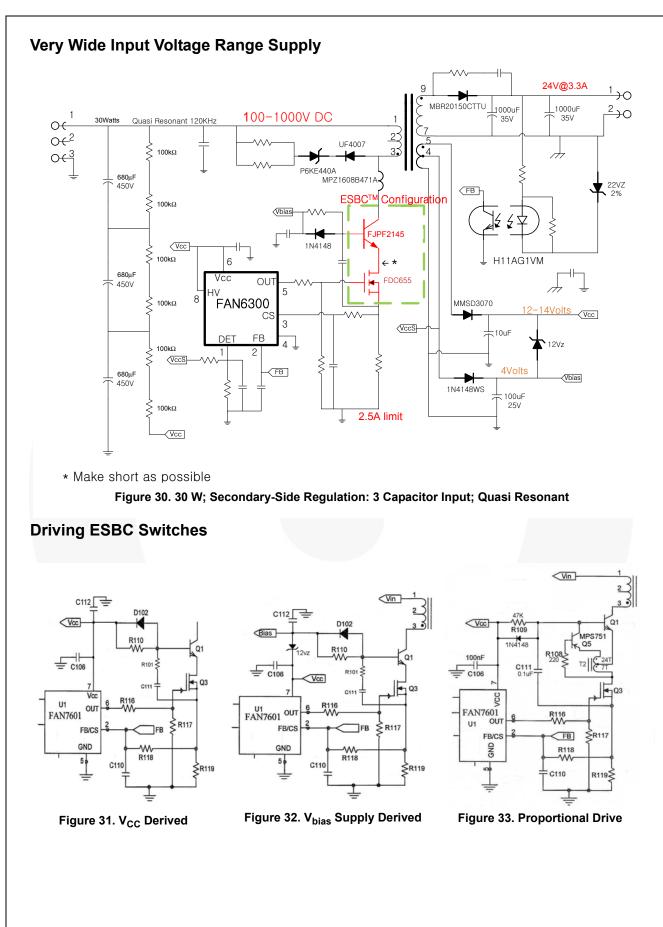
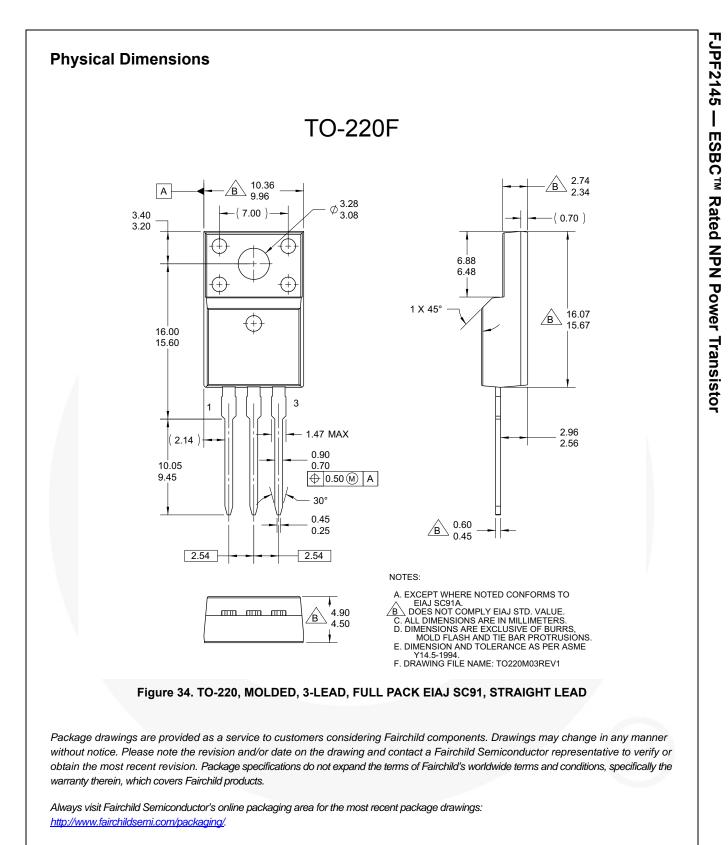


Figure 29. Storage Time - ESBC FET Gate (off) to I<sub>C</sub> Fall - time



© 2013 Fairchild Semiconductor Corporation FJPF2145 Rev. 1.0.0



For current tape and reel specifications, visit Fairchild Semiconductor's online packaging area: <u>http://www.fairchildsemi.com/dwg/TO/TO220M03.pdf</u>.

#### FAIRCHILD

SEMICONDUCTOR

#### TRADEMARKS

AX-CAP®

Build it Now™

CorePOWER™

CROSSVOLT™

CorePLUS™

**DEUXPEED**<sup>®</sup>

Dual Cool™

**EcoSPARK**®

ESBC™

F®

FACT

FAST®

FastvCore™

FFTBench™

Fairchild®

EfficientMax™

BitSiC™

CTL™

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks. 2Cool™ **FPS™** Sync-Lock™  $(\mathbf{I})_{\mathbf{s}}$ AccuPower™ F-PFS™

Global Power Resource<sup>SM</sup> GreenBridge™ Green FPS™ Green FPS™ e-Series™ Gmax™ **GTO™** IntelliMAX™ Current Transfer Logic™ **ISOPLANAR™** Making Small Speakers Sound Louder and Better™ MegaBuck™ MICROCOUPLER™ MicroFET™ MicroPak™ MicroPak2™ Fairchild Semiconductor® MillerDrive™ FACT Quiet Series™ MotionMax™ mWSaver™ OptoHiT™ **OPTOLOGIC® OPTOPLANAR**<sup>®</sup>

FRFET®

PowerTrench<sup>®</sup> PowerXS<sup>™</sup> Programmable Active Droop™ QFET QS™ Quiet Series™ RapidConfigure™  $\bigcirc$ <sup>TM</sup> Saving our world, 1mW/W/kW at a time™ SignalWise™ SmartMax™ SMART START™ Solutions for Your Success™ SPM<sup>®</sup> STEALTH™ SuperFET<sup>®</sup> SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS<sup>®</sup> SvncFET™

TinyBoost™ TinyBuck™ TinyCalc™ TinyLogic® TINYOPTO™ TinyPower™ TinyPWM™

TinyWire™ TranSiC™ TriFault Detect™ TRUECURRENT®\* սSerDes™

## 

UHC Ultra FRFET™ UniFET™ VCX™ VisualMax™ VoltagePlus™ XS™

\* Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

#### As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- 2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

#### PRODUCT STATUS DEFINITIONS

#### Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 164

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent-Marking.pdf</u>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor has against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death ass

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81–3–5817–1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

© Semiconductor Components Industries, LLC

Downloaded from Arrow.com.