# BFU660F

# NPN wideband silicon RF transistor

Rev. 1 — 11 January 2011

**Product data sheet** 

### 1. Product profile

### 1.1 General description

NPN silicon microwave transistor for high speed, low noise applications in a plastic, 4-pin dual-emitter SOT343F package.

#### **CAUTION**



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

#### 1.2 Features and benefits

- Low noise high linearity RF transistor
- High output third-order intercept point 27 dBm at 1.8 GHz
- 40 GHz f<sub>T</sub> silicon technology

#### 1.3 Applications

- Analog/digital cordless applications
- X-band high output buffer amplifier
- ZigBee
- SDARS second stage LNA
- LTE, cellular, UMTS



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#### **NPN** wideband silicon RF transistor

#### 1.4 Quick reference data

Table 1. Quick reference data

|                     | _                                     |   |     |     | _    |     |      |
|---------------------|---------------------------------------|---|-----|-----|------|-----|------|
| Symbol              | Parameter                             | Conditions  |     | Min | Тур  | Max | Unit |
| $V_{CBO}$           | collector-base voltage                | open emitter  |     | -   | -    | 16  | V    |
| $V_{CEO}$           | collector-emitter voltage             | open base   |     | -   | -    | 5.5 | V    |
| $V_{EBO}$           | emitter-base voltage                  | open collector  |     | -   | -    | 2.5 | V    |
| I <sub>C</sub>      | collector current                     |   |     | -   | 30   | 60  | mΑ   |
| P <sub>tot</sub>    | total power dissipation               | T <sub>sp</sub> ≤ 90 °C   | [1] | -   | -    | 225 | mW   |
| h <sub>FE</sub>     | DC current gain                       | $I_C = 10 \text{ mA}; V_{CE} = 2 \text{ V};$<br>$T_j = 25 \text{ °C}$                                     |     | 90  | 135  | 180 |      |
| $C_{CBS}$           | collector-base capacitance            | $V_{CB} = 2 V$ ; $f = 1 MHz$  |     | -   | 138  | -   | fF   |
| f <sub>T</sub>      | transition frequency                  | $I_C$ = 20 mA; $V_{CE}$ = 1 V;<br>f = 2 GHz; $T_{amb}$ = 25 °C  |     | -   | 21   | -   | GHz  |
| IP3 <sub>O</sub>    | output third-order intercept point    | $I_C$ = 40 mA; $V_{CE}$ = 4 V;<br>f = 5.8 GHz; $T_{amb}$ = 25 °C  |     | -   | 28   | -   | dBm  |
| G <sub>p(max)</sub> | maximum power gain                    | $I_C = 30 \text{ mA}; V_{CE} = 1 \text{ V};$<br>f = 1.8 GHz; $T_{amb} = 25 \text{ °C}$                    | [2] | -   | 24   | -   | dB   |
| NF                  | noise figure                          | $I_C$ = 6 mA; $V_{CE}$ = 2 V;<br>f = 1.8 GHz; $\Gamma_S$ = $\Gamma_{opt}$ ;<br>$T_{amb}$ = 25 °C          |     | -   | 0.65 | -   | dB   |
| P <sub>L(1dB)</sub> | output power at 1 dB gain compression | $I_{C}$ = 60 mA; $V_{CE}$ = 4 V;<br>$Z_{S}$ = $Z_{L}$ = 50 $\Omega$ ;<br>$f$ = 1.8 GHz; $T_{amb}$ = 25 °C |     | -   | 17   | -   | dBm  |

<sup>[1]</sup>  $T_{sp}$  is the temperature at the solder point of the emitter lead.

## 2. Pinning information

Table 2. Discrete pinning

|     | 21001010    |                    |                |
|-----|-------------|--------------------|----------------|
| Pin | Description | Simplified outline | Graphic symbol |
| 1   | emitter     |                    |                |
| 2   | base        | 3 4                | 4<br>          |
| 3   | emitter     |                    | 2              |
| 4   | collector   |                    | 1, 3           |
|     |             | 2 1                | mbb159         |

## 3. Ordering information

Table 3. Ordering information

| Type number | Package |   |         |
|-------------|---------|---|---------|
|             | Name    | Description   | Version |
| BFU660F     | -       | plastic surface-mounted flat pack package; reverse pinning; 4 leads | SOT343F |

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<sup>[2]</sup>  $G_{p(max)}$  is the maximum power gain, if K > 1. If K < 1 then  $G_{p(max)} = MSG$ .

## 4. Marking

Table 4. Marking

| Type number | Marking | Description               |
|-------------|---------|---------------------------|
| BFU660F     | D3*     | * = p : made in Hong Kong |
|             |         | * = t : made in Malaysia  |
|             |         | * = w : made in China     |

## 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

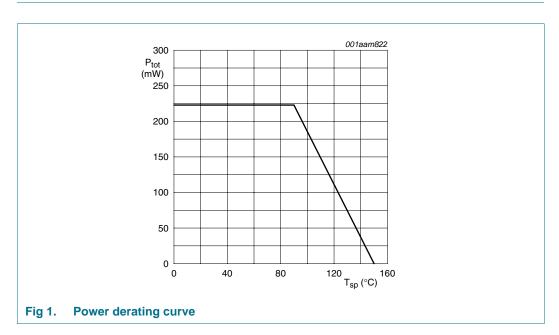
| Symbol           | Parameter                 | Conditions                 | Min          | Max  | Unit |
|------------------|---------------------------|----------------------------|--------------|------|------|
| $V_{CBO}$        | collector-base voltage    | open emitter               | -            | 16   | V    |
| $V_{CEO}$        | collector-emitter voltage | open base                  | •            | 5.5  | V    |
| V <sub>EBO</sub> | emitter-base voltage      | open collector             | •            | 2.5  | V    |
| I <sub>C</sub>   | collector current         |                            | -            | 60   | mA   |
| P <sub>tot</sub> | total power dissipation   | $T_{sp} \le 90  ^{\circ}C$ | <u>[1]</u> _ | 225  | mW   |
| T <sub>stg</sub> | storage temperature       |                            | -65          | +150 | °C   |
| Tj               | junction temperature      |                            | -            | 150  | °C   |
|                  |                           |                            |              |      |      |

<sup>[1]</sup>  $T_{sp}$  is the temperature at the solder point of the emitter lead.

### 6. Thermal characteristics

Table 6. Thermal characteristics

| Symbol         | Parameter  | Conditions | Тур | Unit |
|----------------|--|------------|-----|------|
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point |            | 270 | K/W  |



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### 7. Characteristics

**Table 7. Characteristics** 

 $T_i = 25$  °C unless otherwise specified

| Symbol                         | Parameter                             | Conditions   | Min | Тур  | Max | Unit |
|--------------------------------|---------------------------------------|--|-----|------|-----|------|
| $V_{(BR)CBO}$                  | collector-base breakdown voltage      | $I_C = 2.5 \mu A; I_E = 0 \text{ mA}$  | 16  | -    | -   | V    |
| V <sub>(BR)CEO</sub>           | collector-emitter breakdown voltage   | $I_C = 1 \text{ mA}$ ; $I_B = 0 \text{ mA}$  | 5.5 | -    | -   | V    |
| Ic                             | collector current                     |  | -   | 30   | 60  | mΑ   |
| I <sub>CBO</sub>               | collector-base cut-off current        | $I_E = 0 \text{ mA}; V_{CB} = 8 \text{ V}$   | -   | -    | 100 | nΑ   |
| h <sub>FE</sub>                | DC current gain                       | $I_C = 10 \text{ mA}; V_{CE} = 2 \text{ V}$  | 90  | 135  | 180 |      |
| C <sub>CES</sub>               | collector-emitter capacitance         | $V_{CB} = 2 V$ ; $f = 1 MHz$   | -   | 297  | -   | fF   |
| C <sub>EBS</sub>               | emitter-base capacitance              | V <sub>EB</sub> = 0.5 V; f = 1 MHz   | -   | 664  | -   | fF   |
| C <sub>CBS</sub>               | collector-base capacitance            | $V_{CB} = 2 V$ ; $f = 1 MHz$   | -   | 138  | -   | fF   |
| f <sub>T</sub>                 | transition frequency                  | $I_C$ = 20 mA; $V_{CE}$ = 1 V; f = 2 GHz;<br>$T_{amb}$ = 25 °C                                   | -   | 21   | -   | GHz  |
| G <sub>p(max)</sub>            | maximum power gain                    | $I_C = 30 \text{ mA}; V_{CE} = 1 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$                       | [1] |      |     |      |
|                                |                                       | f = 1.5 GHz  | -   | 25   | -   | dB   |
|                                |                                       | f = 1.8 GHz  | -   | 24   | -   | dB   |
|                                |                                       | f = 2.4 GHz  | -   | 22   | -   | dB   |
|                                |                                       | f = 5.8 GHz  | -   | 12.5 | -   | dB   |
| s <sub>21</sub>   <sup>2</sup> | insertion power gain                  | $I_C = 30 \text{ mA}; V_{CE} = 1 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$                       |     |      |     |      |
|                                |                                       | f = 1.5 GHz  | -   | 20   | -   | dB   |
|                                |                                       | f = 1.8 GHz  | -   | 18.5 | -   | dB   |
|                                |                                       | f = 2.4 GHz  | -   | 16   | -   | dB   |
|                                |                                       | f = 5.8 GHz  | -   | 8.5  | -   | dB   |
| NF                             | noise figure                          | $I_C$ = 6 mA; $V_{CE}$ = 2 V; $\Gamma_S$ = $\Gamma_{opt}$ ; $\Gamma_{amb}$ = 25 °C               |     |      |     |      |
|                                |                                       | f = 1.5 GHz  | -   | 0.60 | -   | dB   |
|                                |                                       | f = 1.8 GHz  | -   | 0.65 | -   | dB   |
|                                |                                       | f = 2.4 GHz  | -   | 0.70 | -   | dB   |
|                                |                                       | f = 5.8 GHz  | -   | 1.20 | -   | dB   |
| G <sub>ass</sub>               | associated gain                       | $I_C$ = 6 mA; $V_{CE}$ = 2 V; $\Gamma_S$ = $\Gamma_{opt}$ ; $\Gamma_{amb}$ = 25 °C               |     |      |     |      |
|                                |                                       | f = 1.5 GHz  | -   | 21   | -   | dB   |
|                                |                                       | f = 1.8 GHz  | -   | 20   | -   | dB   |
|                                |                                       | f = 2.4 GHz  | -   | 17.5 | -   | dB   |
|                                |                                       | f = 5.8 GHz  | -   | 12   | -   | dB   |
| P <sub>L(1dB)</sub>            | output power at 1 dB gain compression | $I_C = 60 \text{ mA}; V_{CE} = 4 \text{ V};$<br>$Z_S = Z_L = 50 \Omega; T_{amb} = 25 \text{ °C}$ |     |      |     |      |
|                                |                                       | f = 1.5 GHz  | -   | 17   | -   | dBm  |
|                                |                                       | f = 1.8 GHz  | -   | 17   | -   | dBm  |
|                                |                                       | f = 2.4 GHz  | -   | 16   | -   | dBm  |
|                                |                                       | f = 5.8 GHz  | -   | 18.5 | -   | dBm  |

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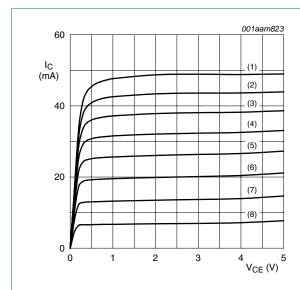
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 Table 7.
 Characteristics ...continued

 $T_i = 25$  °C unless otherwise specified

| Symbol                                    | Parameter                          | Conditions  | Min | Тур | Max | Unit |
|---|------------------------------------|---|-----|-----|-----|------|
| IP3 <sub>O</sub> output third-order inter | output third-order intercept point | $I_{C}$ = 40 mA; $V_{CE}$ = 4 V;<br>$Z_{S}$ = $Z_{L}$ = 50 $\Omega$ ; $T_{amb}$ = 25 °C |     |     |     |      |
|   |                                    | f = 1.5 GHz   | -   | 27  | -   | dBm  |
|   |                                    | f = 1.8 GHz   | -   | 27  | -   | dBm  |
|   |                                    | f = 2.4 GHz   | -   | 27  | -   | dBm  |
|   |                                    | f = 5.8 GHz   | -   | 28  | -   | dBm  |

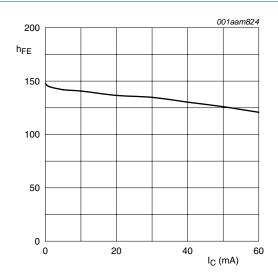
[1]  $G_{p(max)}$  is the maximum power gain, if K > 1. If K < 1 then  $G_{p(max)} = MSG$ .



 $T_{amb} = 25 \, ^{\circ}C.$ 

- (1)  $I_B = 400 \mu A$
- (2)  $I_B = 350 \mu A$
- (3)  $I_B = 300 \mu A$
- (4)  $I_B = 250 \mu A$
- (5)  $I_B = 200 \mu A$
- (6)  $I_B = 150 \mu A$
- (7)  $I_B = 100 \mu A$
- (8)  $I_B = 50 \mu A$

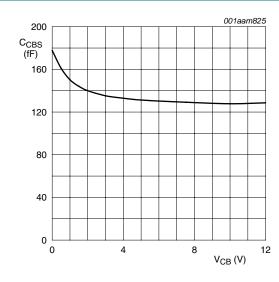
Fig 2. Collector current as a function of collector-emitter voltage; typical values



 $V_{CE}$  = 2 V;  $T_{amb}$  = 25 °C.

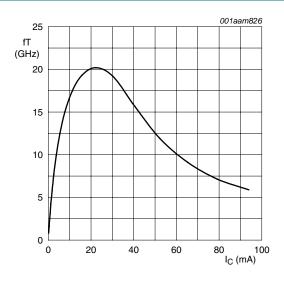
Fig 3. DC current gain as a function of collector current; typical values

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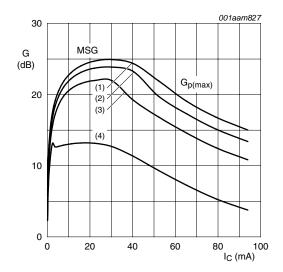
f = 1 MHz,  $T_{amb} = 25$  °C.

Fig 4. Collector-base capacitance as a function of collector-base voltage; typical values



 $V_{CE}$  = 1 V; f = 2 GHz;  $T_{amb}$  = 25 °C.

Fig 5. Transition frequency as a function of collector current; typical values



 $V_{CE} = 1 \text{ V}; T_{amb} = 25 \text{ }^{\circ}\text{C}.$ 

(1) f = 1.5 GHz

(2) f = 1.8 GHz

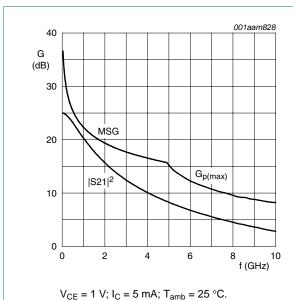
(3) f = 2.4 GHz

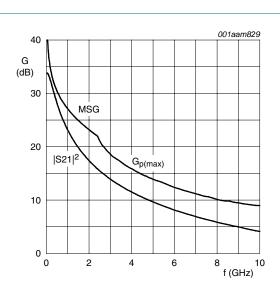
(4) f = 5.8 GHz

(5) f = 12 GHz

Fig 6. Gain as a function of collector current; typical value

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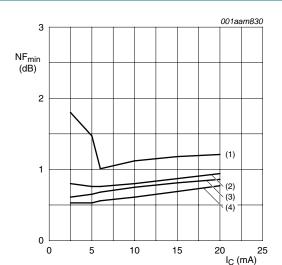


 $V_{CE}$  = 1 V;  $I_{C}$  = 30 mA;  $T_{amb}$  = 25 °C.

Fig 8.

Gain as a function of frequency; typical values

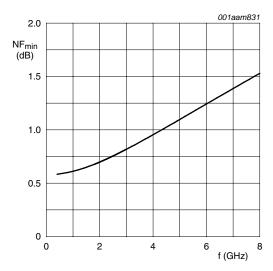
Gain as a function of frequency; typical values Fig 7.



 $V_{CE} = 2 \text{ V}$ ;  $T_{amb} = 25 \, ^{\circ}\text{C}$ .

- (1) f = 5.8 GHz
- (2) f = 2.4 GHz
- (3) f = 1.8 GHz
- (4) f = 1.5 GHz

Minimum noise figure as a function of Fig 9. collector current; typical values



$$V_{CE}$$
 = 2 V;  $I_{C}$  = 6 mA;  $T_{amb}$  = 25 °C.

Fig 10. Minimum noise figure as a function of frequency; typical values

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## 8. Package outline

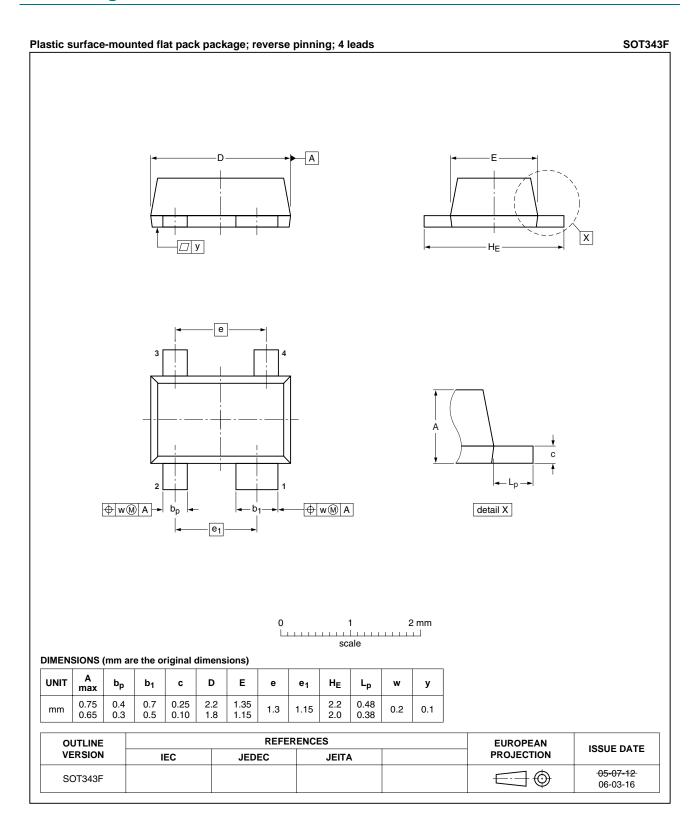


Fig 11. Package outline SOT343F

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### 9. Abbreviations

Table 8. Abbreviations

| Acronym | Description                                |
|---------|--|
| DC      | Direct Current                             |
| LNA     | Low Noise Amplifier                        |
| LTE     | Long Term Evolution                        |
| NPN     | Negative-Positive-Negative                 |
| RF      | Radio Frequency                            |
| SDARS   | Satellite Digital Audio Radio Service      |
| UMTS    | Universal Mobile Telecommunications System |

# 10. Revision history

Table 9. Revision history

| Document ID | Release date | Data sheet status  | Change notice | Supersedes |
|-------------|--------------|--------------------|---------------|------------|
| BFU660F v.1 | 20110111     | Product data sheet | -             | -          |

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#### 11.1 Data sheet status

| Document status[1][2]          | Product status[3] | Definition  |
|--------------------------------|-------------------|---|
| Objective [short] data sheet   | Development       | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification     | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production        | This document contains the product specification.                                     |

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- [2] The term 'short data sheet' is explained in section "Definitions"
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# **BFU660F**

#### NPN wideband silicon RF transistor

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