DISCRETE SEMICONDUCTORS

DATA SHEET

BFT25NPN 2 GHz wideband transistor

Product specification



NPN 2 GHz wideband transistor

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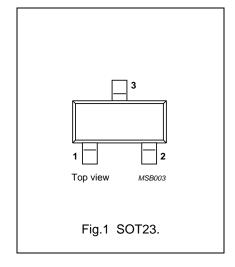
DESCRIPTION

NPN transistor in a plastic SOT23 envelope.

It is primarily intended for use in RF low power amplifiers, such as in pocket phones, paging systems, etc. The transistor features low current consumption (100 μA to 1 mA); due to its high transition frequency, it also has excellent wideband properties and low noise up to high frequencies.

PINNING

PIN	PIN DESCRIPTION		
Code: V1p			
1	base		
2	emitter		
3	collector		



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	8	V
V _{CEO}	collector-emitter voltage	open base	_	5	V
I _c	DC collector current		_	6.5	mA
P _{tot}	total power dissipation	up to $T_s = 167$ °C; note 1	_	30	mW
f _T	transition frequency	$I_C = 1 \text{ mA}; V_{CE} = 1 \text{ V}; f = 500 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	2.3	_	GHz
C _{re}	feedback capacitance	$I_C = 1 \text{ mA}; V_{CE} = 1 \text{ V}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$	_	0.45	pF
G _{UM}	maximum unilateral power gain	$I_C = 1 \text{ mA}; V_{CE} = 1 \text{ V}; f = 500 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	18	_	dB
F	noise figure	$I_C = 1 \text{ mA}; V_{CE} = 1 \text{ V}; f = 500 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	3.8	_	dB

LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	_	8	V
V_{CEO}	collector-emitter voltage	open base	-	5	V
V_{EBO}	emitter-base voltage	open collector	-	2	V
I _C	DC collector current		_	6.5	mA
I _{CM}	peak collector current	f > 1 MHz	-	10	mA
P _{tot}	total power dissipation	up to T _s = 167 °C; note 1	-	30	mW
T _{stg}	storage temperature		-65	150	°C
Tj	junction temperature		_	175	°C

Note

1. T_s is the temperature at the soldering point of the collector tab.

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THERMAL RESISTANCE

SYMBOL	PARAMETER	CONDITIONS	THERMAL RESISTANCE
R _{th j-s}	thermal resistance from junction to soldering point	up to $T_s = 167^{\circ}C$; note 1	260 K/W

Note

1. T_s = is the temperature at the soldering point of the collector tab.

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 5 V	_	_	50	nA
h _{FE}	DC current gain	$I_C = 10 \mu A; V_{CE} = 1 V$	20	30	-	
		I _C = 1 mA; V _{CE} = 1 V	20	40	-	
f _T	transition frequency	I _C = 1 mA; V _{CE} = 1 V; f = 500 MHz	1.2	2.3	-	GHz
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = 0.5 \text{ V}$; $f = 1 \text{ MHz}$	_	_	0.6	pF
C _e	emitter capacitance	$I_c = I_c = 0$; $V_{EB} = 0$; $f = 1 \text{ MHz}$	_	_	0.5	pF
C _{re}	feedback capacitance	$I_C = 1 \text{ mA}; V_{CE} = 1 \text{ V}; f = 1 \text{ MHz};$ $T_{amb} = 25 ^{\circ}C$	_	_	0.45	pF
G _{UM}	maximum unilateral power gain (note 1)	$I_C = 1 \text{ mA}; V_{CE} = 1 \text{ V}; f = 500 \text{ MHz};$ $T_{amb} = 25 ^{\circ}\text{C}$	_	18	_	dB
		$I_C = 1 \text{ mA}; V_{CE} = 1 \text{ V}; f = 800 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	-	12	_	dB
F	noise figure	$I_C = 0.1 \text{ mA}; V_{CE} = 1 \text{ V};$ f = 500 MHz; $T_{amb} = 25 \text{ °C}$	_	5.5	_	dB
		$I_C = 1 \text{ mA}; V_{CE} = 1 \text{ V}; f = 500 \text{ MHz};$ $T_{amb} = 25 ^{\circ}\text{C}$	_	3.8	_	dB

Note

1.
$$G_{UM}$$
 is the maximum unilateral power gain, assuming S_{12} is zero and
$$G_{UM} = 10 log \frac{\left|S_{21}\right|^2}{(1-\left|S_{11}\right|^2)(1-\left|S_{22}\right|^2)} dB.$$

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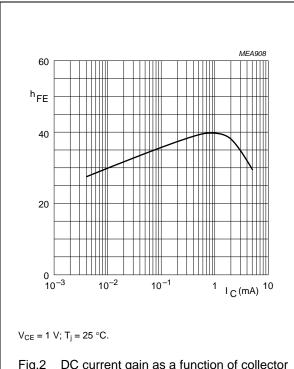


Fig.2 DC current gain as a function of collector current.

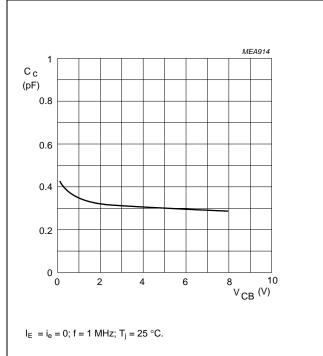
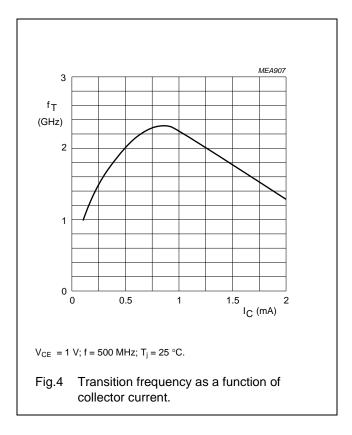
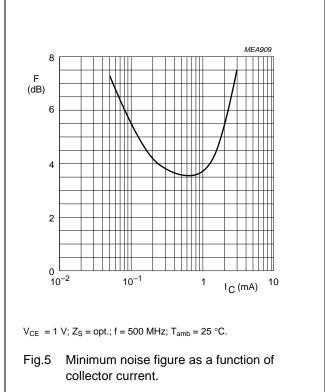


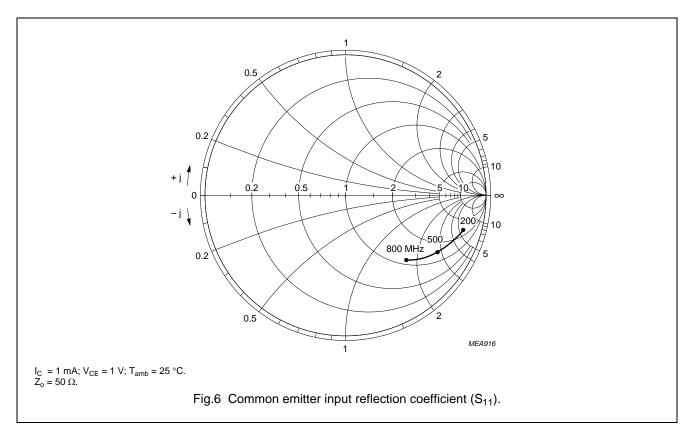
Fig.3 Collector capacitance as a function of collector-base voltage.

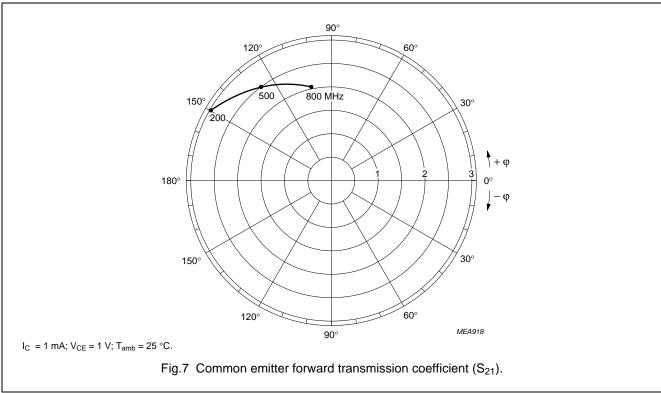




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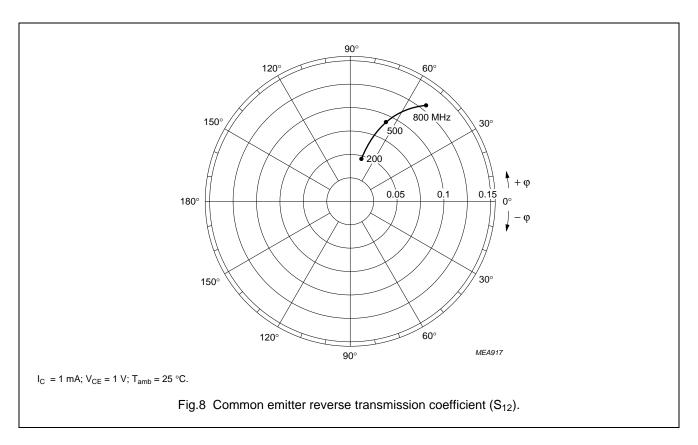


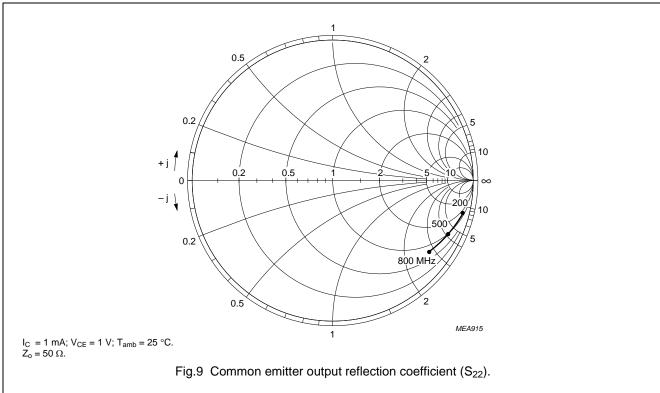


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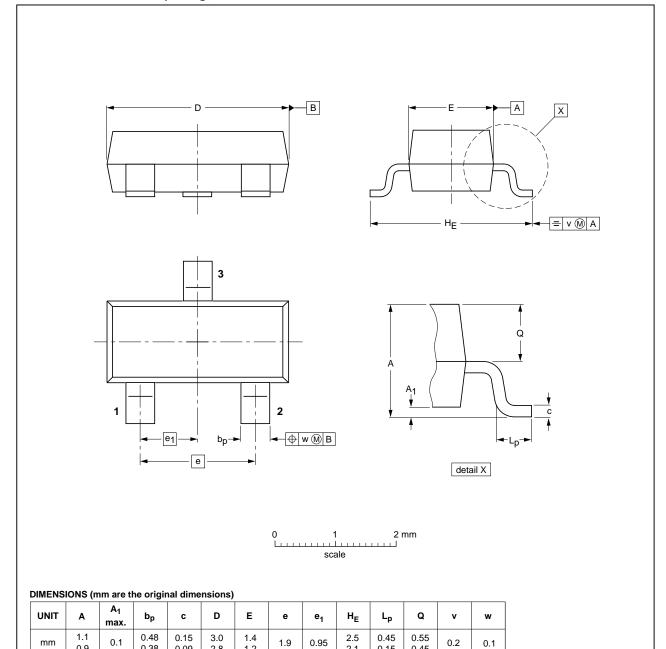
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PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	LINE REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-04-11-04 06-03-16

0.9

0.38

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DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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