74AUP1G17

Low-power Schmitt trigger

Rev. 13 — 13 January 2022

Product data sheet

1. General description

The 74AUP1G17 is a single buffer with Schmitt-trigger input. This device ensures very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V. This device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- CMOS low power dissipation
- · High noise immunity
- Overvoltage tolerant inputs to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level B
- Low static power consumption; I_{CC} = 0.9 μA (maximum)
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - JESD8-5 (1.8 V to 2.7 V)
 - JESD8C (2.7 V to 3.6 V)
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 Class 3A exceeds 5000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 Class C3 exceeds 1000 V
 - MM: JESD22-A115-A exceeds 200 V
- · Multiple package options
- Specified from -40 ° C to +85 ° C and -40 ° C to +125 ° C



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3. Ordering information

Table 1. Ordering information

Type number	Package								
	Temperature range	Name	Description	Version					
74AUP1G17GW	-40 ° C to +125 ° C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1					
74AUP1G17GV	-40 ° C to +125 ° C	SC-74A	plastic surface-mounted package; 5 leads	SOT753					
74AUP1G17GM	-40 ° C to +125 ° C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886					
74AUP1G17GN	-40 ° C to +125 ° C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	SOT1115					
74AUP1G17GS	-40 ° C to +125 ° C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202					
74AUP1G17GX	-40 ° C to +125 ° C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 × 0.8 × 0.32 mm	SOT1226-3					
74AUP1G17GX4	-40 °C to +125 °C	X2SON4	plastic thermal enhanced extremely thin small outline package; no leads; 4 terminals; body 0.6 × 0.6 × 0.32 mm	SOT1269-2					

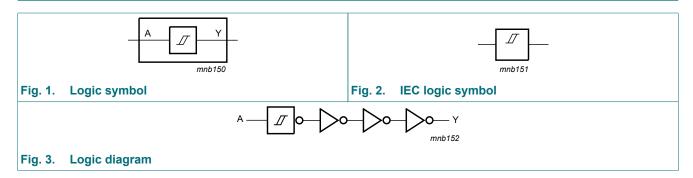
4. Marking

Table 2. Marking

Type number	Marking code[1]
74AUP1G17GW	рЈ
74AUP1G17GV	рЈ
74AUP1G17GM	pJ
74AUP1G17GN	рЈ
74AUP1G17GS	рЈ
74AUP1G17GX	рЈ
74AUP1G17GX4	рЈ

^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

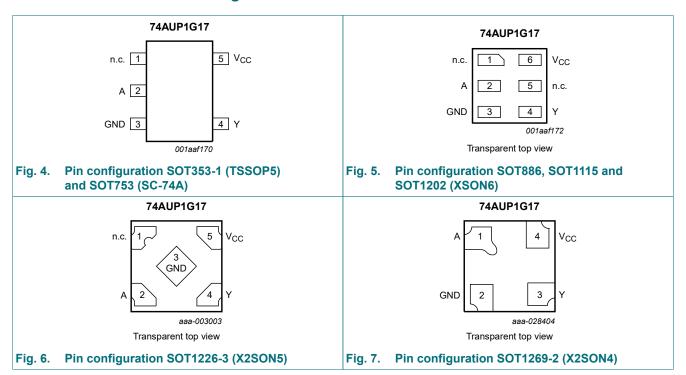
5. Functional diagram



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6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

Symbol	Pin	Pin						
	SC-74A, TSSOP5 and X2SON5	SC-74A, TSSOP5 and X2SON5 XSON6 X2SON						
n.c.	1	1, 5	-	not connected				
Α	2	2	1	data input				
GND	3	3	2	ground (0 V)				
Υ	4	4	3	data output				
V _{CC}	5	6	4	supply voltage				

7. Functional description

Table 4. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$

Input	Output
A	Υ
L	L
Н	Н

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
Io	output current	V _O = 0 V to V _{CC}	-	±20	mA
I _{CC}	supply current		-	+50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C			
		TSSOP5, SC-74A, XSON6 and [2] X2SON5 package	-	250	mW
		X2SON4 package [3]	-	150	mW

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

For SOT753 (SC-74A) package: P_{tot} derates linearly with 3.8 mW/K above 85 °C.

For SOT886 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package: P_{tot} derates linearly with 3.2 mW/K above 71 $^{\circ}\text{C}.$

For SOT1202 (XSON6) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

For SOT1226-3 (X2SON5) package: P_{tot} derates linearly with 3.0 mW/K above 67 °C.

For SOT1269-2 (X2SON4) package: P_{tot} derates linearly with 1.7 mW/K above 57 °C.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
V _O	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V _{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C

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^[2] For SOT353-1 (TSSOP5) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

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10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 25	5 °C		•	•		
V _{OH}	HIGH-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I_{O} = -2.3 mA; V_{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I_{O} = -2.7 mA; V_{CC} = 3.0 V	2.72	-	-	V
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I_{O} = 2.3 mA; V_{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		$I_{O} = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.44	V
l _l	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.1	μΑ
I _{OFF}	power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V	-	-	±0.2	μΑ
ΔI_{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.2	μΑ
I _{CC}	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	0.5	μΑ
ΔI _{CC}	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	40	μΑ
Cı	input capacitance	V_I = GND or V_{CC} ; V_{CC} = 0 V to 3.6 V	-	1.1	-	pF
Co	output capacitance	V _O = GND; V _{CC} = 0 V	-	1.7	-	pF

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -4	10 °C to +85 °C	'	-			'
V _{OH}	HIGH-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I_{O} = -2.3 mA; V_{CC} = 2.3 V	1.97	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.67	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.55	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.5	μΑ
I _{OFF}	power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V	-	-	±0.5	μΑ
Δl _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.6	μΑ
I _{CC}	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.9	μΑ
ΔI _{CC}	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	50	μΑ

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -4	10 °C to +125 °C		-1	1		
V _{OH}	HIGH-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I_{O} = -2.7 mA; V_{CC} = 3.0 V	2.40	-	-	V
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.30	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{T+}$ or V_{T-}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I_{O} = 2.3 mA; V_{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		$I_{O} = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.50	V
l _l	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.75	μΑ
I _{OFF}	power-off leakage current	V_{I} or $V_{O} = 0 \text{ V}$ to 3.6 V; $V_{CC} = 0 \text{ V}$	-	-	±0.75	μA
Δl _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.75	μA
I _{CC}	supply current	V_{I} = GND or V_{CC} ; I_{O} = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	1.4	μA
ΔI _{CC}	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	75	μΑ

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11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 9

Symbol	Parameter	Conditions		25 °C		-40 °C to	o +85 °C	-40 °C to	+125 °C	Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
C _L = 5 p	F							,		
t _{pd}	propagation	A to Y; see <u>Fig. 8</u> [2]								
	delay	V _{CC} = 0.8 V	-	19.0	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.6	5.7	10.6	2.5	10.9	2.5	11.1	ns
		V _{CC} = 1.4 V to 1.6 V	2.4	4.2	6.5	2.3	7.1	2.3	7.4	ns
		V _{CC} = 1.65 V to 1.95 V	2.0	3.6	5.5	1.9	6.1	1.9	6.3	ns
		V _{CC} = 2.3 V to 2.7 V	1.9	3.0	4.2	1.8	4.6	1.8	4.8	ns
		V _{CC} = 3.0 V to 3.6 V	1.8	2.7	3.6	1.5	3.8	1.5	4.0	ns
C _L = 10	pF									
t _{pd}	propagation	A to Y; see Fig. 8 [2]								
	delay	V _{CC} = 0.8 V	-	22.5	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.9	6.6	12.4	2.7	12.9	2.7	13.0	ns
		V _{CC} = 1.4 V to 1.6 V	2.6	4.8	7.8	2.4	8.3	2.4	8.7	ns
		V _{CC} = 1.65 V to 1.95 V	2.5	4.2	6.3	2.4	6.8	2.4	7.1	ns
		V _{CC} = 2.3 V to 2.7 V	2.3	3.5	4.8	2.1	5.3	2.1	5.6	ns
		V _{CC} = 3.0 V to 3.6 V	2.1	3.3	4.4	2.0	4.6	2.0	4.8	ns
C _L = 15	pF									
t_{pd}	propagation	A to Y; see Fig. 8 [2]								
	delay	V _{CC} = 0.8 V	-	26.0	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.2	7.4	14.1	3.1	14.7	3.1	14.9	ns
		V _{CC} = 1.4 V to 1.6 V	3.1	5.4	8.7	2.8	9.5	2.8	9.9	ns
		V _{CC} = 1.65 V to 1.95 V	2.7	4.7	7.1	2.7	7.8	2.7	8.2	ns
		V _{CC} = 2.3 V to 2.7 V	2.6	4.0	5.6	2.5	6.0	2.5	6.3	ns
		V _{CC} = 3.0 V to 3.6 V	2.5	3.7	4.9	2.2	5.2	2.2	5.5	ns
$C_{L} = 30$	pF									
t_{pd}	propagation	A to Y; see <u>Fig. 8</u> [2]								
	delay	V _{CC} = 0.8 V	-	36.3	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.9	9.7	19.0	3.7	19.8	3.7	20.1	ns
		V _{CC} = 1.4 V to 1.6 V	3.5	7.0	11.2	3.6	12.4	3.6	13.0	ns
		V _{CC} = 1.65 V to 1.95 V	3.5	6.0	9.2	3.4	10.1	3.4	10.7	ns
		V _{CC} = 2.3 V to 2.7 V	3.4	5.1	7.0	3.2	7.5	3.2	7.9	ns
		V _{CC} = 3.0 V to 3.6 V	3.3	4.8	6.2	3.1	7.1	3.1	7.5	ns

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Symbol	mbol Parameter Conditions 25 °C			-40 °C to	o +85 °C	-40 °C to	+125 °C	Unit			
			Min	Typ[1]	Max	Min	Max	Min	Max		
$C_L = 5 p$	F, 10 pF, 15 pF	and 30 pF									
C _{PD}	power	$f = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ [3]									
	dissipation capacitance	V _{CC} = 0.8 V	-	2.5	-	-	-	-	-	pF	
	Capacitarioc	V _{CC} = 1.1 V to 1.3 V	-	2.7	-	-	-	-	-	pF	
		V _{CC} = 1.4 V to 1.6 V	-	2.8	-	-	-	-	-	pF	
			V _{CC} = 1.65 V to 1.95 V	-	3.0	-	-	-	-	-	pF
		V _{CC} = 2.3 V to 2.7 V	-	3.5	-	-	-	-	-	pF	
		V _{CC} = 3.0 V to 3.6 V	-	4.0	-	-	-	-	-	pF	

- [1] All typical values are measured at nominal V_{CC}.
- 2] t_{pd} is the same as t_{PLH} and t_{PHL}
- [3] \dot{C}_{PD} is used to determine the dynamic power dissipation (P_D in μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

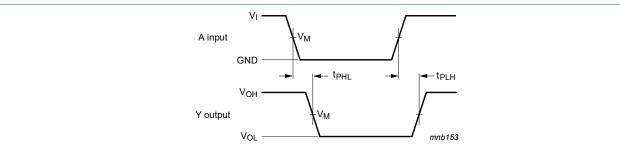
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\sum (C_L \times V_{CC})^2 \times f_o$ = sum of the outputs.

11.1. Waveform and test circuit



Measurement points are given in <u>Table 9</u>.

Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

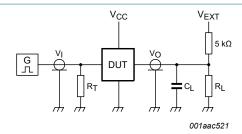
Fig. 8. The data input (A) to output (Y) propagation delays

Table 9. Measurement points

Supply voltage	Output	Input					
V _{CC}	V _M	$V_{\rm M}$ $V_{\rm I}$ $t_{\rm r} = t_{\rm f}$					
0.8 V to 3.6 V	0.5 x V _{CC}	0.5 x V _{CC}	V _{CC}	≤ 3.0 ns			

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Test data is given in Table 10.

Definitions for test circuit:

 R_L = Load resistance.

 C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator.

 V_{EXT} = External voltage for measuring switching times.

Fig. 9. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load	V _{EXT}			
V _{CC}	CL	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}	
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 x V _{CC}

[1] For measuring enable and disable times, R_L = 5 k Ω . For measuring propagation delays, setup and hold times and pulse width R_L = 1 M Ω .

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12. Transfer characteristics

Table 11. Transfer characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	5 °C					
V _{T+}	positive-going threshold voltage	see Fig. 10 and Fig. 11				
		V _{CC} = 0.8 V	0.30	-	0.60	V
		V _{CC} = 1.1 V	0.53	-	0.90	V
		V _{CC} = 1.4 V	0.74	-	1.11	V
		V _{CC} = 1.65 V	0.91	-	1.29	V
		V _{CC} = 2.3 V	1.37	-	1.77	V
		V _{CC} = 3.0 V	1.88	-	2.29	V
V _{T-}	negative-going threshold voltage	see <u>Fig. 10</u> and <u>Fig. 11</u>				
		V _{CC} = 0.8 V	0.10	-	0.60	V
		V _{CC} = 1.1 V	0.26	-	0.65	V
		V _{CC} = 1.4 V	0.39	-	0.75	V
		V _{CC} = 1.65 V	0.47	-	0.84	V
		V _{CC} = 2.3 V	0.69	-	1.04	V
		V _{CC} = 3.0 V	0.88	-	1.24	V
V _H	hysteresis voltage	see Fig. 10, Fig. 11, Fig. 12 and Fig. 13				
		V _{CC} = 0.8 V	0.07	-	0.50	V
		V _{CC} = 1.1 V	0.08	-	0.46	V
		V _{CC} = 1.4 V	0.18	-	0.56	V
		V _{CC} = 1.65 V	0.27	-	0.66	V
		V _{CC} = 2.3 V	0.53	-	0.92	V
		V _{CC} = 3.0 V	0.79	-	1.31	V

Product data sheet

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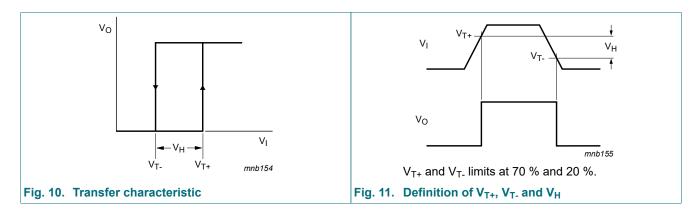
Low-power Schmitt trigger

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	40 °C to +85 °C					
V _{T+}	positive-going threshold voltage	see Fig. 10 and Fig. 11				
		V _{CC} = 0.8 V	0.30	-	0.60	V
		V _{CC} = 1.1 V	0.53	-	0.90	V
		V _{CC} = 1.4 V	0.74	-	1.11	V
		V _{CC} = 1.65 V	0.91	-	1.29	V
		V _{CC} = 2.3 V	1.37	-	1.77	V
		V _{CC} = 3.0 V	1.88	-	2.29	V
V _{T-}	negative-going threshold voltage	see <u>Fig. 10</u> and <u>Fig. 11</u>				
		V _{CC} = 0.8 V	0.10	-	0.60	V
		V _{CC} = 1.1 V	0.26	-	0.65	V
		V _{CC} = 1.4 V	0.39	-	0.75	V
		V _{CC} = 1.65 V	0.47	-	0.84	V
		V _{CC} = 2.3 V	0.69	-	1.04	V
		V _{CC} = 3.0 V	0.88	-	1.24	V
V _H	hysteresis voltage	see Fig. 10, Fig. 11, Fig. 12 and Fig. 13				
		V _{CC} = 0.8 V	0.07	-	0.50	V
		V _{CC} = 1.1 V	0.08	-	0.46	V
		V _{CC} = 1.4 V	0.18	-	0.56	V
		V _{CC} = 1.65 V	0.27	-	0.66	V
		V _{CC} = 2.3 V	0.53	-	0.92	V
		V _{CC} = 3.0 V	0.79	-	1.31	V

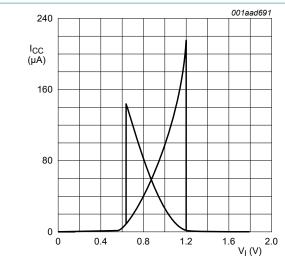
Low-power Schmitt trigger

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -4	10 °C to +125 °C					
V _{T+}	positive-going threshold voltage	see <u>Fig. 10</u> and <u>Fig. 11</u>				
		V _{CC} = 0.8 V	0.30	-	0.62	V
		V _{CC} = 1.1 V	0.53	-	0.92	V
		V _{CC} = 1.4 V	0.74	-	1.13	V
		V _{CC} = 1.65 V	0.91	-	1.31	V
		V _{CC} = 2.3 V	1.37	-	1.80	V
		V _{CC} = 3.0 V	1.88	-	2.32	٧
V _{T-}	negative-going threshold voltage	see <u>Fig. 10</u> and <u>Fig. 11</u>				
		V _{CC} = 0.8 V	0.10	-	0.60	V
		V _{CC} = 1.1 V	0.26	-	0.65	V
		V _{CC} = 1.4 V	0.39	-	0.75	V
		V _{CC} = 1.65 V	0.47	-	0.84	V
		V _{CC} = 2.3 V	0.69	-	1.04	V
		V _{CC} = 3.0 V	0.88	-	1.24	V
V _H	hysteresis voltage	see Fig. 10, Fig. 11, Fig. 12 and Fig. 13				
		V _{CC} = 0.8 V	0.07	-	0.50	V
		V _{CC} = 1.1 V	0.08	-	0.46	V
		V _{CC} = 1.4 V	0.18	-	0.56	V
		V _{CC} = 1.65 V	0.27	-	0.66	V
		V _{CC} = 2.3 V	0.53	-	0.92	V
		V _{CC} = 3.0 V	0.79	-	1.31	V

12.1. Waveforms transfer characteristics



Low-power Schmitt trigger



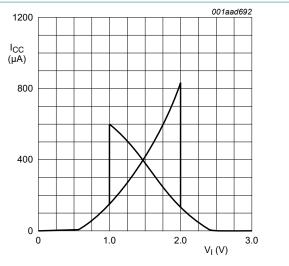


Fig. 12. Typical transfer characteristics; $V_{CC} = 1.8 \text{ V}$

Fig. 13. Typical transfer characteristics; $V_{CC} = 3.0 \text{ V}$

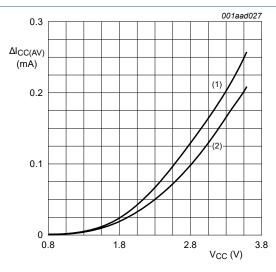
13. Application information

The slow input rise and fall times cause additional power dissipation, this can be calculated using the following formula:

 $P_{ad} = f_i x (t_r x I_{CC(AV)} + t_f x I_{CC(AV)}) x V_{CC}$ where:

- P_{ad} = additional power dissipation (μW);
- f_i = input frequency (MHz);
- t_r = input rise time (ns); 10 % to 90 %;
- t_f = input fall time (ns); 90 % to 10 %;
- I_{CC(AV)} = average additional supply current (μA).

Average I_{CC} differs with positive or negative input transitions, as shown in Fig. 14.



- (1) Positive-going edge.
- (2) Negative-going edge.

Linear change of V_I between 0.8 V and 2.0 V. All values given are typical, unless otherwise specified.

Fig. 14. Average I_{CC} as a function of V_{CC}

Low-power Schmitt trigger

14. Package outline

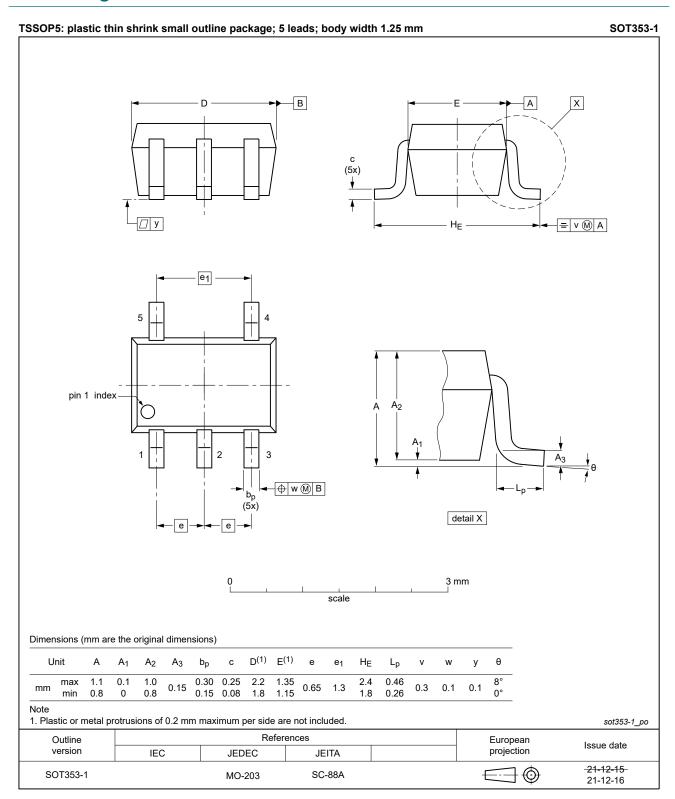


Fig. 15. Package outline SOT353-1 (TSSOP5)

Low-power Schmitt trigger

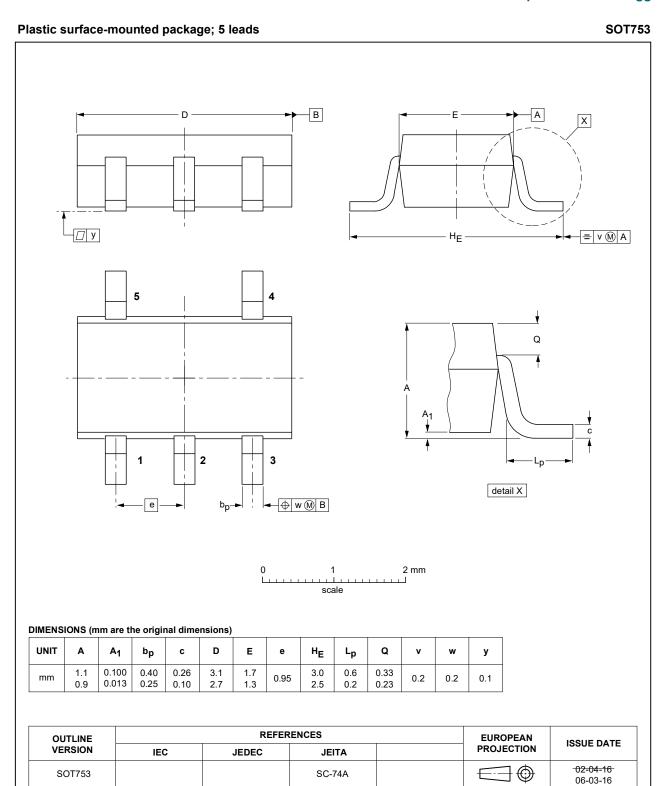


Fig. 16. Package outline SOT753 (SC-74A)

Low-power Schmitt trigger

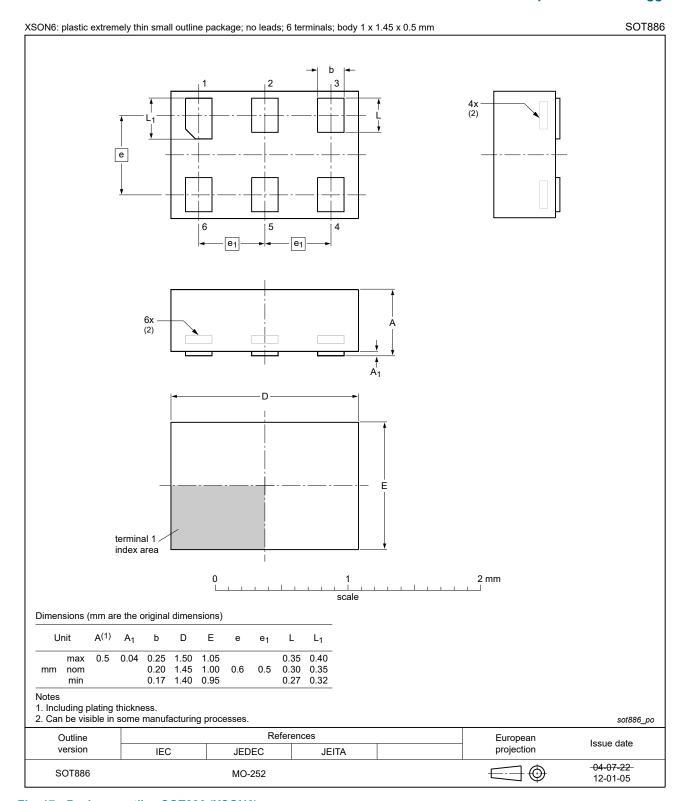


Fig. 17. Package outline SOT886 (XSON6)

Low-power Schmitt trigger

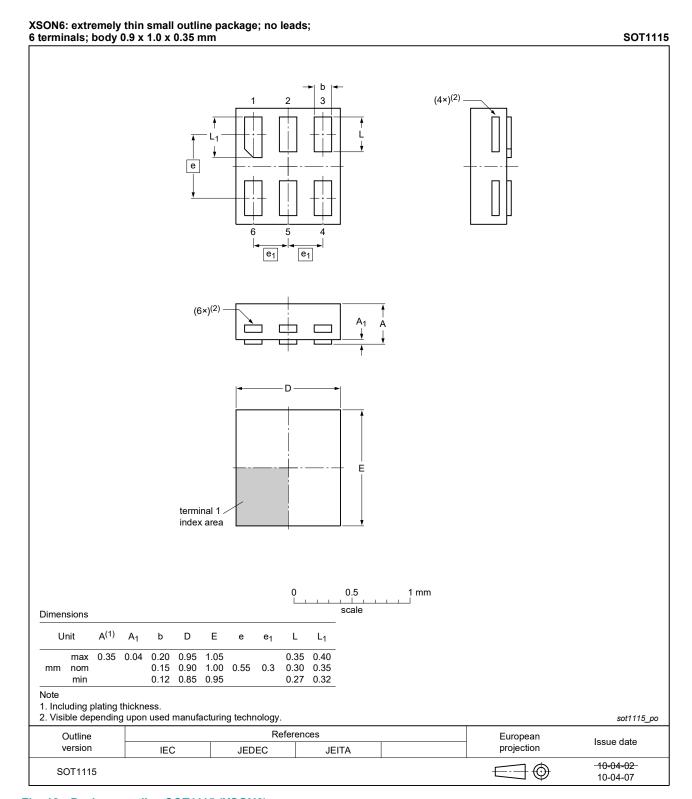


Fig. 18. Package outline SOT1115 (XSON6)

Low-power Schmitt trigger

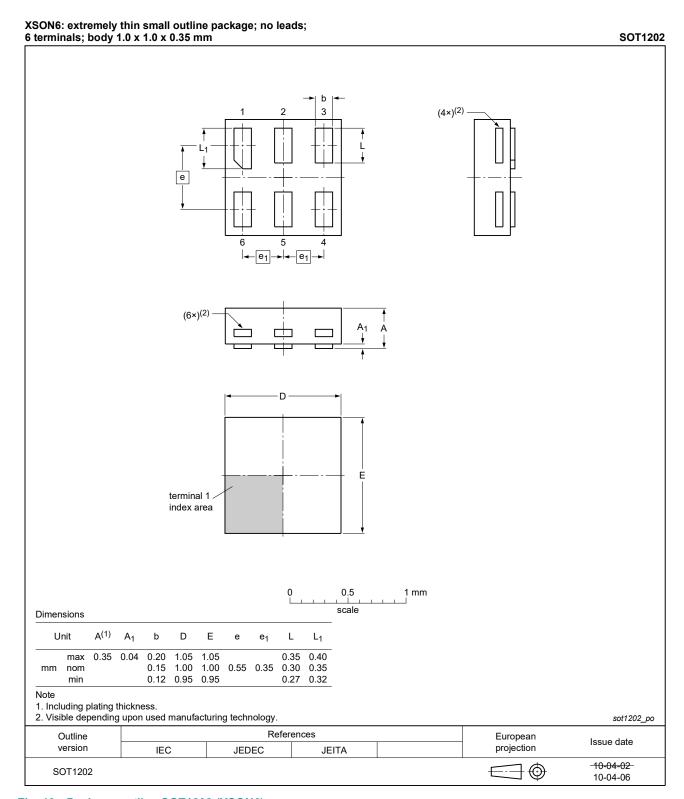


Fig. 19. Package outline SOT1202 (XSON6)

Low-power Schmitt trigger

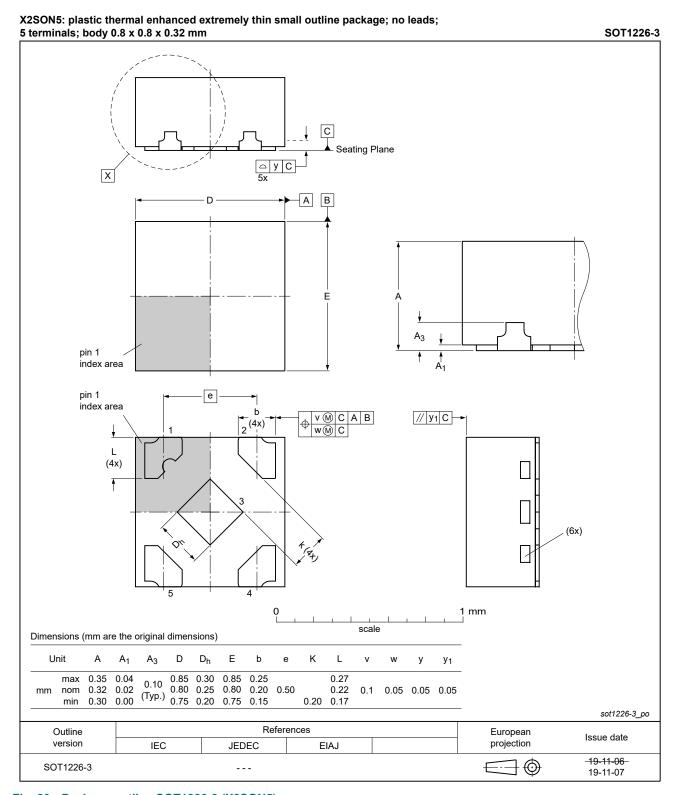


Fig. 20. Package outline SOT1226-3 (X2SON5)

Low-power Schmitt trigger

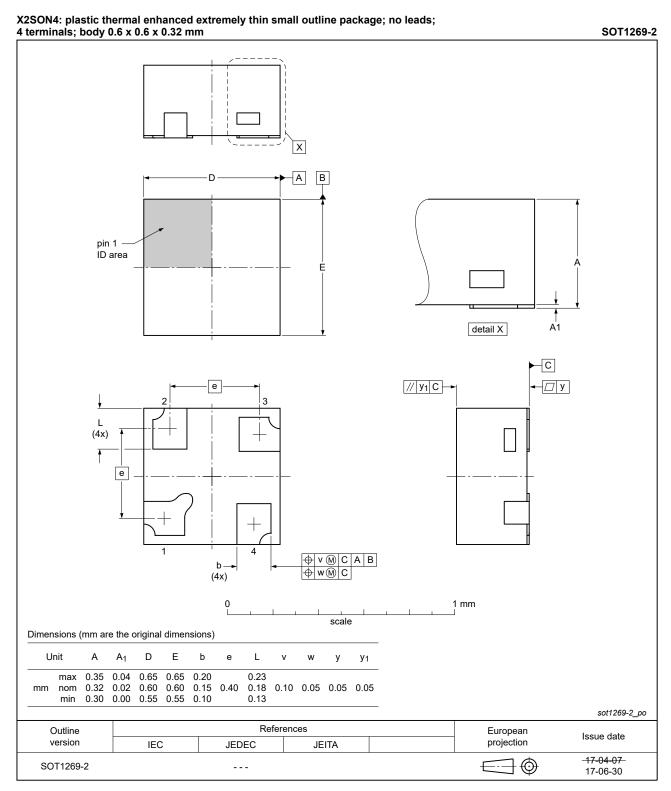


Fig. 21. Package outline SOT1269-2 (X2SON4)

Low-power Schmitt trigger

15. Abbreviations

Table 12. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

16. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
74AUP1G17 v.13	20220113	Product data sheet	-	74AUP1G17 v.12		
Modifications:	Fig. 15: Package outline drawing for SOT353-1 (TSSOP5) has changed.					
74AUP1G17 v.12	20210707	Product data sheet	-	74AUP1G17 v.11		
Modifications:	Type number 74.	0N5) package changed to SOT122 AUP1G17GF (SOT891/XSON6) r g values for P _{tot} total power dissipa	emoved.			
74AUP1G17 v.11	20180608	Product data sheet	-	74AUP1G17 v.10		
Modifications:	Added type num	ber 74AUP1G17GX4 (SOT1269-2	2)			
74AUP1G17 v.10	20170519	Product data sheet	-	74AUP1G17 v.9		
Modifications:	Nexperia.	The format of the data chock has been reaccigned to comply with the identity galacimos of				
74AUP1G17 v.9	20161104	Product data sheet	-	74AUP1G17 v.8		
Modifications:	Added type num	ber 74AUP1G17GV (SOT753)				
74AUP1G17 v.8	20150115	Product data sheet	-	74AUP1G17 v.7		
Modifications:	Marking code <u>Ta</u>	ble 2: typo corrected in type numb	per 74AUP1G17GX			
74AUP1G17 v.7	20120716	Product data sheet	-	74AUP1G17 v.6		
Modifications:	Package outline	drawing of SOT1226 (Fig. 20) mo	dified.			
74AUP1G17 v.6	20120412	Product data sheet	-	74AUP1G17 v.5		
Modifications:	 Added type number 74AUP1G17GX (SOT1226) Package outline drawing of SOT886 (Fig. 17) modified. 					
74AUP1G17 v.5	20111124	Product data sheet	-	74AUP1G17 v.4		
Modifications:	Legal pages updated.					
74AUP1G17 v.4	20100715	Product data sheet	-	74AUP1G17 v.3		
74AUP1G17 v.3	20090710	Product data sheet	-	74AUP1G17 v.2		
74AUP1G17 v.2	20060727	Product data sheet	-	74AUP1G17 v.1		
74AUP1G17 v.1	20050726	Product data sheet	-	-		

Low-power Schmitt trigger

17. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Nexperia

Low-power Schmitt trigger

74AUP1G17

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